DENON



Please refer to the MODIFICATION NOTICE.

| SERVICE MANUAL | | | | | | | | |
|---|---------------|----------------|--------------|--------|---------|--------|-----|-----|
| MODEL | JP | E3 | E2 | EK | E2A | E1C | E1K | EUT |
| DCD-710AE | | | \checkmark | | | | | |
| | CD PL/ | YER | | | · | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| • For purposes of improvement, specifications | and desig | n are su | hiect to | change | without | notice | | |
| Please use this service manual with referring | | | | _ | | | | |
| Some illustrations using in this service man | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | | | | | | |
| | | | l | | | | | |
| | DEN | | l | | | | | |
| | D&M Holdir | ngs Inc. | | | | | | |
| 009-0V02DM/DG0911 Copyright 200 | 9 D&M Holding | is Inc. All ri | ghts reserv | ed. | | | | |

SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Before returning the unit to the customer, make sure you make either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 milliamps, or if the resistance from chassis to either side of the power cord is less than 460 kohms, the unit is defective.

LASER RADIATION

Do not stare into beam or view directly with optical instruments, class 3A laser product.

CAUTION Please heed the points listed below during servicing and inspection.

○ Heed the cautions!

Spots requiring particular attention when servicing, such as the cabinet, parts, chassis, etc., have cautions indicated on labels or seals. Be sure to heed these cautions and the cautions indicated in the handling instructions.

○ Caution concerning electric shock!

- (1) An AC voltage is impressed on this set, so touching internal metal parts when the set is energized could cause electric shock. Take care to avoid electric shock, by for example using an isolating transformer and gloves when servicing while the set is energized, unplugging the power cord when replacing parts, etc.
- (2)There are high voltage parts inside. Handle with extra care when the set is energized.

Caution concerning disassembly and assembly!

Though great care is taken when manufacturing parts from sheet metal, there may in some rare cases be burrs on the edges of parts which could cause injury if fingers are moved across them. Use gloves to protect your hands.

Only use designated parts!

The set's parts have specific safety properties (fire resistance, voltage resistance, etc.). For replacement parts, be sure to use parts which have the same properties. In particular, for the important safety parts that are marked \triangle on wiring diagrams and parts lists, be sure to use the designated parts.

Be sure to mount parts and arrange the wires as they were originally!

For safety reasons, some parts use tape, tubes or other insulating materials, and some parts are mounted away from the surface of printed circuit boards. Care is also taken with the positions of the wires inside and clamps are used to keep wires away from heating and high voltage parts, so be sure to set everything back as it was originally.

◎ Inspect for safety after servicing!

Check that all screws, parts and wires removed or disconnected for servicing have been put back in their original positions, inspect that no parts around the area that has been serviced have been negatively affected, conduct an insulation check on the external metal connectors and between the blades of the power plug, and otherwise check that safety is ensured.

(Insulation check procedure)

Unplug the power cord from the power outlet, disconnect the antenna, plugs, etc., and turn the power switch on. Using a 500V insulation resistance tester, check that the insulation resistance between the terminals of the power plug and the externally exposed metal parts (antenna terminal, headphones terminal, microphone terminal, input terminal, etc.) is $1M\Omega$ or greater. If it is less, the set must be inspected and repaired.

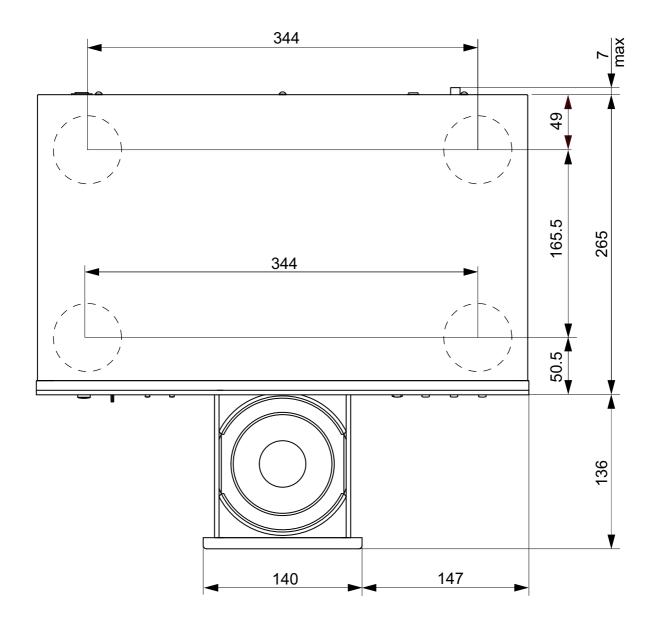
CAUTION Concerning important safety parts

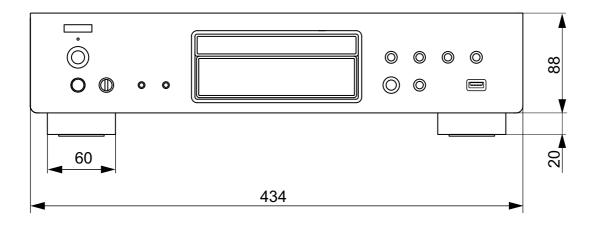
Many of the electric and structural parts used in the set have special safety properties. In most cases these properties are difficult to distinguish by sight, and using replacement parts with higher ratings (rated power and withstand voltage) does not necessarily guarantee that safety performance will be preserved. Parts with safety properties are indicated as shown below on the wiring diagrams and parts lists is this service manual. Be sure to replace them with parts with the designated part number.

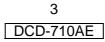
- (1) Schematic diagrams ... Indicated by the \triangle mark.
- (2) Parts lists ... Indicated by the A mark.

Using parts other than the designated parts could result in electric shock, fires or other dangerous situations.

DIMENSION







WIRE ARRANGEMENT

If wire bundles are untied or moved to perform adjustment or parts replacement etc., be sure to rearrange them neatly as they were originally bundled or placed afterward.

Otherwise, incorrect arrangement can be a cause of noise generation.

Wire arrangement viewed from the top

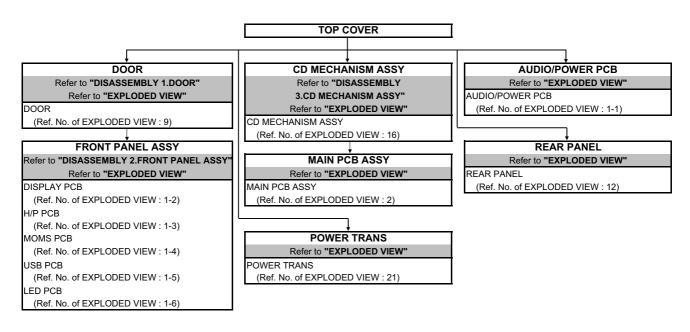
Front Panel side



Back Panel side

DISASSEMBLY

- Disassemble in order of the arrow of the figure of following flow.
- In the case of the re-assembling, assemble it in order of the reverse of the following flow.
- In the case of the re-assembling, observe "attention of assembling" it.



About the photos used for descriptions in the "DISASSEMBLY" section.

- The direction from which the photographs used herein were photographed is indicated at "Direction of photograph: ***" at the left of the respective photographs.
- Refer to the table below for a description of the direction in which the photos were taken.
- · Photographs for which no direction is indicated were taken from above the product.

The viewpoint of each photograph (photography direction)

[View from above]



Front side

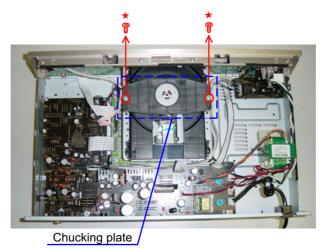




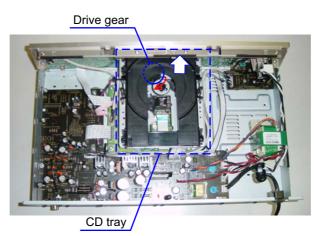
1. DOOR

Proceeding : TOP COVER \rightarrow DOOR

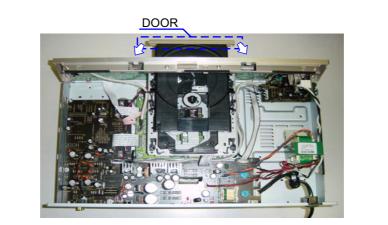
(1) Take off the Chucking plate after removing screws.

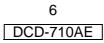


(2) Open the CD tray by turning the Drive gear clockwise.

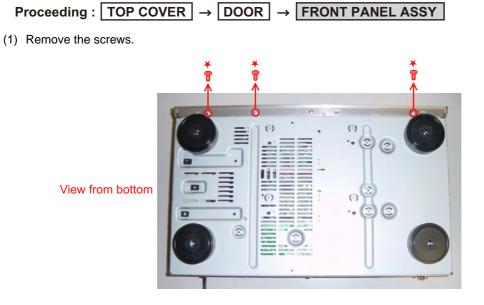


(3) Detach the DOOR.

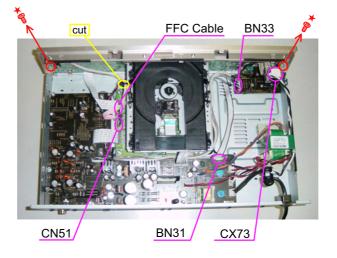




2. FRONT PANEL ASSY



(2) Cut the clamp bands, disconnect the connector wires and FFC Cable Remove the screws..

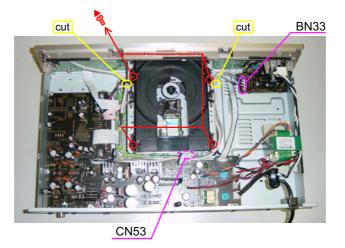


Please refer to "EXPLODED VIEW" for the disassembly method of each PCB included in FRONT PANEL ASSY.

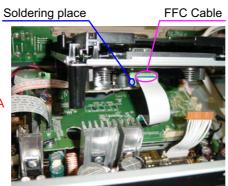
3. CD MECHANISM ASSY

Proceeding : TOP COVER \rightarrow DOOR \rightarrow CD MECHANISM ASSY

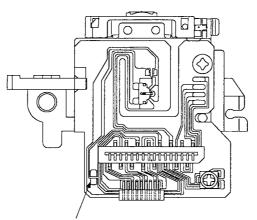
(1) Cut the clamp bands, disconnect the connector wires. Remove the screws.



(2) Laser short-circuit in Pick-up of CD MECHANISM, then disconnect the FFC Cable.



Direction of photograph: A



Laser short-circuit

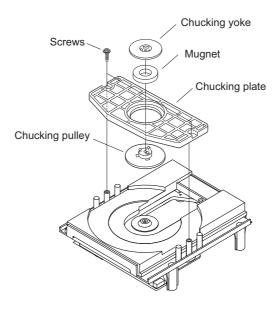
DISASSEMBLY OF MECHANIC

(Follow the procedure below in reverse order when reassembling.)

Caution : The optical pickup can damaged by sassily by static electricity charged on human body. Take necessary anti-static measures when repairing around the optical pickup.

1. Chucking plate

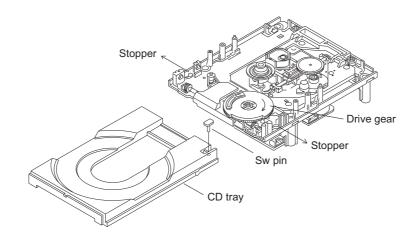
- (1) Remove 2 top screws, then detach the Chucking plate.
- (2) Detaching the Chucking pulley and chucking yoke by removing the 3 hooks, when abandoning CD MECHA ass'y.



2. CD tray

When abandoning CD MECHA ass'y, please detach the CD tray.

- (1) Detach the Sw pin on the CD tray.
- (2) Open the CD tray by turning the Drive gear clockwise.
- (3) Open the Stopper as shown in the fig., then detach CD tray.





Note Handling and Replacement of the Laser pick-up

1. Protection of the LD

Short a part of the LD circuit by soldering. After connection to a circuit, remove the short solder.

2. Precautions when handling the laser CD mechanism

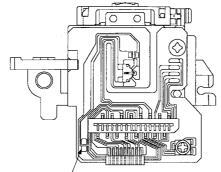
- Handle the laser pick-up so that it is not exposed to dust.
- Do not leave the laser pick-up bare. Be sure to cover it.
- If dust adheres on lens of the pick-up, blow it off with a blower brush.
- Do not shock the laser pick-up.
- Do not watch the light of the laser pick-up.

3. Cautions on assembling and adjustment

- Be sure that to the bench, jig, head of soldering iron (with ceramic) and measuring instruments are well grounded.
- Workers who handle the laser pick-up must be grounded.
- The finished mechanism (prior to anchoring in the set) should be protected against static electricity and dust. The mechanism must be stored that damaging outside forces are not received.
- When carrying the finished mechanism, hold it by the chassis body
- For proper operation, storage and operating environment should not contain corrosive gases. For example H2S, SO₂, NO₂, Cl₂ etc. In addition storage environment should not have materials that emit corrosive gases especially from silicic, cyanic, formalin and phenol group. I the mechanism or the set, existence of corrosive gases may cause no rotation in motor.

4. Determining whether the laser pick-up is defective

- Measure the waveform at RFO-VC on "MCU P.W.B. Unit ". (For measuring points and waveforms, see pages 21.)
- The laser pick-up is OK if the amplitude level of the measured RFO waveform is between 0.4 and 1.1 Vp-p, defective otherwise.



Protective soldering place for laser diode.

 $(x_1, y_2) \in \mathbb{R}^{n-1}$

SPECIAL MODE

| No | Key name | Function | Display |
|-----|--|---|---|
| 1 | Service mode | POWER switch is turned to on while pressing the ▲ and ■ button same time on DCD- 710AE.(Model name display) | |
| 1.1 | Version No. of main µ-com Display mode | Press the ►►I button. | "****" |
| 1.2 | Version No. of USB μ -com Display mode | Press the ►►I button while version No. of main µ -com is displayed. | |
| 1.3 | FLD check mode | When display is version No. of USB μ-com and the ►►I button is pressed, turn on the entire display is flashing with an interval of about 1 second. Press the ►►I button again in the turn on each grid of FLD. (Each grid is displayed at interval of 1 second.) | |
| 1.4 | Input button name Display mode | When display is each grid of the FLD check mode and the ►►I button is pressed, display name of the pressing button. Turn off the POWER switch to clear this mode. | |
| 2 | CD test mode | POWER switch is turned to on while pressing the ▲ and ■ button same time on DCD- 710AE.(Entering the Service mode) Press the ►/ III and ■ button same time while service mode.(CD TEST MODE display) | |
| 2.1 | Disc loading | Press the ▲ button to open the tray. Set a disc on the tray, then press the ▲ button again to close the tray. | |
| 2.2 | Servo check | Press the ►/ II button. Execute the following steps. ① LD ON (with servo still stopped) ② FOCUS ON (disc rotation, tracking off) If no disc loaded, retry then stop. ③ CLV ON | ▶II flashing 1 1 |

| No | Key name | Function | Display |
|-----|--------------------|---|---|
| | | ④ TRACKING ON | |
| 2.2 | Servo check | (5) SUB CODE readout (playback sound output) The BER (Block Error Rate) generated in 1 second's time is displayed on the upper tier of the display. The playback position (IN/MID/OUT) can be changed by pressing the I◄◄ or ►►I button. The count of the errors that could not be corrected with the C1 error correction system is displayed. | Image: Sector of the sector |
| | | ⑥ When display is as in ⑤ and the ►/ III button is pressed, conduct BER (Block Error Rate) display for 2 seconds. The BER (Block Error Rate) generated in 10 second's time is displayed of the display. ※Press ►/ III button continuously for over 1 second to switch directly to SUB CODE readout in step ⑤. | PPP : Playback position (IN/MID/OUT) ####### : 1 second's B.E.R., *****: 10 second's B.E.R. |
| 2.3 | Pickup movement | In the stop mode, pickup moves in REV (inwards) or FWD (outwards) direction when I < or > I button pressed. When I < button pressed, move to stop operation after detection that inner switch has turned on. Pickup movement stops when button released. (Pickup moves while button is pressed.) | |
| 2.4 | Stop | When ■ button is pressed, play operation and servo stop. After stopping, conduct reading of auto adjust values. | |
| 2.5 | All servo on | When ■ and ▲ button is pressed, all servos turn on, auto adjustment is performed and switch to playback operation. (Playback sound output) (NOTE)When the ■ button is pressed for over 1 second while the laser turns on and the laser current is measured. | |

| No | Key name | Function | Display |
|-----|-----------------------------|---|---|
| 2.6 | Laser current is display | When the button is pressed for over 1 second while the DCD-710AE is in the stop mode, the laser turns on and the laser current is measured. The laser drive current undergoes A/D conversion for calculation. Decimal values are discarded. The first current value is measured 3 seconds after the laser turns on. The current value is updated every 3 seconds. Press the button, CD test mode display (2) reappears. | XX : Stored data : EEPROM Stored value YY : Current value |
| | | Stored data is not cleared, even when the DCD-710AE is reset. ① Overwriting the stored data When the ►/ III button is pressed for over 5 seconds while the laser current is displayed, the current value is stored in the EEPROM (overwriting the stored data). Once rewriting is completed, the display in 2.6 reappears. | |
| | | Press the ■ button after executing the servo auto adjustment. When ►►I button is pressed, the adjustment values are displayed in the following order. ① FOCUS BALANCE | • ►II flashing ① ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ ◎ |
| 2.7 | Adjustment value | ② FOCUS GAIN③ TRACKING BALANCE | XX : Adjustment value (2) (2) (2) (2) (2) (3) |
| | display | (1) TRACKING GAIN (5) FOCUS OFFSET | XX : Adjustment value (4) (4) (5) (5) (7) (7) (7) (7) (7) (7) (7) (7 |
| | | (6) TRACKING OFFSET | XX : Adjustment value 6 Image: Sector Secto |

| No | Key name | Function | Display |
|-----|---|--|---|
| 2.7 | Adjustment value display | ⑦ RFRP ⑧ Return to ①. (Note) If auto adjustment is not completed, proper upluse are not displayed. | ⑦ ► II = = = = = = = = = = = = = = = = = = |
| 2.8 | Accumulated laser on time display | values are not displayed. When the DISC/USB and ▲ button is pressed while the DCD-710AE is in the stop mode, the accumulated laser on time is displayed. The laser drive times are added and the result is displayed. One count corresponds to 10 minutes. (Values under 10 minutes are discarded.) Count values are stored in the EEPROM every 10 minutes. The accumulated laser on time is displayed in hours. Laser ON/OFF is monitored and counted. Press the ■ button, CD test mode display (2) reappears. The count values are not cleared, even when the set is reset. Minimum display specification No. digits stored in EPROM: 4, 0XFFFF No. digits displayed: 5 When the time exceeds 10922 hours, the stored data is not updated and the value is fixed to 0xFFFF. (The display is fixed to 10922 hours.) Count value is reset When the ►/III button is pressed for over 5 seconds while the accumulated laser on time is display in 2.8 (00000 hours on the top line) reappears, and after 5 seconds the model name display reappears. Count value is reset upon shipment from the factory and when the mechanism is replaced. | Image: Second |
| 3 | CD heat run mode | AC is turned on while pressing the ▲ and ■ button same time on Main Unit. Switches to mode according to button input. Press a reverse button to switch the mode while displaying the version of the main µ com. (Normal heat run mode, Heat run short mode, Automatic mounting mode.) If an error occurs, display the error and stop operation at that point. Number of operations held. While heat run, the operation of each button is not valid except the ON/STANDBY button and pressing the ▲ and ■ button same time. | |

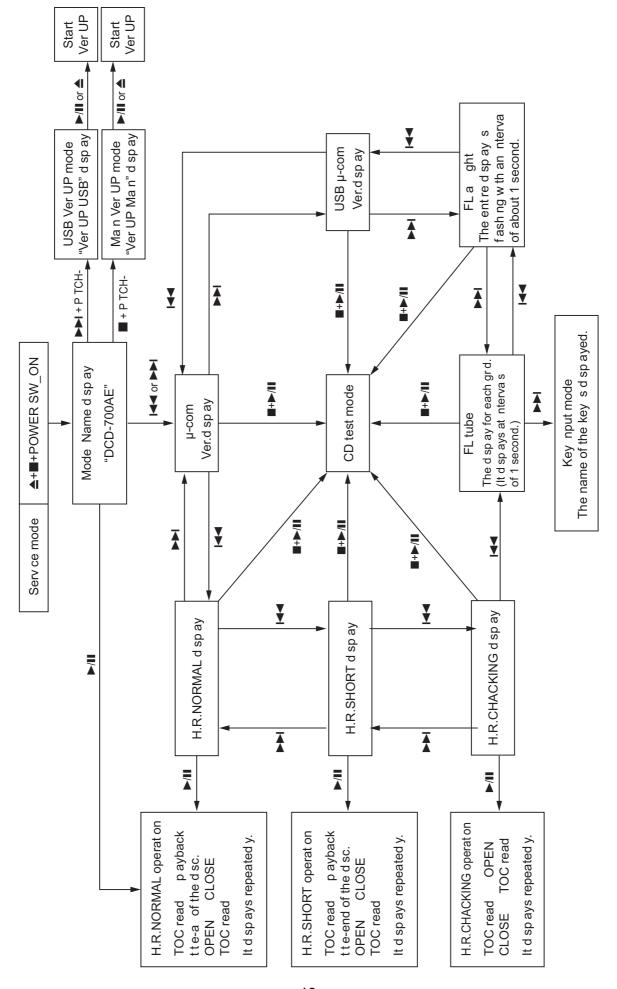
| No | Key name | Function | Display |
|-----|-------------------------|--|---|
| | | Press the ►/ II button while name of the model is displayed or H.R.NORMAL is displayed. Count this as the 0th heat run repetition. | Model Name display H.R.NORMAL display |
| | | | |
| | | | Normal heat run mode Normal display except when ► light, II flashing |
| | | $$ Play from the first to last track on disc. | Normal display except when ► light, II flashing |
| 3.1 | Normal heat run mode | ② After disc playback has finished, move pickup to innermost position and open tray. | |
| | | | XXXX : No. of heat run repetitions |
| | | ③ When loader open status detected, close tray again, re-read TOC and start playback from the first track on the disc. | ③ Normal display except when ► light, II flashing |
| | | | ④ |
| | | ④ The heat run repetition no. is incremented (increased by 1) when the tray is opened. If the ■ and ▲ button is pressed while operat- ing, number of heat run is displayed for 3 sec- onds. | Image: Constraint of the second se |
| | | ⑤ Conduct ① to ③ repeatedly. | |
| | | Press the ►/ II button while name of the | • H.R.SHORT display |
| | | H.R.SHORT is displayed.Count this as the 0th heat run repetition. | |
| | Heat run Short mode | | Heat run Short mode Normal display except when ► light, II light |
| | | 1 Play the last track only on disc. | ① Normal display except when ► light, II light |
| | | | 2 |
| 3.2 | | ② After disc playback has finished, move pickup to innermost position and open tray. | XXXX : No. of heat run repetitions |
| | | ③ When loader open status detected, close tray again, re-read TOC and start playback from the first track on the disc. | ③ Normal display except when ► light, Ⅱ light ④ |
| | | ④ The heat run repetition no. is incremented (increased by 1) when the tray is opened. If the ■ and ▲ button is pressed while oper- ating, number of heat run is displayed for 3 seconds. | |
| | | (5) Conduct (1) to (3) repeatedly. | |

| No | Key name | Function | Display |
|-----|----------------------------|---|--|
| | | | • H.R.CHACKING display |
| | | H.R.CHACKING is displayed.Count this as the 0th heat run repetition. | |
| | | | Heat run short mode Normal display except when ► flashing, II flash- ing |
| | | ① Play the last track only on disc. | ①Number of heat run is display |
| 3.3 | Automatic mounting mode | | XXXX : No. of heat run repetitions |
| | | | READING, CLOSEdisplay Normal display except when ► flashing, II flash- ing |
| | | ② The heat run repetition no. is incremented | ②Number of heat run is display |
| | | (increased by 1) when the tray is opened. If the ■ and ▲ button is pressed while oper- ating, number of heat run is displayed for 3 seconds. | XXXX : No. of heat run repetitions |
| | | ③ Conduct ① repeatedly. | |
| | | E1-00 : Disc cannot be detected E1-01 : Tracking offset adjustment not poss ble | |
| | | E1-02: Focus offset adjustment not possible | |
| | | E2-00: Focus servo dropped during playback. E2-01: Focus servo dropped during searching. | |
| | | E2-03 : Focus servo dropped during TOC reading. | X-XX : Error cord |
| | | E2-05: Focus servo dropped during manual search. | |
| | | E2-10: Subcode can no longer be read during playback | |
| | | E2-11 : Subcode can no longer be read during searching | |
| | Error display | E2-12: Subcode can no longer be read during TOC reading | |
| 3.4 | | E2-14: Subcode cannot be read during pause E2-15: Subcode cannot be read during manual | |
| | | E3-00: TOC could not be read within specified | |
| | | time E3-01: PVD/SVD analysis could not be | |
| | | completed within specified time E4-04 : Search time out | |
| | | E4-05 : Error in communications with CD decoder | |
| | | E5-00 : Inner switch not on | |
| | | E6-00 : Inner switch not off | |
| | | E8-00: Tray is not opened by the specified time. | |
| | | E8-01 : Tray is not closed by the specified time. E9-01 : Other error | |
| | | | |

| No | Key name | Function | Display |
|-----|----------------------------------|---|---|
| | | Number of heat run is display Press the ►►I button while the error is displayed. No. heat runs is displayed for 5 seconds, the error display reappears. | |
| 3.4 | Error display | | XXXX : No. of heat run repetitions Error display reappears after 5 seconds. See 3.4. |
| | | ② The track no. and time when the error occurred is displayed Press the I◄◀ button while the error is displayed. The track no. and time when the error occurred is displayed for 5 seconds, then error display reappears. | ② ▶, II with mode light or flashing. ▶1. ▶1. ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ |
| 4 | Main µ-com Version up mode | POWER switch is turned to on while simultaneously pressing ≜ and ■ button. While displaying model name, when ■ and the PITCH- button are pressed simultaneously, the tray opens. | Lecconce de la conce Set di se de 20010se |
| | | If an the ▲ or ►/ II button is pushed and a tray is closed, a disc will be loaded and Version up will be started. " INVALID DISC" is displayed when not found upgrade file and the tray is opened. | ① During a file search ② E E I E E E E E E E E E E E E E E E E |
| | Start version up | | Image: Sector state |
| 4.1 | | | |
| | | | Image: Second |
| | | | |
| 4.2 | End version up | After Version UP is completed, a tray is opened and it stops in | |
| | | Press POWER SW while simultaneously pressing ▲ and ▶►I button. | |
| 5 | Initialize | The system is reset, and once this is completed the unit is set to the normal mode. DIMMER : 100% PURE DIRECT : OFF DISC/USB : DISC REMOTE/BROWSE : REMOTE | |
| | | The laser current initial value and laser accumulated on time is not cleared. | |
| 6 | USB µ -com Version up mode | Press POWER SW while simultaneously pressing ▲ and ■ button. While displaying model name, when ■ and the PITCH- button are pressed simultaneously, the tray opens. | |

| No | Key name | Function | Display |
|-----|---------------------|---|--|
| 6.1 | Start version up | If an the ▲ or ►/ III button is pushed and a tray is closed, a disc will be loaded and Version up will be started. " INVALID DISC" is displayed when not found upgrade file and the tray is opened. | ① During a file search ② During a file check ③ During deletion ③ During deletion ④ Writing ④ When a file is not found ○ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ |
| 6.2 | End version up | After Version UP is completed, a tray is opened and it stops in | |
| 7 | EEPROM TEST mode | Press POWER SW while simultaneously pressing PITCH- and DISC/US button. POWER LED lights to the orange (TEST start). MUTING ON After EEPROM TEST, in the case of OK, LED lights to the green. After EEPROM TEST, in the case of NG, LED lights to the red. Mode is cancelled with POWER OFF. | |

Special mode transition diagram



19 DCD<u>-710AE</u>

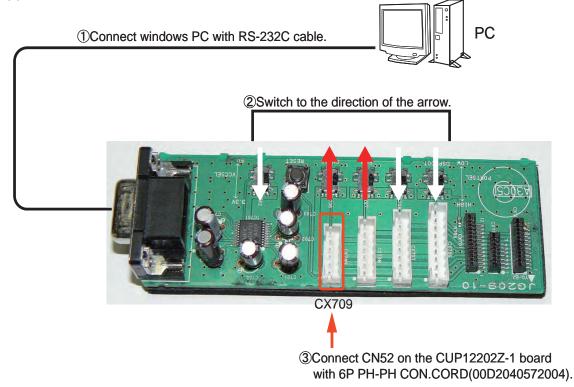
VERSION UPGRADE PROCEDURE OF FIRMWARE

Initial Version No.of main $\mu\text{-com}$ and USB $\mu\text{-com}.$

- 1 Main µ-com Ver 0035
- $\textcircled{2} \text{ USB } \mu\text{-com} \qquad \qquad \text{USB Ver 0014}$
- $\%\,$ When update Firmware, please confirm a last version in SDI.
- 1. Update for IC15(TMP92FD28AFG)
- (1) Prepare the windows PC that installed the FlashProg.EXE.

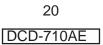


(2) Connect SPK-581 as forrows.

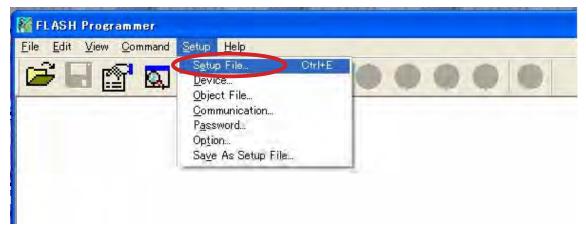


(3) Double click FlashProg.exe, and launch the FlashProgrammer.





(4) Click the Setup in the menu bar and select the Setup File.



(5) Click Device tab.

| Setu | η ρ | × |
|------|---|---|
| | Setup File Device Object File Communication | 1 |
| | Chosen Device Series Address Option TMP92FD28 Device TMP92FD28 Address Option Setting Apply | |
| | Select a device name. To fix your selection, press the [Apply] button. If your desired device name cannot be displayed, the definition file may be old. Get the latest definition file from our Web site. | |
| | Password Option OK Cancel | |

(6) Choose the TMP91FD28 in the Device, and choose the 900/H1 in the Series. Click Apply, and display the chosen Dvice.

| Set | up | × |
|-----|---|---|
| | Setup File Device Object File Communication | 1 |
| | Chosen Device TMP92FD28 Series 900/H1 Setting Device TMP92FD28 Address Option Setting Apply | |
| | Select a device name. To fix your selection, press the [Apply] button. If your desired device name cannot be displayed, the definition file may be old. Get the latest definition file from our Web site. | |
| | Password Option OK Cancel | |

(7) Click Password.

| Set | tup | × |
|-----|---|---|
| | Setup File Device Object File Communication | |
| | Chosen Device Series Address Option TMP92FD28 Device TMP92FD28 Apply | |
| | Select a device name. To fix your selection, press the [Apply] button. If your desired device name cannot be displayed, the definition file may be old. Get the latest definition file from our Web site. | |
| | Password Option OK Cancel | |

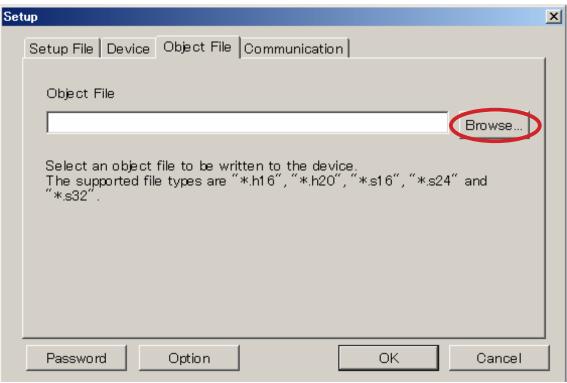
(8) Choose the Device is Blank. And Click OK.

| tup Password | |
|---|------------------|
| Device Password | |
| • Device is Blank | Search from File |
| O Auto(Search from Password Log File) | |
| C Manual | |
| Input Type C Ascli 💿 Hex | |
| Password www.www.www.www.www.www.www.www.www.ww | okok |
| Password count storage address | |
| Password comparison start address | |
| -Object File Password | |
| C Auto(Search from Object File) | |
| 🖸 Manual | |
| Password count storage address | |
| Password comparison start adress | |
| Save Password Log File | |
| | |
| Help | K Cancel |

(9) Click Object File tab.

| Set | | × |
|-----|---|-------|
| | Setup File Device Object File Communication | |
| | Object File | |
| | Browse | |
| | Select an object file to be written to the device. The supported file types are "*.h16", "*.h20", "*.s16", "*.s24" and "*.s32". | |
| | Password Option OK Cancel | , |

(10) Click Browse.



(11) Choose the Motorola S Format(*.s16,*.s24,*.s32) in Files of type. Choose the BOLERO_ver014.s24, and click Open.

| ファイルを開く | × |
|---|---|
| ファイルの場所の: 🔁 USB 💽 💌 🗈 💕 🎫 | 1 |
| BOLERO_ver014.s24 |] |
| | |
| | |
| | |
| | |
| ファイル名(N): BOLERO_ver014.s24 開((())) | |
| ファイルの種類(①・ Motorola S Format(*.s16,*.s24,*.s32) 🔹 キャンセル | |

(12) The place of the file is displayed.

| Se | etup |
|----|---|
| | Setup File Device Object File Communication |
| | Object File |
| | ↓¥02_光ディスク¥モデル¥第2G¥DCD-710AEX¥Farm¥08_SYS_↓● Browse |
| | Select an object file to be written to the device. The supported file types are "*.h16", "*.h20", "*.s16", "*.s24" and "*.s32". |
| | Password Option OK Cancel |

BOLERO_ver014.s24

Motorola S Format(*.s16,*.s24,*.s32)

開(⊙)

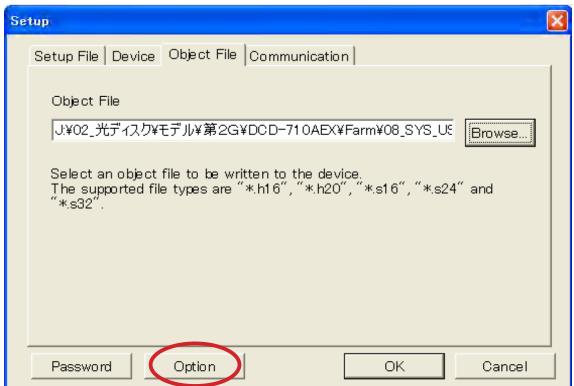
キャンセル

•

ファイル名(<u>N</u>):

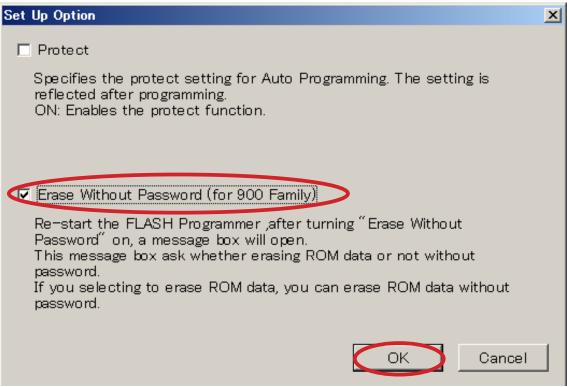
ファイルの種類(工):

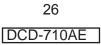
(13) Click Option.



(14) Choose Erase Without Password (for 900 Family).

And Click OK.





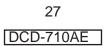
(15) Click Communication tab.

| Set | Setup | | x |
|-----|---|-------------|---|
| | Setup File Device Object File Communication | | |
| | | | |
| | COM Port COM3 - | | |
| | | | |
| | 🔿 Data Rate Manual 💿 Data Rate Auto | | |
| | | | |
| | Data Rate 9600 🗾 bps Data Rate 115200 bps | | |
| | Clock 9 | MHz | |
| | Enter the port name and the clock frequency used by the ta | | |
| | The divisor value is automatically calculated and the data rate 115200 bro | e is set to | |
| | 115200 bps. The clock frequency that can be used vary with each target | device and | |
| | should be checked in the datasheet of the target device. | | |
| | | |] |
| | Password Option OK | Cancel | |
| | | | |

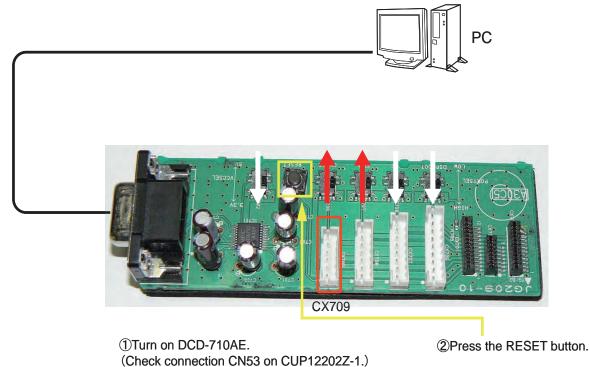
(16)Choose the Serial port number in the COM Port. Check the Data Rate Manual, and input 9 in the Clock. And Click OK.

| Setup | x |
|--|------|
| Setup File Device Object File Communication | |
| | |
| Choose the Serial port that connect with 232C ca | ble. |
| O Data Rate Manual O Data Rate Auto Data Rate 9600 ▼ bps Data Rate 115200 bps | |
| Clock 9 MHz | |
| Enter the port name and the clock frequency used by the target device. The divisor value is automatically calculated and the data rate is set to 115200 bps. | |
| The clock frequency that can be used vary with each target device and should be checked in the datasheet of the target device. | |
| Password Option OK Cancel | |

The setting is completed.



(17) Turn on DCD-710AE, and press the RESET button.



(18) If the connection with the Flash Programmer is successfully made, a dialogue box saying "Erase All data in this devise? (Y/N)" appears automatically. If the connection fails, error message will appear. (Ex.: E000)



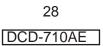
Click Yes.

(19) Click A (Auto Programing) to start writing.

| F 🕅 | LASH | Prog | ramn | ner | | | | | | | | | | |
|--------------|--------------|------|-------------|------------|---------------|--------------|---|---|---|---|---|------------|----------|--|
| <u>F</u> ile | <u>E</u> dit | ⊻iew | <u>C</u> or | nmand | <u>S</u> etup | <u>H</u> elp | | | | | | | \frown | |
| | 6 | | T | <u>Q</u> , | 2 | щ | ? | E | Р | V | C | B (| A | |
| | | | | | | | | | | | | | | |

(20) Turn off DCD-710AE.

(21) Remove the SPK-581 from DCD-710AE.

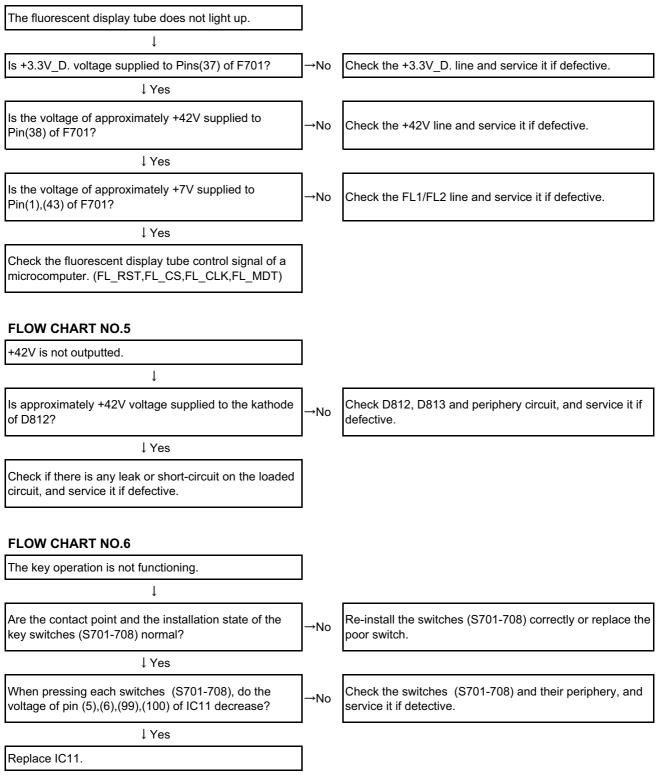


TROUBLE SHOOTING

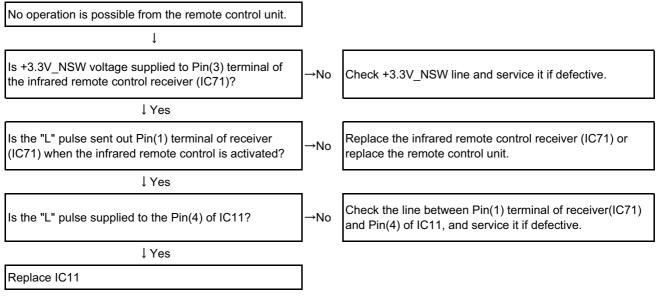
FLOW CHART NO.1

| The power cannot be turned on. |] | |
|---|-----|--|
| Ļ | - | |
| Is Power LED lit? | →No | Check if there is any leak or short-circuiting rectifier circuit |
| ↓Yes | - | of SUB Power transformer, and service it if defective. (T801, D818, D819, D820, |
| Is the +3.3V_NSW line voltage normal? | →No | D821,D822,D823,IC86,Q805,Q806) |
| ↓Yes | - | |
| Is the fuse normal? | →No | See FLOW CHART No.2 <the blows="" fuse="" out.=""></the> |
| ↓Yes | - | |
| Is normal state restored when once unplugged power cord is plugged again after several seconds? | →No | Check if there is any leak or short-circuiting on rectifier circuit of MAIN Power transformer, and service it if |
| ↓Yes | 4 | defective. |
| Is the ST+5V line voltage normal? | →No | (MAIN TRANS,D807,IC84) |
| ↓Yes | 4 | |
| Check each voltage regulator circuit and service it if defective. | | |
| FLOW CHART NO.2 The fuse blows out. |] | |
| Ļ | 4 | |
| Check if there is any leak or short-circuiting on rectifier circuit of MAIN Power transformer, and service it if defective. (D807,IC84,IC83,D801,D802,D803,D804,IC81,IC82) | | |
| Ļ | 1 | |
| After servicing, replace the fuse. |] | |
| FLOW CHART NO.3 | | |
| +3.3V_D is not outputted. |] | |
| Ļ | - | |
| Is +5V_D voltage supplied to Pin(1) of IC85? | →No | Check IC84 and the periphery circuit, and service it if defective. |
| ↓ Yes | 4 | , |
| Check IC85 and the periphery circuit, and service it if defective. | | |

FLOW CHART NO.4



FLOW CHART NO.7

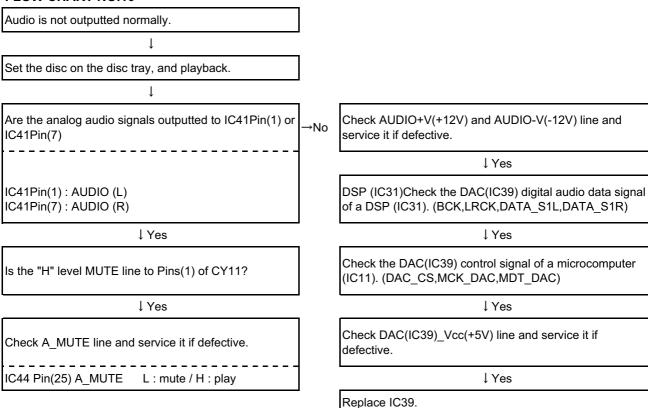


FLOW CHART NO.8

if defective.

| The disc tray cannot be opened and closed. (It can be done using the remote control unit.) | | | | | | |
|---|-----|------------------------------------|--|--|--|--|
| Ļ | | | | | | |
| Is the normal control voltage inputted to Pin(6) of IC11? Refer to "FLOW CHART NO.6" <the functioning.="" is="" key="" not="" operation=""></the> | →No | Replace the "OP/CL" button (S707). | | | | |
| ↓ Yes | _ | | | | | |
| Refer to "FLOW CHART NO.9" <the and="" be="" cannot="" closed.="" disc="" opened="" tray=""></the> | | | | | | |
| FLOW CHART NO.9 | _ | | | | | |
| The disc tray cannot be opened and closed. | | | | | | |
| Ļ | - | | | | | |
| Check the line between CN23 and IC14, and service it | | | | | | |

FLOW CHART NO.10



↓ Yes

↓ Yes

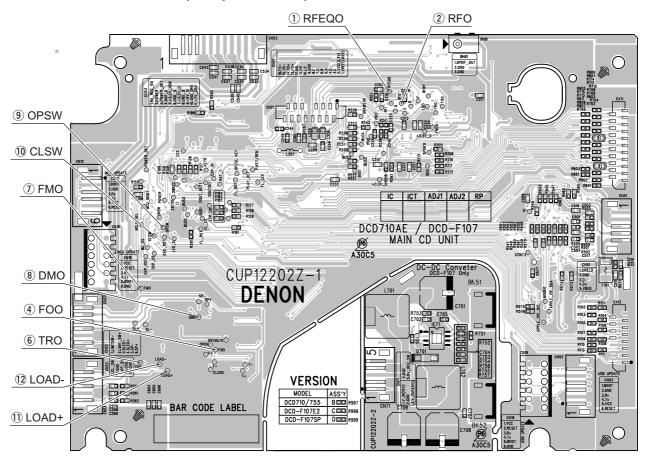
↓ Yes

↓ Yes

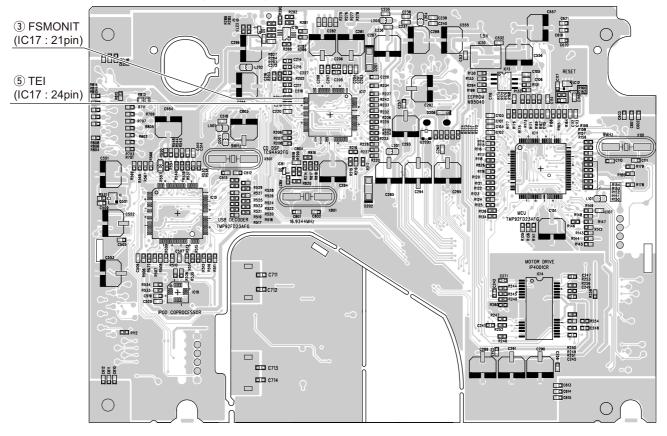
MEASURING POINT AND WAVEFORMS

MEASURING POINT

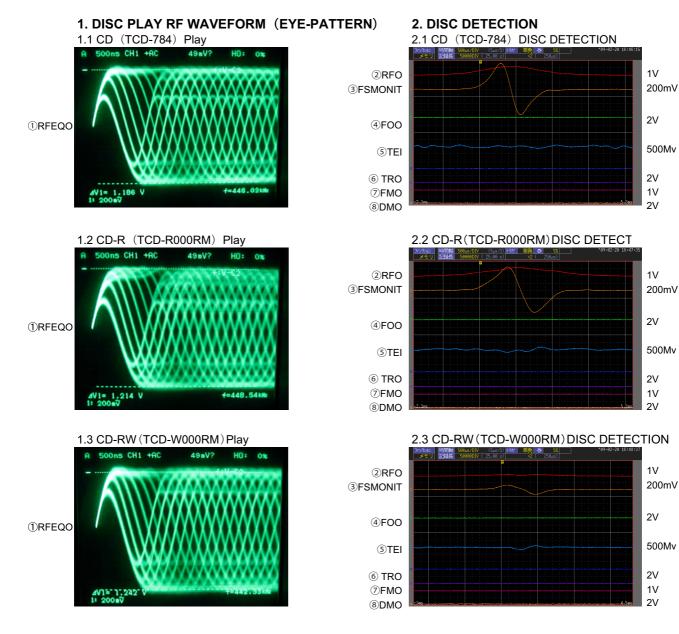
CUP12202 MAIN PCB ASSY (Component side)

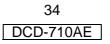


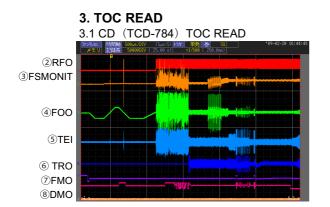
CUP12202 MAIN PCB ASSY (Foil side)



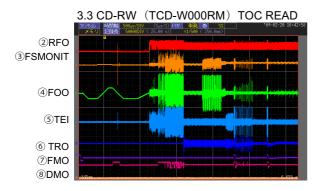
WAVEFORMS



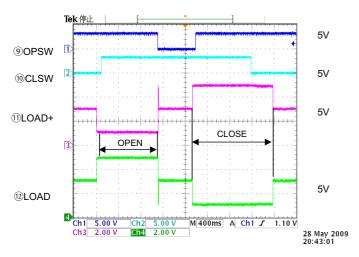




3.2 CD-R (TCD-R000RM) TOC READ
 arrow
 arrow
 arrow
 arrow
 brow
 brow
 brow
 brow
 brow
 brow
 brow
 brow
 brow



5. LOADER OPEN-CLOSE



4. FOCUS ADJUSTMENT

1V

200mV

2V

500Mv

2V

1V

2V

1V

200mV

2V

500Mv

2V

1V

2V

1V

200mV

2V

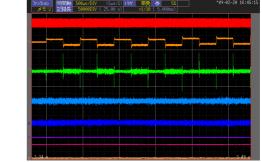
500Mv

2V

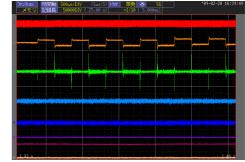
1V

2V

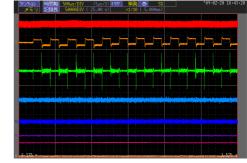
4.1 CD (TCD-784) FOCUS ADJUSTMENT

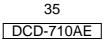


4.2 CD-R (TCD-R000RM) FOCUS ADJUSTMENT



4.3 CD-R (TCD-R000RM) FOCUS ADJUSTMENT

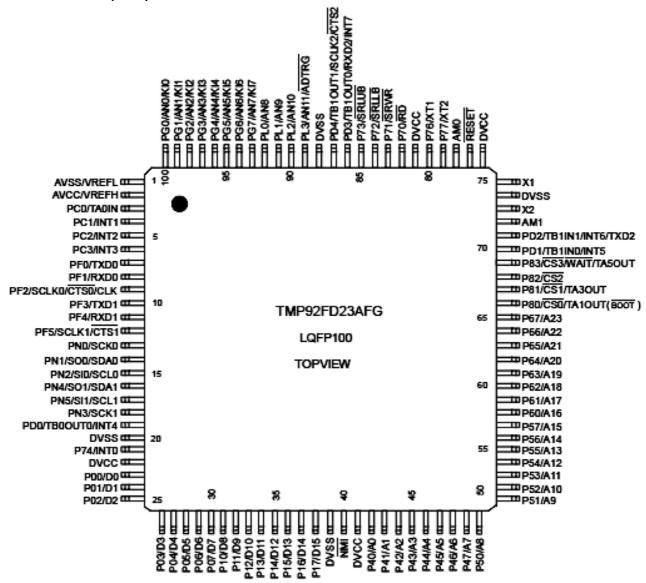




SEMICONDUCTORS

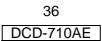
Only major semiconductors are shown, general semiconductors etc. are omitted to list. The semiconductor which described a detailed drawing in a schematic diagram are omitted to list.

1. IC's TMP92FD23AFG (IC11)

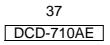


TMP92FD23AFG Terminal Function

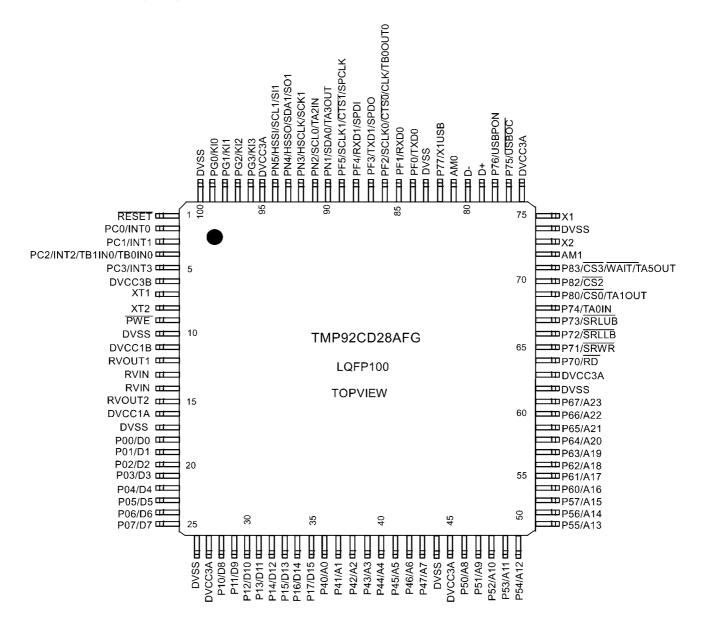
| Pin No | IC Terminal ame | Terminal name | I/O setting | Terminal function | Remarks |
|-----------|--------------------|---------------|----------------|------------------------------------|-------------------------------|
| 1 | AVSS/VREFL | Power | Р | Power supply (GND) | |
| | | supply(GND) | | | |
| 2 | AVCC/VREFH | Power | Р | Power supply (+3.3V) | |
| | | supply(+3.3V) | | | |
| 3 | PC0/TA0IN | Non(PU) | I | Non(PU) | Port only for input (Schmitt) |
| 4 | PC1/INT1 | Non(PU) | I | POWER KEY (F107 only) | Port only for input (Schmitt) |
| 5 | PC2/INT2 | PLAY KEY | I | PLAY KEY | Port only for input (Schmitt) |
| 6 | PC3/INT3 | OP/CL KEY | I | OP/CL KEY | Port only for input (Schmitt) |
| 7 | PF0/TXD0 | FL_MDT | 0 | FL tube communication line (data) | (Schmitt I input) |
| 8 | PF1/RXD0 | OPEN | 0 | Non (NC) | (Schmitt I input) |
| 9 | PF2/SCLK0/CTS0/CLK | FL_CLK | 0 | FL tube communication line (clock) | (Schmitt I input) |
| 10 | PF3/TXD1/HSSO | [TXD] | 0 | DENON BUS [Communication lines for | (Schmitt I input) |
| | | | | writing](F107 only) | |
| 11 | PF4/RXD1/HSSI | [RXD] | I | DENON BUS [Communication lines for | (Schmitt I input) |
| | | | | writing](F107 only) | |



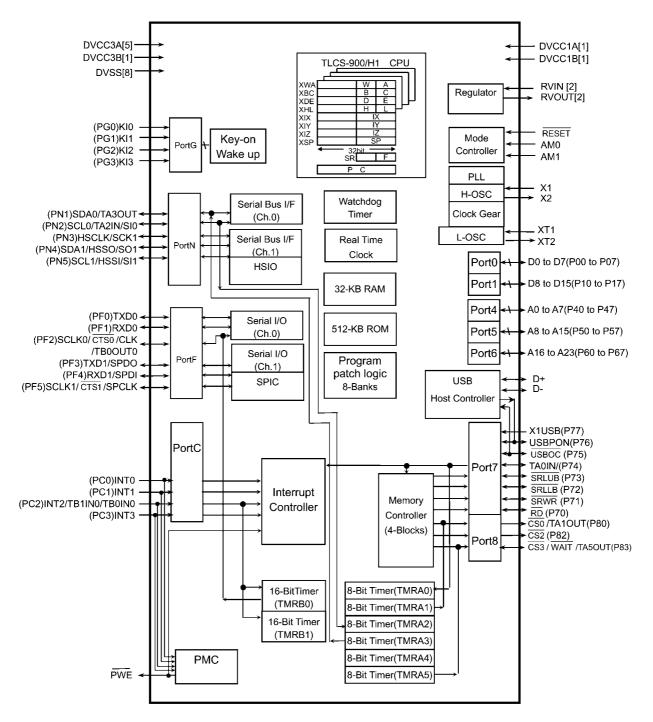
| Pin No | IC Terminal ame | Terminal name | I/O setting | Terminal function | Remarks | | | | |
|-----------|--------------------------------------|--------------------------|--------------------|--|--|--|--|--|--|
| 12 | PF5/SCLK1/CTS1/ HSCLK | 100KΩ/PD | I | DENON BUS(F107 only) | (Schmitt I input) | | | | |
| 13 | PN0/SCK0 | CONT1 | 0 | Reserved | (Schmitt I input), When resetting it, it becomes output latch 1. | | | | |
| 14 | PN1/SO0/SDA0 | CONT2 | I | Reserved | (Schmitt I input and open drain output), When resetting it, it becomes output latch 1. | | | | |
| 15 | PN2/SI0/SCL0 CONT3 O Reserved | | Reserved | (Schmitt I input and open drain output), When resetting it, it becomes output latch 1. | | | | | |
| 16 | PN4/SO1/SDA1 | E2P_DI | 0 | Cereal data output for E2PROM | (Schmitt I input and open drain output), When resetting it, it becomes output latch 1. | | | | |
| 17 | PN5/SI1/SCL1 | E2P_DO | I | Cereal data input for E2PROM | (Schmitt I input and open drain output), When resetting it, it becomes output latch 1. | | | | |
| 18 | PN3/SCK1 | E2P_CLK | 0 | Serial clock output for E2PROM | (Schmitt I input)§When resetting it, it becomes output latch 1. | | | | |
| 19 | PD0/TB0OUT0/INT4 | PU | I | DENON BUS (F107 only) | (Schmitt I input) | | | | |
| 20 | VSS Power supply P Power supply (GND | | Power supply (GND) | | | | | | |
| 21 | P74/INT0 | REMOCON | I | Remote control input (710only) | Port only for input (Schmitt) | | | | |
| 22 | DVCC | Power supply (+3.3V) | Р | Power supply(+3.3V) | | | | | |
| 23 | P00/D0 | OPEN_F | 0 | Tray OPEN control | P0x : Setting is possible by 1bit unit | | | | |
| 24 | P01/D1 | CLSE_F | 0 | Tray CLOSE control | | | | | |
| 25 | P02/D2 | A_Mute | 0 | DAC output audio mute | L; Mute, H; Mute cancel | | | | |
| 26 | P03/D3 | DRVMUTE | 0 | CD drive mute output | L; Mute, H; Mute cancel (Cancels above 3V) | | | | |
| 27 | P04/D4 | INSW | I | CD drive limit SW input | | | | | |
| 28 | P05/D5 | CLSW | I | CD drive close SW input | | | | | |
| 29 | P06/D6 | OPSW | I | CD drive opening SW input | | | | | |
| 30 | P07/D7 | (USB_DET) PU | I | Reserved | | | | | |
| 31 | P10/D8 | FL_RST | 0 | FL tube communication line (reset) | P1x : Setting is possible by 1bit unit | | | | |
| | P11/D9 | FL_CS | 0 | FL tube communication line (chip selection) | | | | | |
| | P12/D10 | E2P_CS | 0 | Chip selection for E2PROM (Act:H) | | | | | |
| 34 | P13/D11 | MODEL SEL | I | Model select H: 710AE / L: F107 | | | | | |
| | P14/D12 | 14BUS0 (DSP) | 0 | *TC94A92FG bus control | | | | | |
| | P15/D13 | 14BUS1 (DSP) | 0 | *TC94A92FG bus control | | | | | |
| | P16/D14 | BUS2 (DSP) | 0 | *TC94A92FG bus control | | | | | |
| | P17/D15 | 92BUS3 (DSP) | O P | *TC94A92FG bus control | | | | | |
| | DVSS | Power supply (GND) | | Power supply (GND) | | | | | |
| 40 | NMI | PULL DOWN (0Ω) | I | NMI | BaseModel is PULL DOWN(0Ω). | | | | |
| | DVCC | Power supply (+3.3V) | Р | Power supply(+3.3V) | | | | | |
| | P40/A0 | MODE0 | I | Select destination | P4x : Setting is possible by 1bit unit | | | | |
| | P41/A1 | MODE1 | | Select destination | 00 : E2, 01 : E3, 10 : JP, 11 : E1C | | | | |
| | P42/A2 | LED_R | 0 | STB is red LED | H;ON L;OFF | | | | |
| | P43/A3 | LED_G | 0 | P.ON is green LED | H;ON L;OFF | | | | |
| | P44/A4 | DAC_CS | 0 | DAC1796 CS | | | | | |
| 47 | P45/A5 | (DAC_MDO) | 0 | (DAC1796 MDO) No control | 710: PD in 1M | | | | |
| | P46/A6 P47/A7 | DAC_RST MDT_DAC/DXP/ | 0 | DAC1796 RST DAC1796 MDI | 710; PD in 1M | | | | |
| 50 | P50/A8 | BU MCK_DAC/DXP/ BU | 0 | DAC1796 MCK | P5x : Setting is possible by 1bit unit | | | | |
| 51 | P51/A9 | DXP_CS | 0 | Chip selector for DXP6000 | | | | | |
| | P52/A10 | DXP_RST | 0 | Reset for DXP6000 | | | | | |
| | P53/A11 | BU_CS | 0 | Chip selector for BU2630 | | | | | |
| 54 | P54/A12 | MCK_SEL | 0 | MCLK selector | | | | | |
| 55 | P55/A13 | POWER | 0 | MainTRANS on / off | | | | | |
| 56 | P56/A14 | USBRST(DSP) | 0 | Reset for TMP92FD28FG | | | | | |
| 57 | P57/A15 | DECRST | 0 | Reset for TC94A92FG | RESET in OR of D305 and D306 → RESET in +3.3V_D | | | | |
| 58 | P60/A16 | OPEN | 0 | Non | P6x : Setting is possible by 1bit unit | | | | |



| Pin No | IC Terminal ame | Terminal name | I/O setting | Terminal function | Remarks |
|-----------|-------------------------------------|--------------------------------|----------------|---|---|
| 59 | P61/A17 | (SRAMSTB) | I | Reserved | |
| 60 | P62/A18 | PWR_DET | I | "AC power OFF detection input (When it is unplugged AC : L)" | |
| 61 | P63/A19 | DOUT_MUTE | 0 | Mute output of digital data | |
| 62 | P64/A20 | DOUT_SEL | 0 | Digital data output selection | |
| 63 | P65/A21 | 92SBSY | | OASIS system busy input | Connection with Borelo (4) pin. |
| 64 | P66/A22 | 28INTO | 0 | Reserved Reserved | |
| 65 66 | P67/A23 P80/CS0/ | BOOT_CONT BOOT | 0 | BOOT (for farm writing) | Only the output port |
| | TA1OUT[BOOT] P81/CS1/TA3OUT | OPEN | 0 | Non | |
| 67 68 | P81/CS1/TA3001 P82/CS2 | 5V_REG_SW | 0 | Reserved | Only the output port Only the output port |
| 69 | P83/CS3/WAIT/ | 92BUCK(DSP) | 0 | *TC94A92FG bus control | Schmitt input |
| 70 | TA5OUT | | | | |
| 70 71 | PD1/TB1IN0/INT5 PD2/TB1IN1/INT6/ | 92DREQ(MP3) 28TXD | 0 | OASIS DREQ input For TMP92FD28FG communication | (Schmitt I input) only for input (Schmitt I input) |
| 1 | TXD2 | 20170 | 0 | FOI TMP92FD28FG COMMUNICATION | (Schmitt Linput) |
| 72 | AM1 | PULL UP(0Ω) | 1 | AM1 Pull UP | Fixed H |
| 73 | X2 | Oscillator | 0 | Oscillator connection pin | |
| 74 | DVSS | connection pin Power supply | Р | Power supply (GND) | |
| 74 | | (GND) | Г | | |
| 75 | X1 | Oscillator connection pin | I | Oscillator connection pin | |
| 76 | DVCC | Power supply (+3.3V) | Р | Power supply (+3.3V) | |
| 77 | RESET | RESET | I | Reset input of μ -com | |
| 78 | AM0 | PULL UP(0Ω) | I | AM0 Pull UP | Fixed H |
| 79 | P77/XT2 | PULL UP(47kΩ) | 0 | Non | Open drain output |
| 80 | P76/XT1 | 92CCE(DSP) | 0 | *TC94A92FG bus control | Open drain output R644(OPEN);GND |
| 81 | DVCC | Power supply (+3.3V) | Р | Power supply (+3.3V) | |
| 82 | P70/RD | CHECKIN(100KΩ/ PD) | I | P.W.B. check mode | Schmitt input and with PU resistance |
| 83 | P71/SRWR | CHECK1(100KΩ/ PD) | I | P.W.B. check mode | Schmitt input and with PU resistance |
| 84 | P72/SRLLB | CHECK2(100KΩ/ PD) | I | P.W.B. check mode | Schmitt input and with PU resistance |
| 85 | P73/SRLUB | CHECK3(100KΩ/ PD) | I | P.W.B. check mode | Schmitt input |
| 86 | PD3/TB1OUT0/RXD2/ INT7 | 28RXD | I | For TMP92FD28FG communication | (Schmitt I input) |
| 87 | PD4/TB1OUT1/SCLK2/ CTS2 | OPEN | 0 | Non | (Schmitt I input) |
| 88 | DVSS | Power supply(GND) | Р | Power supply(GND) | |
| 89 | PL3/AN11/ADTRG | 100KΩPD | I | Non | Port only for input(Schmitt) |
| 90 | PL2/AN10 | 100KΩ/PD | 1 | Non | Port only for input(Schmitt) |
| 91 | PL1/AN9 | 100KΩ/PD | 1 | Non | Port only for input(Schmitt) |
| 92 | PL0/AN8 | 100KΩ/PD | 1 | Non | Port only for input(Schmitt) |
| 93 | PG7/AN7/KI7 | 100KΩ/PD | I | Non | Port only for input(Schmitt)/Key on W.UP |
| 94 | PG6/AN6/KI6 | 100KΩ/PD | I | Non | Port only for input(Schmitt)/Key on W.UP |
| 95 | PG5/AN5/KI5 | 100KΩ/PD | I | Non | Port only for input(Schmitt)/Key on W.UP |
| 96 | PG4/AN4/KI4 | 100KΩ/PD | I | Non | Port only for input(Schmitt)/Key on W.UP |
| 97 | PG3/AN3/KI3 | 100KΩ/PD | I | Non | Port only for input(Schmitt)/Key on W.UP |
| 98 | PG2/AN2/KI2 | LD_CHCK | I | Input for LD check | Port only for input(Schmitt)/Key on W.UP |
| 99 | PG1/AN1/KI1 | KEY1 | I | KEY input 1 | Port only for input(Schmitt)/Key on W.UP |
| 100 | PG0/AN0/KI0 | KEY0 | I | KEY input 10 | Port only for input(Schmitt)/Key on W.UP |



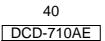
39 DCD-710AE



(): Initial function after reset

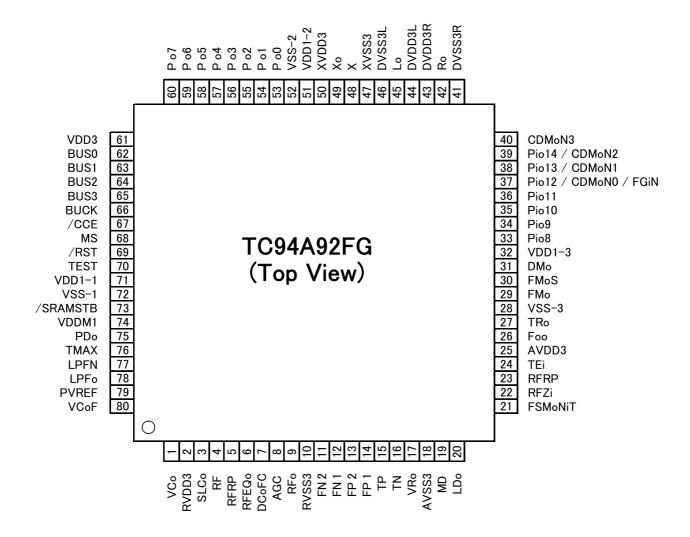
TMP92CD28AFG Terminal Function

| Pin No | IC Terminal name | DCD-710AE/755SE Terminal name | I/O | RST | INIT | STB | Pull U/D | Port function |
|-----------|----------------------------|----------------------------------|-----|-----|------|-----|-------------|--|
| 1 | /RESET | RESETTerminal | I | I | Ι | - | PU | Port only for input (Schmitt input and PU resistance) |
| 2 | PC0/INT0 | Reserved | - 1 | - | Ι | - | PD | Port only for input (with Schmidt input) |
| 3 | PC1/INT1 | Non (PD) | Ι | - | Ι | - | PD | Port only for input (with Schmidt input) |
| 4 | PC2/INT2/TB1IN0/ TB0IN0 | SYSTEM BUSY input | I | - | I | - | | Port only for input (with Schmidt input) |
| 5 | PC3/INT3 | STREQ input | I | - | Ι | - | | Port only for input (with Schmidt input) |
| 6 | DVCC3B | Power supply (+3.3V) | Ρ | Р | Ρ | Р | - | Power supply (+3.3V) |
| 7 | XT1 | Non (NC) | Ι | - | Ι | - | - | Input port |
| 8 | XT2 | Non (NC) | 0 | 0 | Ι | - | - | Output port |



| PME Non (NC) D - O - Output port 10 DVSS Power supply (ADD) P P P P Power supply (ADD) 11 DVCC1B Power supply (ADD) P P P P Power supply (ADD) 12 RVUT1 Bulkin +15.V Regulator 0 - O - Voltage output is not in the Flash version is a terminal Power supply (ADD) 13 RVIN Bulkin +15.V Regulator 1 - 1 - - Flash version is a terminal Power supply (ADD) 14 RVIN Bulkin +15.V Regulator 0 - 0 - P< | Pin No | IC Terminal name | DCD-710AE/755SE Terminal name | I/O | RST | INIT | STB | Pull U/D | Port function |
|--|-----------|------------------|----------------------------------|-----|-----|-------|-----|-------------|--|
| Power supply (+1.5V) P P P P P Personal supply (+1.5V) 12 RVOLT1 Bulkin +1.5V Regulator input I - 0 - - Voltage output is not in the Flash version output 13 RVIN Bulkin +1.5V Regulator input I - I - I - Flash version is a terminal Power supply (+1.5V) 14 RVIN Bulkin +1.5V Regulator output I - I - I - Voltage output is not in the Flash version output 15 RVOLT2 Bulkin +1.5V Regulator P | 9 | PWE | Non (NC) | 0 | - | 0 | - | - | Output port |
| 12 RVDUT1 Built-in +1.5V Regulator upput 0 - 0 - Voltage output is not in the Flash version upput 13 RVIN Built-in +1.5V Regulator input I - I - Flash version is a terminal Power supply input 14 RVIN Built-in +1.5V Regulator output I - I - - Flash version is a terminal Power supply input 15 RVOT2 Built-in +1.5V Regulator output P P P P Power supply(FLSV) 16 DVCC1A Power supply (-1.5V) P P P Power supply(FLSV) 18 POIDO Non (NC) O - OL - Pox: Setting is possible by thut unit 20 POIDO Non (NC) O - OL - Pox: Setting is possible by thut unit 21 POIDO Non (NC) O - OL - Pox: Setting is possible by thut unit 22 POIDO Non (NC) O - OL - Pox: Setting is possible by t | 10 | DVSS | Power supply (GND) | Р | Р | Р | Р | - | Power supply (GND) |
| 12 RVOLT1 Bulkin +1.5V Regulator input 0 - 0 - Voltage output is not in the Flash version input 13 RVIN Bulkin +1.5V Regulator input I - I - Flash version is a terminal Power supply input 14 RVIN Bulkin +1.5V Regulator output 0 - 0 - - Rash version is a terminal Power supply (15.0V 15 RVOUT2 Bulkin +1.5V Regulator output 0 P P P P Power supply(RSD) 16 DVCC1A Power supply (-1.5V) P P P Power supply(RSD) 17 DVSS Power supply (-1.5V) O - OL - Pox: setting is possible by tolumit 18 P0200 Non (NC) O - OL - Pox: setting is possible by tolumit 21 P0202 Non (NC) O - OL - Pox: setting is possible by tolumit 22 P0202 Non (NC) O - OL - Pox: setting i | 11 | DVCC1B | Power supply (+1.5V) | Р | Р | Р | Р | - | Power supply (+1.5V) |
| 13 RVIN Built-in +15V Regulator input 1 - - - Flash version is a terminal Power supply input 14 RVIN Built-in +15V Regulator output 1 - 1 - - Flash version is a terminal Power supply (SND) 15 RVOUT2 Built-in +15V Regulator output 0 - 0 - - Voltage output is not in the Flash version output 16 DVCC1A Power supply (HDV) P P P P Power supply (SND) 18 P0000 Non (NC) 0 - O(L) - Por: Setting is possible by to thu unit 21 P0202 Non (NC) 0 - O(L) - Por: Setting is possible by to thu unit 22 P02056 Non (NC) 0 - O(L) - Por: Setting is possible by to thu unit 24 P06056 Non (NC) 0 - O(L) - Por: Setting is possible by to thu unit 25 P0707 Non (NC) 0 - O(L) | 12 | RVOUT1 | Built-in +1.5V Regulator | 0 | - | 0 | - | - | |
| 14 RVIN Built-in + 15V Regulator output I - I - - Flash version is a terminal Power supply (NDC1A 15 RVOUT2 Built-in + 15V Regulator output 0 - 0 - - Votage output is not in the Flash version output 16 DVCC1A Power supply (15V) P P P P - Power supply (15V) 18 P0070 Non (NC) 0 - 0(1) - - Pore: Setting is possible by thit unit 19 P01701 Non (NC) 0 - 0(1) - - Pore: Setting is possible by thit unit 12 P04704 Non (NC) 0 - 0(1) - - Pore: Setting is possible by thit unit 28 P04707 Non (NC) 0 - 0(1) - Pore: Setting is possible by thit unit 28 P04708 Non (NC) 0 - 0(1) - Pore: Setting is possible by thit unit 28 P14704 Non (NC) 0 <td>13</td> <td>RVIN</td> <td>Built-in +1.5V Regulator</td> <td>Ι</td> <td>-</td> <td>Ι</td> <td>-</td> <td>-</td> <td>Flash version is a terminal Power supply</td> | 13 | RVIN | Built-in +1.5V Regulator | Ι | - | Ι | - | - | Flash version is a terminal Power supply |
| 15 RV0UT2 Built-in +15V Regulator output 0 - 0 - 0 - 0 - Voltage output is not in the Flash version output/(1-15V) 16 DVCC1A Power supply (1-15V) P P P P P P P P P P P P Power supply (1-15V) 17 DVSS Power supply (1-15V) 0 - 0(1) - Pox: Setting is possible by 1bit unit 19 P0100 Non (NC) 0 - 0(1) - Pox: Setting is possible by 1bit unit 21 P03005 Non (NC) 0 - 0(1) - Pox: Setting is possible by 1bit unit 23 P0505 Non (NC) 0 - 0(1) - Pox: Setting is possible by 1bit unit 24 P0606 Non (NC) 0 - 0(1) - Pox: Setting is possible by 1bit unit 25 P0070 Non (NC) 0 - 0(1) - Pox: Setting is possible by 1bit unit < | 14 | RVIN | Built-in +1.5V Regulator | I | - | I | - | - | Flash version is a terminal Power supply |
| 16 DYCC1A Power supply (r1.5V) P | 15 | RVOUT2 | Built-in +1.5V Regulator | 0 | - | 0 | - | - | Voltage output is not in the Flash version |
| 17 DVSS Power supply (GND) P | 16 | DVCC1A | | Р | Р | Р | Р | - | Power supply(+1.5V) |
| 18 PowD Non (NC) 0 - OL - Pox: Setting is possible by thiu unit 19 P01/01 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 19 P03/02 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 12 P04/04 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 12 P06/05 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 12 P06/05 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 12 P06/07 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 14 P10/08 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 11 P10/01 Non (NC) 0 - O(L) - Pox: Setting is possible by thiu unit 12 | 17 | | | Р | Р | Р | Р | - | |
| 19 PolID1 Non (NC) 0 - O(L) - Poc: Setting is possible by Tbit unit 21 P02D2 Non (NC) 0 - O(L) - POx: Setting is possible by Tbit unit 21 P05D5 Non (NC) 0 - O(L) - POx: Setting is possible by Tbit unit 22 P05D5 Non (NC) 0 - O(L) - POX: Setting is possible by Tbit unit 24 P06D6 Non (NC) 0 - O(L) - POX: Setting is possible by Tbit unit 25 P07/D7 Non (NC) 0 - O(L) - POX: Setting is possible by Tbit unit 26 P07/D8 Non (NC) 0 - O(L) - POX: Setting is possible by Tbit unit 31 P12/D10 Non (NC) 0 - O(L) - POX: Setting is possible by Tbit unit 32 P14/D12 C/HECK IN 100k PD 0 - O(L) - POX: Setting is possible by Tbit unit 34 | 18 | P00/D0 | | 0 | - | O(L) | - | - | |
| 20 Poz/D2 Non (NC) 0 - O(L) - Pox: Setting is possible by 1bit unit 21 P03/D3 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit 23 P05/D5 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit 24 P06/D6 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit 25 P07/D7 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit 26 DVSS Power supply (GND) P P P P P Power supply (GND) 27 DVCC3A Power supply (GND) 0 - O(L) - P0x: Setting is possible by 1bit unit 29 P11/D9 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit 30 P12/D10 Non (NC) 0 - O(L) - P0x: Setting is possible by 1bit unit | | | | 0 | - | · · · | - | - | |
| 1 Poil Non (NC) 0 - O(L) - Poil Setting is possible by Thit unit 23 PoiADA Non (NC) 0 - O(L) - PoiX: Setting is possible by Thit unit 24 PoiADE Non (NC) 0 - O(L) - PoiX: Setting is possible by Thit unit 25 PoiXDS Power supply (ND) P P P P Power supply (ND) 26 DVSS Power supply (A)3V) P P P P Power supply (SND) 27 DVCC3A Power supply (A)3V) P P P P Power supply (A)3V) 28 P10DB Non (NC) O - O(L) - POX: Setting is possible by Thit unit 29 P112D1 Non (NC) O - O(L) - POX: Setting is possible by Thit unit 31 P13Ch1 MODEL I, E107 H ; 7 I - O(L) - POX: Setting is possible by Thit unit 32 P14/D12 | | | | - | - | () | - | - | |
| 22 PolAD4 Non (NC) 0 - O(L) - Pol: Setting is possible by thit unit 23 POS/D5 Non (NC) 0 - O(L) - POX: Setting is possible by thit unit 24 PO6/D6 Non (NC) 0 - O(L) - POX: Setting is possible by thit unit 25 POYO7 Non (NC) 0 - O(L) - POX: Setting is possible by thit unit 26 DVSS Power supply (GND) P P P P Power supply (GND) 27 DVCC3A Power supply (GND) O - O(L) - POX: Setting is possible by thit unit 28 P10/D8 Non (NC) 0 - O(L) - POX: Setting is possible by thit unit 30 P12/D1 OHECK IN 100k PD 0 - O(L) - POX: Setting is possible by thit unit 32 P14/D12 CHECK 1 100k PD 0 - O(L) - POX: Setting is possible by thit unit 34 <td></td> <td></td> <td></td> <td></td> <td>-</td> <td></td> <td></td> <td>-</td> <td></td> | | | | | - | | | - | |
| 23 POSD6 Non (NC) O - POx: Setting is possible by 1bit unit 24 POSD6 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 25 POYD7 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 26 DVSS Power supply (GND) P P P P Power supply (GND) 27 DVCC3A Power supply (GND) O - O(L) - POwer Supply (GND) 28 PI/DD8 Non (NC) O - O(L) - PDx: Setting is possible by 1bit unit 30 P13/D11 MODEL DL; F107 H .7 I - O(L) - POx: Setting is possible by 1bit unit 31 P13/D11 MODEL DL; F107 H .7 I - O(L) - POx: Setting is possible by 1bit unit 32 P14/D12 CHECK1 100k PD O - O(L) - PAx: Setting is possible by 1bit unit 34 P16/D13 CHECK3 | | | | - | | | | | |
| 24 PORD6 Non (NC) O - OL - POx: Setting is possible by 1bit unit 25 POYID7 Non (NC) O - OL - POx: Setting is possible by 1bit unit 26 DVSS Power supply (43.3V) P P P P P Power supply (43.3V) 27 DVCC3A Power supply (43.3V) O - O(L) - POwer supply (43.3V) 28 P10D8 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 30 P12D10 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 31 P14D12 CHECK 1100k PD O - O(L) - POx: Setting is possible by 1bit unit 35 P17D15 CHECK 2100k PD O - O(L) - PAx: Setting is possible by 1bit unit 36 P40/A0 Non (NC) O - O(L) - PAx: Setting is possible by 1bit unit 37 <td></td> <td></td> <td></td> <td>-</td> <td></td> <td>• • •</td> <td></td> <td></td> <td></td> | | | | - | | • • • | | | |
| 25 DO7/D7 Non (NC) O - O(L) - PDx: Setting is possible by 1bit unit 26 DVSS Power supply (GND) P P P P P P Power supply (GND) 28 P10/D8 Non (NC) O - O(L) - PDv: Setting is possible by 1bit unit 30 P12/D10 Non (NC) O - O(L) - PDv: Setting is possible by 1bit unit 31 P13/D11 MODEL ID L; F107 H; 7 I - O(L) - PDv: Setting is possible by 1bit unit 32 P14/D12 CHECK I 100k PD O - O(L) - PDv: Setting is possible by 1bit unit 34 P16/D14 CHECK 3 100k PD O - O(L) - PAx: Setting is possible by 1bit unit 36 P40/A0 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 38 P43/A3 Non (NC) O - O(L) - P4x: Setting is possible by 1bit un | | | | - | | • • • | | | |
| 26 DVSS Power supply (ADD) P | | | | - | | · · / | | | |
| 27 DVCC3A Power supply (+3.3V) P P P P P Personal Power supply (+3.3V) 28 P10/D8 Non (NC) O - O(L) - PD P1x: Setting is possible by thit unit 30 P12/D10 Non (NC) O - O(L) - P0x: Setting is possible by thit unit 31 P13/D11 MODEL ID L; F107 H; 7 I - O(L) - P0x: Setting is possible by thit unit 32 P14/D12 CHECK IN 100k PD O - O(L) - P0x: Setting is possible by thit unit 33 P15/D13 CHECK 3 100k PD O - O(L) - P0x: Setting is possible by thit unit 36 P40/A0 Non (NC) O - O(L) - P4x: Setting is possible by thit unit 37 P41/A1 Non (NC) O - O(L) - P4x: Setting is possible by thit unit 38 P42/A2 Non (NC) O - O(L) - P | | | | - | | | | | |
| 28 P10/D8 Non (NC) O - O(L) - PD P1x: Setting is possible by thit unit 29 P11/D9 Non (NC) O - O(L) - P0x: Setting is possible by thit unit 31 P12/D10 Non (NC) O - O(L) - P0x: Setting is possible by thit unit 32 P14/D12 CHECK IN 100k PD O - O(L) - P0x: Setting is possible by thit unit 32 P14/D12 CHECK 1 100k PD O - O(L) - P0x: Setting is possible by thit unit 34 P16/D14 CHECK 2 100k PD O - O(L) - P0x: Setting is possible by thit unit 36 P47/D15 CHECK 2 100k PD O - O(L) - P4x: Setting is possible by thit unit 37 P47/D15 CHECK 2 100k PD O - O(L) - P4x: Setting is possible by thit unit 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is possible by | | | | | | | | | |
| 29 P1/L09 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 30 P12/D10 Non (NC) O - O(L) - POx: Setting is possible by 1bit unit 31 P13/D11 MODEL ID L; F107 H; 7 I - O(L) - POx: Setting is possible by 1bit unit 32 P14/D12 CHECK IN 100k PD O - O(L) - POx: Setting is possible by 1bit unit 34 P15/D13 CHECK 2 100k PD O - O(L) - POx: Setting is possible by 1bit unit 36 P40/AO Non (NC) O - O(L) - PAx: Setting is possible by 1bit unit 37 P41/A1 Non (NC) O - O(L) - PAx: Setting is possible by 1bit unit 38 P42/A2 Non (NC) O - O(L) - PAx: Setting is possible by 1bit unit 41 P43/A3 Non (NC) O - O(L) - PAx: Setting is possible by 1bit unit < | | | | - | | | | | |
| 30 P12/D10 Non (NC) O - O(L) - P0x : Setting is poss ble by 1bit unit 31 P13/D11 MODEL ID L ; F107 H ;7 I - O(L) - P0x : Setting is poss ble by 1bit unit 32 P14/D12 CHECK IN 100k PD O - O(L) - P0x : Setting is poss ble by 1bit unit 33 P15/D13 CHECK 2 100k PD O - O(L) - P0x : Setting is poss ble by 1bit unit 36 P40/A0 Non (NC) O - O(L) - P0x : Setting is poss ble by 1bit unit 37 P41/A1 Non (NC) O - O(L) - P4x : Setting is poss ble by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x : Setting is poss ble by 1bit unit 41 P44/A4 Non (NC) O - O(L) - P4x : Setting is poss ble by 1bit unit 41 P44/A5 Non (NC) O - O(L) - P4x : Setting is poss ble by 1bit unit < | | | | - | | • • • | | | |
| 31 P13/D11 MODEL ID L; F107 H;7 10AE I - O(L) - P0x: Setting is poss ble by 1bit unit 32 P14/D12 CHECK IN 100k PD O - O(L) - P0x: Setting is poss ble by 1bit unit 33 P15/D13 CHECK I 100k PD O - O(L) - P0x: Setting is poss ble by 1bit unit 34 P16/D14 CHECK 2 100k PD O - O(L) - P0x: Setting is poss ble by 1bit unit 36 P40/AO Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 37 P41/A1 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 39 P43/A3 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P4x: Setting | | | | - | | • • • | | | |
| 10AE Image: Constraint of the second se | | | | | | | | | |
| 33 P15/D13 CHECK 1 100k PD O - O(L) - P0x : Setting is possible by 1bit unit 34 P16/D14 CHECK 2 100k PD O - O(L) - P0x : Setting is possible by 1bit unit 36 P17/D15 CHECK 3 100k PD O - O(L) - P4x : Setting is possible by 1bit unit 37 P41/A1 Non (NC) O - O(L) - P4x : Setting is possible by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x : Setting is possible by 1bit unit 39 P43/A3 Non (NC) O - O(L) - P4x : Setting is possible by 1bit unit 40 P44/A4 Non (NC) O - O(L) - P4x : Setting is possible by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x : Setting is possible by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P6x : Setting is possible by 1bit unit | 31 | P13/D11 | | I | - | O(L) | - | - | P0x : Setting is poss ble by 1bit unit |
| 34 P16/D14 CHECK 2 100k PD 0 - O(L) - P0x: Setting is possible by 1bit unit 36 P47/D15 CHECK 3 100k PD 0 - O(L) - P0x: Setting is possible by 1bit unit 36 P40/A0 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 37 P41/A1 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 38 P42/A2 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 40 P44/A4 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 41 P46/A6 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 42 P46/A6 Non (NC) 0 - O(L) - P4x: Setting is possible by 1bit unit 43 P47/A7 Non (NC) 0 - O(L) - P5x: Setting is possible by 1bit unit <td>32</td> <td>P14/D12</td> <td>CHECK IN 100k PD</td> <td>0</td> <td>-</td> <td>O(L)</td> <td>-</td> <td>-</td> <td>P0x : Setting is poss ble by 1bit unit</td> | 32 | P14/D12 | CHECK IN 100k PD | 0 | - | O(L) | - | - | P0x : Setting is poss ble by 1bit unit |
| 35 P17/D15 CHECK 3 100k PD O - O(L) - P0x: Setting is possible by 1bit unit 36 P40/A0 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 37 P41/A1 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 39 P43/A3 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 40 P44/A4 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P4x: Setting is possible by 1bit unit 43 P47/A7 Non (NC) O - O(L) - P5x: Setting is possible by 1bit unit 44 | 33 | P15/D13 | CHECK 1 100k PD | 0 | - | O(L) | - | - | P0x : Setting is poss ble by 1bit unit |
| 36 P40/A0 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 37 P41/A1 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 40 P44/A4 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 44 DVCC3A Power supply (GND) P P P P P Power supply (GND) 45 | 34 | P16/D14 | CHECK 2 100k PD | 0 | - | O(L) | - | - | P0x : Setting is poss ble by 1bit unit |
| 37 P41/A1 Non (NC) O - O(L) - - P4x: Setting is poss ble by 1bit unit 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 39 P43/A3 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 40 P44/A4 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 44 DVCC3A Power supply (GND) P <td< td=""><td>35</td><td>P17/D15</td><td>CHECK 3 100k PD</td><td>0</td><td>-</td><td>O(L)</td><td>-</td><td>-</td><td>P0x : Setting is poss ble by 1bit unit</td></td<> | 35 | P17/D15 | CHECK 3 100k PD | 0 | - | O(L) | - | - | P0x : Setting is poss ble by 1bit unit |
| 38 P42/A2 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 39 P43/A3 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 40 P44/A4 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 41 P45/A5 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - P4x: Setting is poss ble by 1bit unit 44 DVSS Power supply (SND) P <td< td=""><td>36</td><td>P40/A0</td><td>Non (NC)</td><td>0</td><td>-</td><td>O(L)</td><td>-</td><td>-</td><td>P4x : Setting is poss ble by 1bit unit</td></td<> | 36 | P40/A0 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 39 P43/A3 Non (NC) 0 - O(L) - P4x : Setting is poss ble by 1bit unit 40 P44/A4 Non (NC) 0 - O(L) - P4x : Setting is poss ble by 1bit unit 41 P45/A5 Non (NC) 0 - O(L) - P4x : Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) 0 - O(L) - P4x : Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) 0 - O(L) - P4x : Setting is poss ble by 1bit unit 44 DVSC Power supply (GND) P P P P P Power supply (H33V) 45 DVCC3A Power supply (43.3V) O - O(L) - P5x : Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - | 37 | P41/A1 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 40P44/A4Non (NC)O-O(L)-P4x: Setting is poss ble by 1bit unit41P45/A5Non (NC)O-O(L)-P4x: Setting is poss ble by 1bit unit42P46/A6Non (NC)O-O(L)-P4x: Setting is poss ble by 1bit unit43P47/A7Non (NC)O-O(L)-P4x: Setting is poss ble by 1bit unit44DVSSPower supply (GND)PPPPP45DVCC3APower supply (H3.3V)PPPPP46P50/A8Non (NC)O-O(L)P5x: Setting is poss ble by 1bit unit47P51/A9Non (NC)O-O(L)P5x: Setting is poss ble by 1bit unit48P52/A10Non (NC)O-O(L)P5x: Setting is poss ble by 1bit unit49P53/A11Non (NC)O-O(L)P5x: Setting is poss ble by 1bit unit50P54/A12Non (NC)O-O(L)P5x: Setting is poss ble by 1bit unit51P55/A13ReservedI-I-PUP5x: Setting is poss ble by 1bit unit52P56/A14ReservedI-I-PUP5x: Setting is poss ble by 1bit unit54P60/A16Non (NC)O-O(L)-PEP6x: Setting is poss ble by 1bit unit54P61/A17Non | 38 | P42/A2 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 41 P45/A5 Non (NC) O - O(L) - - P4x: Setting is poss ble by 1bit unit 42 P46/A6 Non (NC) O - O(L) - - P4x: Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - - P4x: Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - - P4x: Setting is poss ble by 1bit unit 44 DVSS Power supply (GND) P P P P Power supply (5ND) 45 DVCC3A Power supply (43.3V) P P P P Power supply (43.3V) 46 P50/A8 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 48 P52/A13 Reserved I - I - PU P5x: Setting is po | 39 | P43/A3 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 42 P46/A6 Non (NC) O - O(L) - - P4x : Setting is poss ble by 1bit unit 43 P47/A7 Non (NC) O - O(L) - - P4x : Setting is poss ble by 1bit unit 44 DVSS Power supply (GND) P P P P Power supply (GND) 45 DVCC3A Power supply (+3.3V) P P P P Power supply (+3.3V) 46 P50/A8 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit | 40 | P44/A4 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 43 P47/A7 Non (NC) 0 - O(L) - - P4x : Setting is poss ble by 1bit unit 44 DVSS Power supply (SND) P P P P Power supply (SND) 45 DVCC3A Power supply (43.3V) P P P Power supply (43.3V) 46 P50/A8 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I PU P5x : Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 <td>41</td> <td>P45/A5</td> <td>Non (NC)</td> <td>0</td> <td>-</td> <td>O(L)</td> <td>-</td> <td>-</td> <td>P4x : Setting is poss ble by 1bit unit</td> | 41 | P45/A5 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 43 P47/A7 Non (NC) O - O(L) - - P4x : Setting is poss ble by 1bit unit 44 DVSS Power supply (GND) P P P P Power supply (SND) 45 DVCC3A Power supply (+3.3V) P P P P Power supply (+3.3V) 46 P50/A8 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I PU P5x : Setting is poss ble by 1bit unit 52 P5/A14 Reserved I - I PU P5x : Setting is poss ble by 1bit unit 54 | 42 | P46/A6 | Non (NC) | 0 | - | O(L) | - | - | P4x : Setting is poss ble by 1bit unit |
| 44 DVSS Power supply (GND) P | 43 | P47/A7 | Non (NC) | 0 | - | | - | - | P4x : Setting is poss ble by 1bit unit |
| 45 DVCC3A Power supply (+3.3V) P 53/311 <td>44</td> <td></td> <td></td> <td></td> <td>Р</td> <td></td> <td>Р</td> <td>-</td> <td></td> | 44 | | | | Р | | Р | - | |
| 46 P50/A8 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 47 P51/A9 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 49 P53/A11 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 52 P56/A14 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - P6x : Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - I - PU P6x : Setting is poss bl | 45 | | | Р | Р | Р | Р | - | |
| 47 P51/A9 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 48 P52/A10 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 49 P53/A11 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x: Setting is poss ble by 1bit unit 51 P55/A13 Reserved O - O(L) - P5x: Setting is poss ble by 1bit unit 52 P56/A14 Reserved O - O(L) - PU P5x: Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I - PU P5x: Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - PD P6x: Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - O(L) - PD P6x: Setting is poss ble by 1bit unit 57 P63/A19 BUCK O < | 46 | | | 0 | - | O(L) | - | - | |
| 48 P52/A10 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 49 P53/A11 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 52 P56/A14 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - PEx : Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - O(L) - PD P6x : Setting is poss ble by 1bit unit 56 P62/A18 CCE O - I - PU | | | | | - | | - | - | |
| 49 P53/A11 Non (NC) O - O(L) - - P5x : Setting is poss ble by 1bit unit 50 P54/A12 Non (NC) O - O(L) - - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 52 P56/A14 Reserved O - O(L) - PU P5x : Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - P6x : Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - O(L) - PD P6x : Setting is poss ble by 1bit unit 56 P62/A18 CCE O - I - PU P6x : Setting is poss ble by 1bit unit 57 P63/A19 BUCK O - I - PU P6x : Setting is poss ble by 1bit unit 58 | | | | | - | . , | | | |
| 50 P54/A12 Non (NC) O - O(L) - - P5x : Setting is poss ble by 1bit unit 51 P55/A13 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 52 P56/A14 Reserved O - O(L) - PU P5x : Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - PEx : Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - O(L) - PEx : Setting is poss ble by 1bit unit 56 P62/A18 CCE O - I - PU P6x : Setting is poss ble by 1bit unit 57 P63/A19 BUCK O - I - PU P6x : Setting is poss ble by 1bit unit 58 P64/A20 BUS1 I/O - I <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>· · · ·</td></td<> | | | | | | | | | · · · · |
| 51P55/A13ReservedIIIPUP5x : Setting is poss ble by 1bit unit52P56/A14ReservedO-O(L)-PUP5x : Setting is poss ble by 1bit unit53P57/A15ReservedI-I-PUP5x : Setting is poss ble by 1bit unit54P60/A16Non (NC)O-O(L)P6x : Setting is poss ble by 1bit unit55P61/A17Non (PD)O-O(L)-PDP6x : Setting is poss ble by 1bit unit56P62/A18CCEO-I-PUP6x : Setting is poss ble by 1bit unit57P63/A19BUCKO-I-PUP6x : Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (-Port only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O- | | | | | | | | | |
| 52 P56/A14 Reserved O - O(L) - PU P5x : Setting is poss ble by 1bit unit 53 P57/A15 Reserved I - I - PU P5x : Setting is poss ble by 1bit unit 54 P60/A16 Non (NC) O - O(L) - P6x : Setting is poss ble by 1bit unit 55 P61/A17 Non (PD) O - O(L) - P6x : Setting is poss ble by 1bit unit 56 P62/A18 CCE O - I - PU P6x : Setting is poss ble by 1bit unit 57 P63/A19 BUCK O - I - PU P6x : Setting is poss ble by 1bit unit 58 P64/A20 BUS0 I/O - I - PU P6x : Setting is poss ble by 1bit unit 59 P65/A21 BUS1 I/O - I - PU P6x : Setting is poss ble by 1bit unit 61 P67/A23 BUS2 I/O - I - <td></td> <td></td> <td>. ,</td> <td>1</td> <td></td> <td></td> <td></td> <td></td> <td></td> | | | . , | 1 | | | | | |
| 53P57/A15ReservedI-I-PUP5x: Setting is poss ble by 1bit unit54P60/A16Non (NC)O-O(L)-P6x: Setting is poss ble by 1bit unit55P61/A17Non (PD)O-O(L)-PDP6x: Setting is poss ble by 1bit unit56P62/A18CCEO-I-PUP6x: Setting is poss ble by 1bit unit57P63/A19BUCKO-I-PUP6x: Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x: Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x: Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x: Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x: Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)Port only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPort only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPort | | | | 0 | - | | - | | |
| 54P60/A16Non (NC)O-O(L)P6x : Setting is poss ble by 1bit unit55P61/A17Non (PD)O-O(L)-PDP6x : Setting is poss ble by 1bit unit56P62/A18CCEO-I-PUP6x : Setting is poss ble by 1bit unit57P63/A19BUCKO-I-PUP6x : Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)Pot only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPot only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPot only for input (Schmitt input and PU resistance) | | | | - 0 | | | | | |
| 55P61/A17Non (PD)O-O(L)-PDP6x : Setting is poss ble by 1bit unit56P62/A18CCEO-I-PUP6x : Setting is poss ble by 1bit unit57P63/A19BUCKO-I-PUP6x : Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)-Pot only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPort only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPort only for input (Schmitt input and PU resistance) | | | | | | | | | |
| 56P62/A18CCEO-I-PUP6x : Setting is poss ble by 1bit unit57P63/A19BUCKO-I-PUP6x : Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)-Port only for input (Schmitt input and PU resistance)65P71/SRWRNon (PD)O-O(L)-PDPort only for input (Schmitt input and PU resistance) | | | | | | | | | |
| 57P63/A19BUCKO-I-PUP6x : Setting is poss ble by 1bit unit58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)-Pot only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPort only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPDPD | | | | | - | U(L) | - | | |
| 58P64/A20BUS0I/O-I-PUP6x : Setting is poss ble by 1bit unit59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)Pot only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPot only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPDPD | | | | | - | | - | | |
| 59P65/A21BUS1I/O-I-PUP6x : Setting is poss ble by 1bit unit60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPP-Power supply (GND)63DVCC3APower supply (+3.3V)PPPP-Power supply (+3.3V)64P70/RDNon (NC)O-O(L)Pot only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPot only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPot only for input | | | | | - | | - | | |
| 60P66/A22BUS2I/O-I-PUP6x : Setting is poss ble by 1bit unit61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPPPPower supply (GND)63DVCC3APower supply (+3.3V)PPPPPPower supply (+3.3V)64P70/RDNon (NC)O-O(L)Port only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)-PDPort only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPort only for input (Schmitt input and PU resistance) | | | | | | | | | |
| 61P67/A23BUS3I/O-I-PUP6x : Setting is poss ble by 1bit unit62DVSSPower supply (GND)PPPP-Power supply (GND)63DVCC3APower supply (+3.3V)PPPP-Power supply (+3.3V)64P70/RDNon (NC)O-O(L)Port only for input (Schmitt input and PU resistance)65P71/SRWRNon (NC)O-O(L)Port only for input (Schmitt input and PU resistance)66P72/SRLLBNon (PD)O-O(L)-PDPort only for input (Schmitt input and PU resistance) | | | | | - | - | - | | |
| 62 DVSS Power supply (GND) P P P P P Power supply (GND) 63 DVCC3A Power supply (+3.3V) P P P P Power supply (+3.3V) 64 P70/RD Non (NC) O - O(L) - Port only for input (Schmitt input and PU resistance) 65 P71/SRWR Non (NC) O - O(L) - Port only for input (Schmitt input and PU resistance) 66 P72/SRLLB Non (PD) O - O(L) - PD | | | | | - | | - | | |
| 63 DVCC3A Power supply (+3.3V) P P P P P P 64 P70/RD Non (NC) O - O(L) - - Port only for input (Schmitt input and PU resistance) 65 P71/SRWR Non (NC) O - O(L) - - Port only for input (Schmitt input and PU resistance) 66 P72/SRLLB Non (PD) O - O(L) - PD PD | | | | | - | | - | | |
| 64 P70/RD Non (NC) O - O(L) - - Port only for input (Schmitt input and PU resistance) 65 P71/SRWR Non (NC) O - O(L) - - Port only for input (Schmitt input and PU resistance) 66 P72/SRLLB Non (PD) O - O(L) - PD Port only for input (Schmitt input and PU resistance) | | | | | | | | - | |
| 65 P71/SRWR Non (NC) O - O(L) - Port only for input (Schmitt input and PU resistance) 66 P72/SRLLB Non (PD) O - O(L) - PD | | | | | Р | | | - | |
| 65 P71/SRWR Non (NC) O - O(L) - - Port only for input (Schmitt input and PU resistance) 66 P72/SRLLB Non (PD) O - O(L) - PD Port only for input | 64 | P70/RD | Non (NC) | 0 | - | O(L) | - | - | |
| 66 P72/SRLLB Non (PD) O - O(L) - PD Port only for input | 65 | P71/SRWR | Non (NC) | 0 | - | O(L) | - | - | Port only for input |
| | 66 | P72/SRLLB | Non (PD) | 0 | - | O(L) | - | PD | Port only for input |

| Pin No | IC Terminal name | DCD-710AE/755SE Terminal name | I/O | RST | INIT | STB | Pull U/D | Port function |
|-----------|--------------------------------|---|-----|-----|----------|-----|-------------|--|
| 67 | P73/SRLUB | Non (NC) | 0 | - | O(L) | - | - | Port only for input |
| 68 | P74/TA0IN | RESET output for | 0 | - | O(H | - | - | (Schmitt input and PU resistance) I/O port (Schmitt input) |
| | | TC94A92FG | | |) | | | |
| 69 | P80/CS0/TA1OUT/ BOOT | Flash writing BOOT | 0 | I | O(H) | - | PU | Port only for input |
| 70 | P82/CS2 | Reserved | 0 | - | O(H) | - | - | Port only for input |
| 71 | P83/CS3/WAIT/ TA5OUT | TC94A92FG communication LRCK | 0 | - | O(H | - | - | I/O port |
| 72 | AM1 | Operational mode (PU) | I | - | 1 | - | PU | Port only for input (Fixed H) |
| 73 | X2 | Oscillator connection terminal (9.000MHz) | 0 | - | 0 | - | - | Output port |
| 74 | DVSS | Power supply (GND) | Р | Р | Р | Р | - | Power supply (GND) |
| 75 | X1 | Oscillator connection terminal (9.000MHz) | - | - | I | - | - | Input port |
| 76 | DVCC3A | Power supply (+3.3V) | Р | Р | Р | Р | - | Power supply (+3.3V) |
| 77 | P75/USBOC | USB OC (overcurrent detection Act L) input | Ι | - | Ι | - | - | I/O port (Schmitt input) |
| 78 | P76/USBPON | Non (NC) | 0 | - | O(H | - | - | I/O port (Schmitt input) |
| 79 | D+ | USB connection terminal | I/O | - | Ι/Ο | - | - | I/O port |
| 80 | D- | USB connection terminal | I/O | - | I/O | - | - | I/O port |
| 81 | AM0 | Operation mode (PU) | I | - | I | - | PU | Port only for input (Fixed H) |
| 82 | P77/X1USB | Non (PD) | 0 | - | O(H) | - | - | I/O port (Schmitt input) |
| 83 | DVSS | Power supply (GND) | Р | Р | P | Р | - | Power supply (GND) |
| 84 | PF0/TXD0 | Reserved | - | - | I | - | PD | I/O port (Schmitt input) |
| 85 | PF1/RXD0 | Reserved | - | - | Ι | - | PD | I/O port Schmitt input) |
| 86 | PF2/SCLK0/CTS0/ CLK/TB0OUT0 | Non (PD) | Ι | - | I | - | PD | I/O port (Schmitt input) |
| 87 | PF3/TXD1/SPDO | SYS μ -com communication line TXD/Flash writing TXD | 0 | - | 0 | - | PU | I/O port (Schmitt input) |
| 88 | PF4/RXD1/SPDI | SYS μ -com communication line TXD/Flash writing TXD | I | - | I | - | PU | I/O port |
| 89 | PF5/SCLK1/CTS1/ SPCLK | Co-PRO RESET | 0 | - | 0 | - | PU | I/O port |
| 90 | PN1/SDA0/TA3OUT | Co-PRO communication line I2C_SDA | I/O | - | I/O | - | PU | I/O port (Schmitt input, open drain) |
| 91 | PN2/SCL0/TA2IN | Co-PRO communication line I2C_SCL | 0 | - | 0 | - | PU | I/O port (Schmitt input, open drain) |
| 92 | PN3/HSCLK/SCK1 | TC94A92FG communication BCK | 0 | - | 0 | - | - | I/O port (Schmitt input) |
| 93 | PN4/HSSO/SDA1/ SO1 | TC94A92FG communication DATA | 0 | - | 0 | - | - | I/O port Schmitt input, open drain) |
| 94 | PN5/HSSI/SCL1/SI1 | TC94A92FG communication GATE | 0 | - | 0 | - | - | I/O port (Schmitt input, open drain) |
| 95 | DVCC3A | Power supply (+3.3V) | Р | Р | Р | Р | - | Power supply (+3.3V) |
| 96 | PG3/KI3 | Non (PD) | Ι | - | Ι | - | PD | Port only for input (Schmitt)/Key on W.UP |
| 97 | PG2/KI2 | Non (PD) | Ι | - | Ι | - | PD | Port only for input (Schmitt)/Key on W.UP |
| 98 | PG1/KI1 | Non (PD) | I | - | I | - | PD | Port only for input (Schmitt)/Key on W.UP |
| 99 | PG0/KI0 | TC94A92FG DREQ input | Ι | - | Ι | - | PD | Port only for input (Schmitt)/Key on W.UP |
| 100 | DVSS | Power supply (GND) | Р | Р | Р | Р | - | Power supply(GND) |



TC94A92FG Terminal Function

| Pin No. | Symbol | I/O | Description | Default | Remarks |
|------------|--------|------------|--|---------|--|
| 1 | VCoi | O 3AI/F | DSP VCO EFM and PLCK Phase difference signal output pin. (DSP VCO control voltage inputr pin.) | 0 | 3 state output |
| 2 | RVDD3 | | CD DSP Power supply for 3.3V RF amplifier core and PLL circuit | | |
| 3 | SLCo | O 3AI/F | EFM slice level output pin | 0 | Connect capacitor according with se rvo frequency band. |
| 4 | RFi | I 3AI/F | RF signal input pin | I | Selectable Zin 20/10 kΩ |
| 5 | RFRPi | I 3AI/F | RF ripple signal input pin | I | |
| 6 | RFEQo | O 3AI/F | RF equalizer circuit output pin. | 0 | Connect to RFRPi by 0.1uF, to RFi by 4700pF. |
| 7 | DCoFC | O 3AI/F | RFEQo offset compensation LPF output | 0 | Connect to Vro by more than 0.015uF |
| 8 | AGCi | I 3AI/F | RF signal AGC amplifier input pin | Ι | |
| 9 | RFo | O 3AI/F | RF signal generation amplifier output pin | 0 | |
| 10 | RVSS3 | | Grounding pin for 3.3 RF amplifier core and PLL circuit | | |
| 11 | FNI2 | I 3AI/F | Main beam signal input pin. To be connected to PIN diode C. | I | |
| 12 | FNI1 | I 3AI/F | Main beam signal input pin. To be connected to PIN diode A. | I | |
| 13 | FPI2 | I 3AI/F | Main beam signal input pin. To be connected to PIN diode D. | I | |
| 14 | FPI1 | I 3AI/F | Main beam signal input pin. To be connected to PIN diode B. | I | |
| 15 | TPi | I 3AI/F | Sub beam signal input pin. To be connected to PIN diode F. | I | |
| 16 | TNi | I 3AI/F | Sub beam signal input pin. To be connected to PIN diode E. | I | |
| 17 | VRo | O 3AI/F | 1.65 V reference voltage output pin. | 0 | Connected to PVREF, And connect to GNG by 0.1uF+100uF. |
| 18 | AVSS3 | | Grounding pin for 3.3V CD analog circuits. | | |
| 19 | MDi | I 3AI/F | Monitor photodiode amplifier input pin. | I | Reference Voltage=178mVtyp. |
| 20 | LDo | O 3AI/F | Laser diode amplifier output pin | 0 | |

| Pin No. | Symbol | I/O | Description | Default | Remarks |
|------------|---------------------------|-------------|---|---------|--|
| 21 | FSMoNiT | O 3AI/F | Focus Error signal / Sub beam add signal output pin(monitor pin/GND) | 0 | |
| 22 | RFZi | I 3AI/F | RF ripple zero cross signal Input pin | I | |
| 23 | RFRP | O 3AI/F | RF ripple signal output pin. | 0 | |
| 24 | ТЕі | O 3AI/F | Tracking error signal output pin. | 0 | Bulit in serises R=500Ω. Connect to VRo by capacitor. |
| 25 | AVDD3 | | Power supply pin for 3.3 V CD analog circuits. | | |
| 26 | FOo | O 3AI/F | Focus servo equalizer output pin. | 0 | Bulit in serises R=3.3 kΩ |
| 27 | TRo | O 3AI/F | Tracking servo equalizer output pin. | 0 | Bulit in output R=3.3 k Ω |
| 28 | VSS 3 | | Grounding pin for 1.5V Decoder DSP CD circuit | | |
| 29 | FMo | O 3AI/F | Feed servo equalizer output pin. | 0 | Bulit in output R=3.3 k Ω |
| 30 | FMoS | O 3AI/F | Feed servo equalizer output pin. (Stepper motor application) | ο | Bulit in output R=3.3 k Ω |
| 31 | DMo | O 3AI/F | Disc servo equalizer output pin | 0 | Bulit in output R=3.3 kΩ |
| 32 | VDD1 3 | I/O 3I/F | Power supply pin for 1.5V Decoder DSP /CD circuit | | |
| 33 | Pio8 | I/O 3I/F | Port 8(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 34 | Pio9 | I/O 3I/F | Port 9(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 35 | Pio10 | I/O 3I/F | Port 10(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 36 | Pio11 | I/O 3I/F | Port 11 (General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 37 | Pio12/ CDMoN0/ FGiN | I/O 3I/F | Port 12(General Input/Output Port) / CD Monitor 0 / FG signal input | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 38 | Pio13/ CDMoN1 | I/O 3I/F | Port 13(General Input/Output Port) / CD Monitor1 | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 39 | Pio14/ CDMoN2 | I/O 3I/F | Port 14(General Input/Output Port) / CD Monitor 2 | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 40 | CDMoN3 | O 3I/F | CD Monitor3 (Default output : SBSY) | 0 | CMOS Port Refer to [1.2 Pin Assinment Table] |

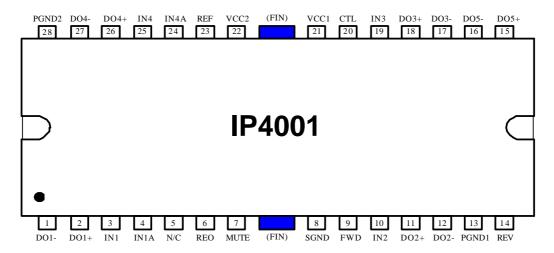
| Pin No. | Symbol | I/O | Description | Default | Remarks |
|------------|--------|-------------|---|---------|--|
| 41 | DVSS3R | | Grounding pin for 3.3V Muiti Bit DAC circuit | | |
| 42 | Ro | O 3AI/F | R channel audio output pin of Audio DAC. | 0 | |
| 43 | DVDD3R | | Power supply pin for 3.3V Audio DAC circuit. | | |
| 44 | DVDD3L | | Power supply pin for 3.3V Audio DAC circuit. | | |
| 45 | Lo | O 3AI/F | L channel audio output pin of Audio DAC | 0 | |
| 46 | DVSS3L | | Grounding pin for 3.3V Muiti Bit DAC Circuit | | |
| 47 | XVSS3 | | Grounding pin for 3.3V clock oscillator circuit | | |
| 48 | Xi | I 3AI/F | System clock Input pin | I | Xtal oscillation circuit. Connect feedback resistor 1 |
| 49 | Хо | O 3AI/F | System clock Output pin | 0 | $M\Omega$ between Xo and Xi |
| 50 | XVDD3 | | Power Supply pin for 3.3V clock oscillator circuit | | |
| 51 | VDD1 2 | | Power Supply pin for 1.5V Digital circuit | | |
| 52 | VSS 2 | | Grounding pin for 1.5V digital circuit | | |
| 53 | Pio0 | I/O 3I/F | Port 0(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 54 | Pio1 | I/O 3I/F | Port 1(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 55 | Pio2 | I/O 3I/F | Port 2(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 56 | Pio3 | I/O 3I/F | Port 3(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 57 | Pio4 | I/O 3I/F | Port 4(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 58 | Pio5 | I/O 3I/F | Port 5(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 59 | Pio6 | I/O 3I/F | Port 6(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 60 | Pio7 | I/O 3I/F | Port 7(General Input/Output Port) | I | CMOS Port Schmitt input Refer to [1 2 Pin Assinment Table] |

| Pin No. | Symbol | I/O | Description | Default | Remarks |
|------------|----------|-------------|--|---------|--|
| 61 | VDD3 | | Power Supply pin for 3.3V Digital circuit | | |
| 62 | BUS0 | I/O 3I/F | Microprocessor I/F data input/output pin 0 | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 63 | BUS1 | I/O 3I/F | Microprocessor I/F data input/output pin 1 | Ι | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 64 | BUS2 | I/O 3I/F | Microprocessor I/F data input/output pin 2 | I | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 65 | BUS3 | I/O 3I/F | Microprocessor I/F data input/output pin 3 | Ι | CMOS Port Schmitt input Refer to [1.2 Pin Assinment Table] |
| 66 | BUCK | I 3I/F | Microprocessor I/F BUS clock Input pin | I | Schmitt input Refer to [1.2 Pin Assinment Table] |
| 67 | /CCE | l 3I/F | Microprocessor I/F chip enable input pin | I | Schmitt input Refer to [1.2 Pin Assinment Table] |
| 68 | MS | l 3I/F | Microprocessor I/F mode selection pin. "H": Parallel I/F, "L": Serial I/F | I | Refer to [1.2 Pin Assinment Table] |
| 69 | /RST | I 3I/F | Reset Input pin | I | Schmitt input |
| 70 | Test | I 3I/F | Test pin("L" fixed) | I | Connect to GND for normal operation |
| 71 | VDD1 1 | | Power Supply pin for 1.5V Digital circuit | | |
| 72 | VSS 1 | | Grounding pin for 1.5V Digital circuit | | |
| 73 | /SRAMSTB | l 3I/F | 1Mbit SRAM stand by pin(/SRAMSTB="L") | I | |
| 74 | VDDM1 | | Power Supply for 1.5V 1Mbit SRAM circuit | | |
| 75 | PDo | O 3AI/F | EFM and PLCK Phase difference signal output pin. | ο | 4 state output (RVDD3, RVSS3,PVREF, Hiz) |
| 76 | ТМАХ | O 3AI/F | TMAX detection result output pin | 0 | 3 state output (RVDD3, RVSS3, Hiz) |
| 77 | LPFN | I 3AI/F | PLL circuit LPF amplifier inversion input pin | I | |
| 78 | LPFo | O 3AI/F | PLL circuit LPF amplifier Output pin | 0 | |
| 79 | PVREF | | PLL circuit 1.65 V reference voltage pin. | | Connected to VRO. Connect to GND by 0.1uF and 100uF. |
| 80 | VCoF | O 3AI/F | VCO filter pin | 0 | Connect to GND by 0.01uF |

3A I/F : 3 V analog circuit input/output pin.

1.5 I/F : 1.5Vdigital input/output pin.

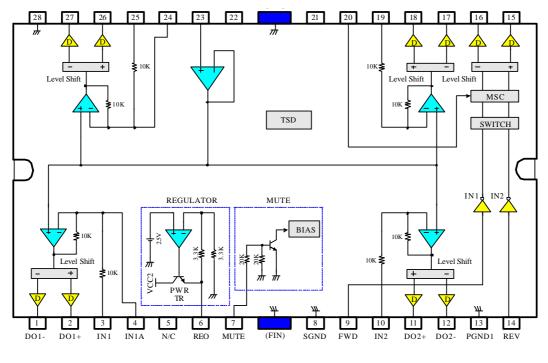
3 I/F : 3 V digital input/output pin.



Pin Discriptions

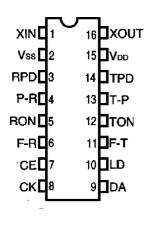
| NO | SYMBOL | I/O | DESCRIPTION | NO | SYMBOL | I/O | DESCRIPTION |
|----|--------|-----|---------------------|----|--------|-----|--|
| 1 | DO1- | 0 | CH1 OUTPUT (-) | 15 | DO5+ | 0 | CH5 OUTPUT (+) |
| 2 | DO1+ | 0 | CH1 OUTPUT (+) | 16 | DO5- | 0 | CH5 OUTPUT (-) |
| 3 | IN1 | I | CH1 INPUT 1 | 17 | DO3- | 0 | CH3 OUTPUT (-) |
| 4 | IN1A | I | CH1 INPUT 2 | 18 | DO3+ | 0 | CH3 OUTPUT (+) |
| 5 | N / C | - | NO-CONNECTION | 19 | IN3 | I | CH3 INPUT |
| 6 | REO | 0 | REGULATOR OUTPUT | 20 | CTL | I | CH5 MOTOR SPEED CONTROL |
| 7 | MUTE | I | MUTE INPUT | 21 | VCC1 | I | SUPPLY VOLTAGE 1 (CH2,CH3,CH5) |
| 8 | SGND | - | SIGNAL GROUND | 22 | VCC2 | I | SUPPLY VOLTAGE 2 (CH1,CH4,SIGNAL,REG) |
| 9 | FWD | I | CH5 INPUT 1 | 23 | REF | I | CH BIAS INPUT |
| 10 | IN2 | I | CH2 INPUT | 24 | IN4A | I | CH4 INPUT 1 |
| 11 | DO2+ | 0 | CH2 OUTPUT (+) | 25 | IN4 | I | CH4 INPUT 2 |
| 12 | DO2- | 0 | CH2 OUTPUT (-) | 26 | DO4+ | 0 | CH4 OUTPUT (+) |
| 13 | PGND1 | - | POWER GROUND 1 | 27 | DO4- | 0 | CH4 OUTPUT (-) |
| 14 | REV | I | CH5 INPUT 2 | 28 | PGND2 | - | POWER GROUND 2 |

Block Diagram

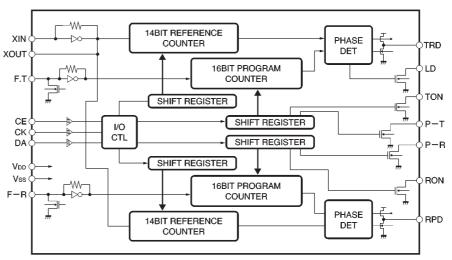


48 DCD-710AE

BU2630FV (IC36)

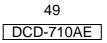


Block Diagram

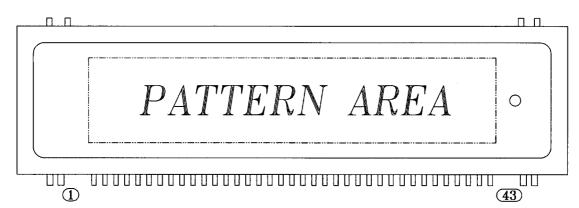


Pin Discriptions

| Pin No. | Pin name | Name | Function | I/O cuircuit |
|---------|----------|-------------------------|--|--------------|
| 16 | XOUT | Orantalana anatan | | |
| 1 | XIN | Crystal resonator | For reference frequency | TYPE A |
| 2 | Vss | | | |
| 3 | RPD | Phase comparator output | This is LO if the locally divided value is higher than the reference frequency, HI if it is lower, and Z if it matches. | TYPE E |
| 4 | P-R | Output port | This is controlled by the input data | TYPE D |
| 5 | RON | Output port | This is controlled by the input data. | TYPED |
| 6 | F-R | VCO input | Local input for reception | TYPE F |
| 7 | CE | Chip enable | | |
| 8 | СК | clock signal | When CE is HIGH, the DA synchronized to the rise of CK is read into the internal shift register, and is latched at the timing of the CE fall. | TYPE B |
| 9 | DA | serial data | | |
| 10 | LD | Unlock output | This goes ON when the PLL is unlocked on the transmission side | TYPE D |
| 11 | F-T | VCO input | Local input for transmission | TYPE F |
| 12 | TON | A | | 7055 |
| 13 | P-T | Output port | This is controlled by the input data | TYPE D |
| 14 | TPD | Phase comparator output | This is LO if the locally divided value is higher than the reference frequency, HI if it is lower, and Z if it matches. | TYPE E |
| 15 | VDD | Power supply | 2.5~5.5V | |



2. FL DISPLAY VFD (HCA-18MS03T) (F701)

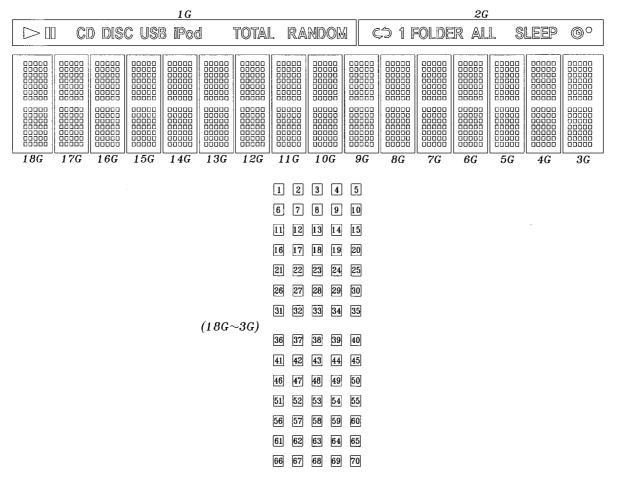


PIN CONNECTION

| PIN NO. | 1 | 2 | 3 | 4~14 | 15~29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 | 40 | 41 | 42 | 43 |
|------------|----|----|----|------|-------|------|----|----|----|--------------------------|-------|------|-----|-------|-------|-------|----|----|----|
| CONNECTION | F1 | NP | NP | NC | NX | TEST | DO | DA | CP | $\overline{\mathrm{CS}}$ | RESET | 0SC0 | VDD | VDISP | D-GND | L-GND | NP | NP | F2 |

a) Fn : Filament pin
2) NP : No pin
3) NX : No extended pin
4) NC : No connection. NC pin should be electrically open on the PC board.

GRID ASSIGNMENT



ANODE CONNECTION

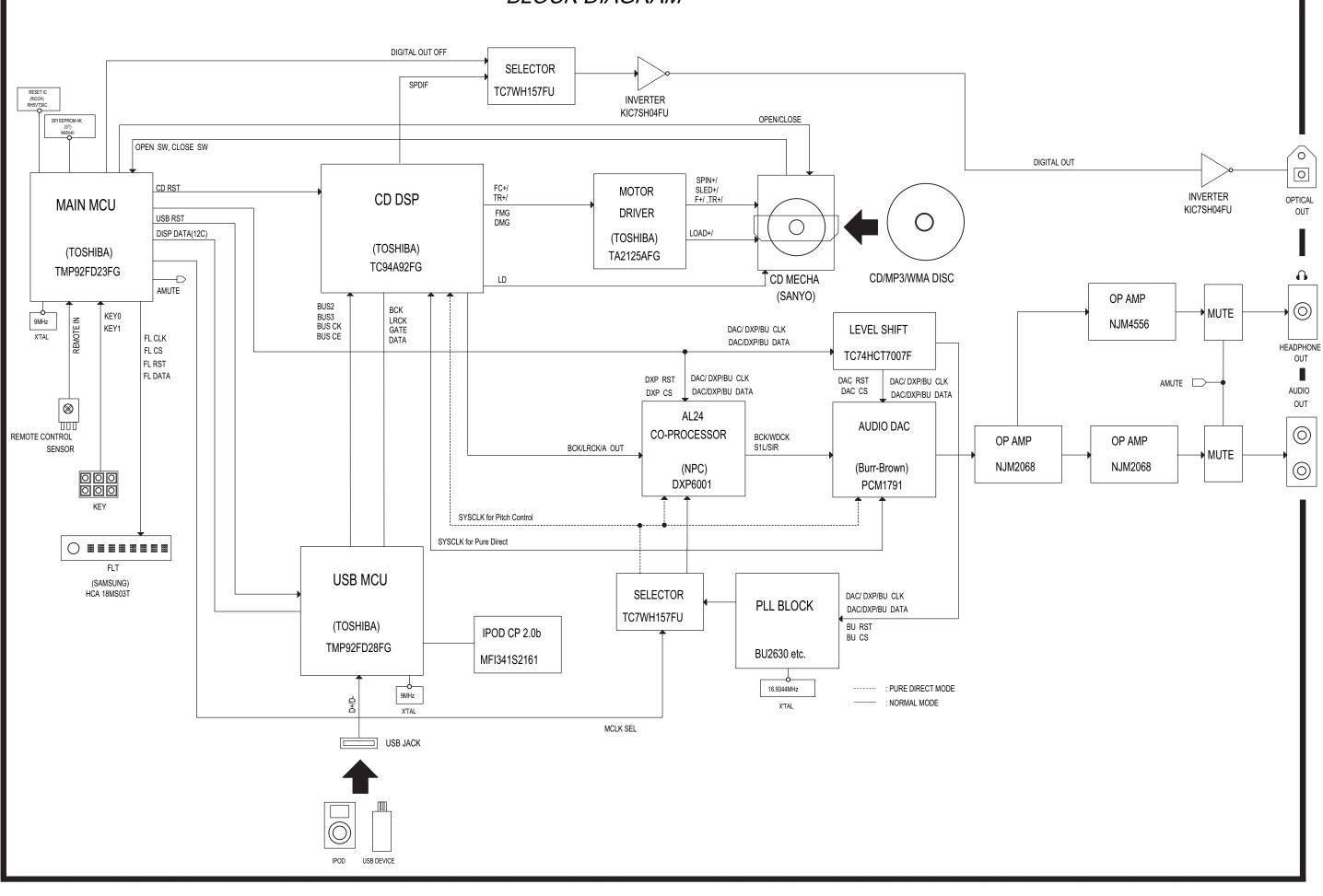
| | СОМ18СОМЗ | COM2 | COM1 |
|---------|-----------|--------|------------------|
| | 18G3G | 2G | 1 G |
| SEGB 1 | 1 | 0 | RANDOM |
| SEGB 2 | 2 | G | TOTAL |
| SEGB 3 | 3 | SLEEP | iPod |
| SEGB 4 | 4 | ALL | USB |
| SEGB 5 | 5 | FOLDER | DISC |
| SEGB 6 | 6 | 1 | CD |
| SEGB 7 | 7 | Ć | |
| SEGB 8 | 8 | | \triangleright |
| SEGB 9 | 9 | | |
| SEGB10 | 10 | | |
| SEGB11 | 11 | | |
| SEGB12 | 12 | | |
| SEGB13 | 13 | | |
| SEGB14 | 14 | | |
| SEGB15 | | | |
| SEGB16 | | | |
| SEGB17 | 17 | | |
| SEGB18 | 18 | | |
| SEGB19 | 19 | | |
| SEGB20 | 20 | | |
| SEGB 21 | 21 | | |
| SEGB22 | 22 | | |
| SEGB23 | 23 | | |
| SEGB24 | 24 | | |
| SEGB25 | 25 | | |
| SEGB26 | 26 | | |
| SEGB27 | 27 | | |
| SEGB28 | 28 | | |
| SEGB29 | 29 | | |
| SEGB30 | 30 | | |
| SEGB 31 | 31 | | |
| SEGB 32 | 32 | | |
| SEGB 33 | 33 | | |
| SEGB34 | 34 | | |
| SEGB35 | 35 | | |

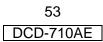
| | СОМ18СОМ3 | COM2 | COM1 |
|---------|-----------|------|------|
| | 18G3G | 2G | 1 G |
| SEGA 1 | 36 | | |
| SEGA 2 | 37 | | |
| SEGA 3 | 38 | | |
| SEGA 4 | 39 | | |
| SEGA 5 | 40 | | |
| SEGA 6 | 41 | | |
| SEGA 7 | 42 | | |
| SEGA 8 | 43 | | |
| SEGA 9 | 44 | | |
| SEGA10 | 45 | | |
| SEGA11 | 46 | | |
| SEGA12 | 47 | | |
| SEGA13 | 48 | | |
| SEGA14 | 49 | | |
| SEGA15 | 50 | | |
| SEGA16 | 51 | | |
| SEGA17 | 52 | | |
| SEGA18 | 53 | | |
| SEGA19 | 54 | | |
| SEGA 20 | 55 | | |
| SEGA 21 | 56 | | |
| SEGA 22 | 57 | | |
| SEGA 23 | 58 | | |
| SEGA 24 | 59 | | |
| SEGA 25 | 60 | | |
| SEGA 26 | 61 | | |
| SEGA 27 | 62 | | |
| SEGA 28 | 63 | | |
| SEGA 29 | 64 | | |
| SEGA 30 | 65 | | |
| SEGA 31 | 66 | | |
| SEGA 32 | 67 | | |
| SEGA 33 | 68 | | |
| SEGA 34 | 69 | | |
| SEGA 35 | 70 | | |

--MEMO--

BLOCK DIAGRAM

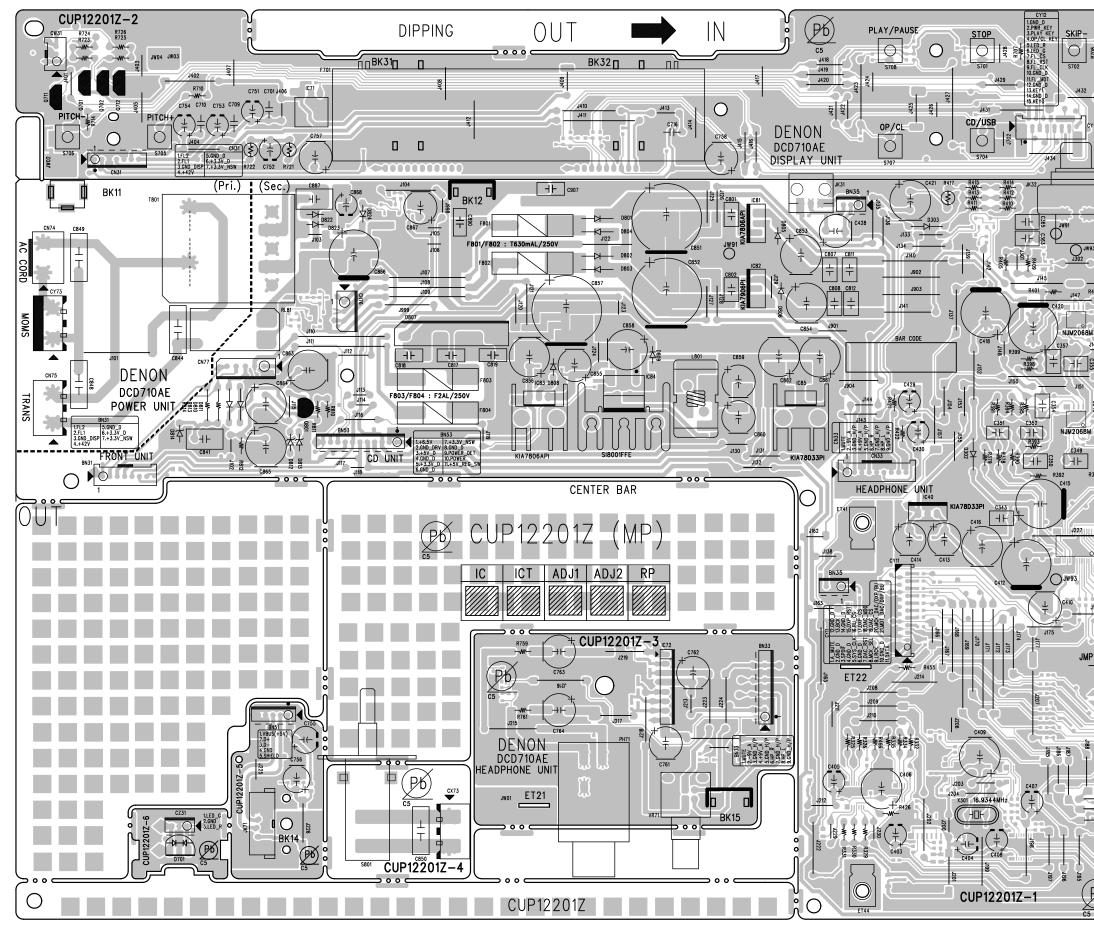
BLOCK DIAGRAM



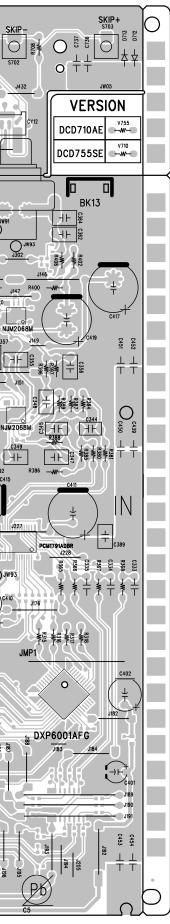


PRINTED WIRING BOARDS

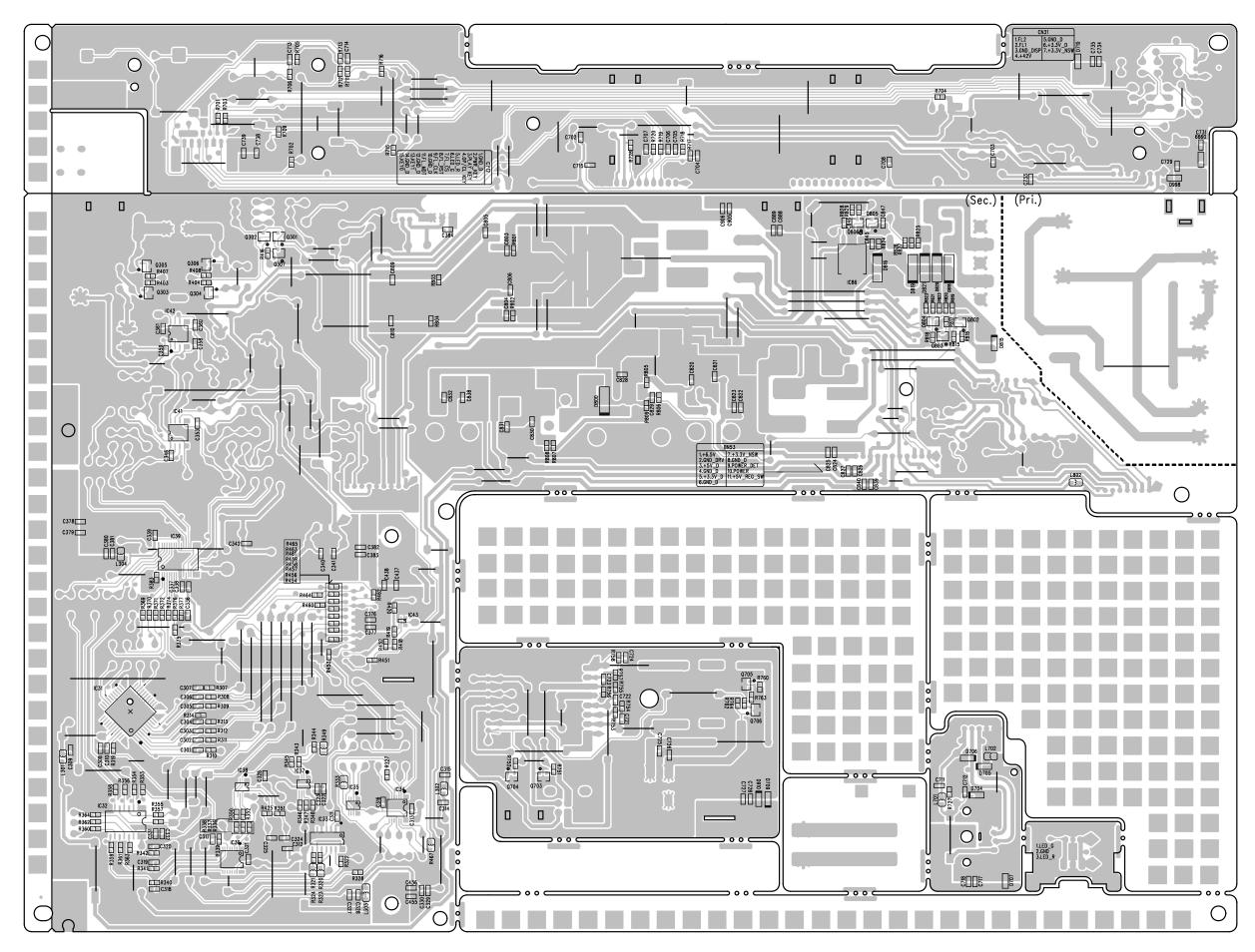
AUDIO/POWER PCB ASSY (1/2)



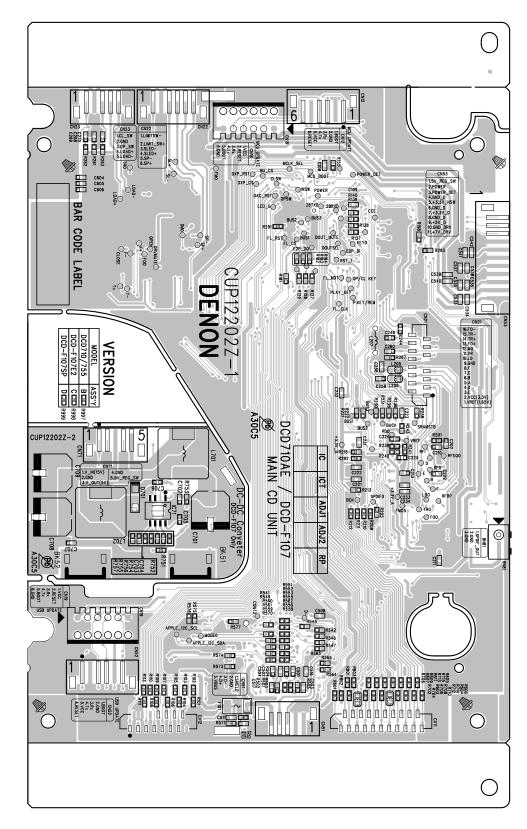
54 DCD-710AE



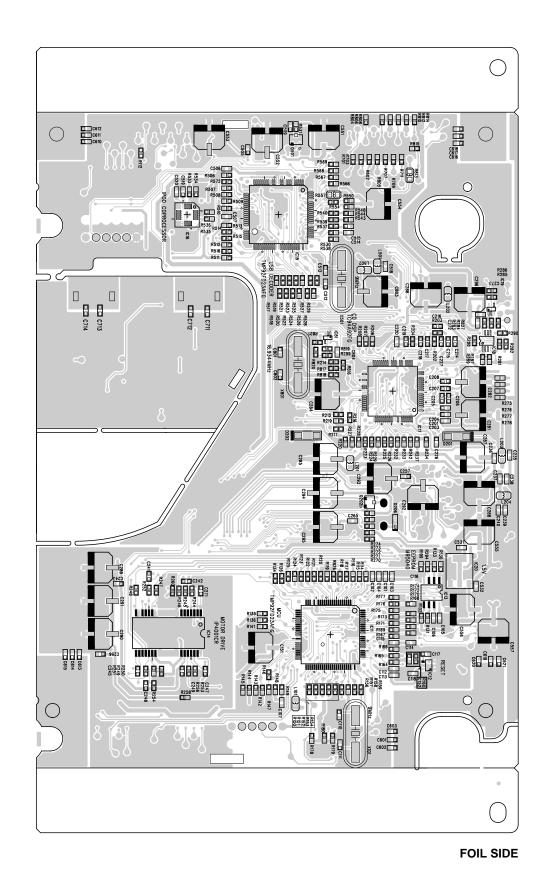
COMPONENT SIDE

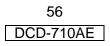


55 DCD-710AE FOIL SIDE



COMPONENT SIDE





NOTE FOR PARTS LIST

- 1. Parts for which "nsp" is indicated on this table cannot be supplied.
- 2. When ordering of part, clearly indicate "1" and "I" (i) to avoid mis-supplying.
- 3. Ordering part without stating its part number can not be supplied.
- 4. Part indicated with the mark "*" is not illustrated in the exploded view.
- 5. Not including General-purpose Carbon Film Resistor in the P.W.Board parts list. (Refer to the Schematic Diagram for those parts.)
- 6. Not including General-purpose Carbon Chip Resistor in the P.W.Board parts list. (Refer to the Schematic Diagram for those parts.)

WARNING:

Parts marked with this symbol \triangle have critical characteristics.

Use ONLY replacement parts recommended by the manufacturer.

Resistors

| E | ĸ.: | RN | 14 | < | 28 | Ξ | _ | 18 | 2 | _ | G | _ | FR | |
|---|------|---------------|-----------------------|------|-------|-----|---|------------|-----------|------------|-----------------|----|--------------|---------|
| | | 1 | Shap and j form | | Po | wer | | les nce | ist- e | All err | owab ror | le | Others | |
| | RD: | Carbon | | 2B : | 1/8 \ | w | F | : | ±1% | | P : | Ρ | ulse-resista | nt type |
| | RC: | Composition | | 2E : | 1/4 \ | w | G | : | ±2% | | NL : | L | ow noise typ | be |
| | RS : | Metal oxide f | film | 2H : | 1/2 \ | w | J | : | $\pm 5\%$ | | NB : | Ν | on-burning | type |
| | RW: | winding | | 3A : | 1 V | v | Κ | : | ±10% | % | FR : | F | use-resistor | · |
| | RN: | Metal film | | 3D : | 2 V | v | Μ | : | ±20% | % | F : | Le | ead wire for | ming |
| | RK : | Metal mixture | e | 3F : | 3 V | v | | | | | | | | |
| | | | | 3H : | 5 V | v | | | | | | | | |

* Resistance

182 1800ohm=1.8kohm \Rightarrow

Indicates number of zeros after effective number.

2-digit effective number.

 \Rightarrow 1.2ohm

1-digit effective number.

2-digit effective number, decimal point indicated by R.

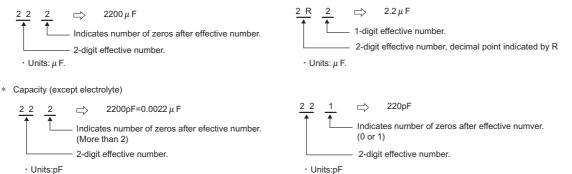
: Units: ohm

1 R 2

Capacitors

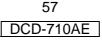
| Ex.: | | CE | 04W | <u>1H</u> | 3R | 2 | _ | M | BP | |
|------|------------|--------------------------|-------------------------------|-----------------------------|------------|--------|------|----------------|--------------|---|
| | | Type ↓ | Shape and per- formance | Dielectric strength ↓ | Сара | acity | Allo | owable or | Others | |
| C | CE : | Aluminum electrolytic | | 0J : 6.3 1A : 10 | | F G | : ± | 1% 2% | HS : BP : | High stability type Non-polar type |
| C | CA : | Aluminium | | 1C: 16 1E: 25 | V | J | : ± | - / • | HR : | Ripple-resistant type For change and discharge |
| | | Tantalum e | | 1V : 35 | V | М | : ± | 20% | HF : | For assuring high requency |
| C | CK : | Film Ceramic | | 1H : 50 2A : 10 | 0 V | _ | - 2 | 80% 20% | U : C : | UL part CSA part |
| | C: P: | Ceramic Oil | | 2B : 12 2C : 16 | | P C | | 100% 0.25pF | W : F : | UL-CSA part Lead wire forming |
| | CM: CF: | Mica Metallized | | 2D: 20 2E: 25 | • • | D = | | 0.5pF thers | | |
| C | CH : | Metallized | | 2H: 50 2J: 63 | 0 V 0 V | | | | | |

Capacity (electrolyte only)



Units:pF

· When the dielectric strength is indicated in AC,"AC" is included after the dieelectric strength value.



PARTS LIST OF P.W.B. UNIT

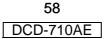
 $\ast\,$ Parts for which "nsp" is indicated on this table cannot be supplied.

* The parts listed below are for maintenance only, might differ from the parts used in the unit in appearances or dimensions.

Note: The symbols in the column "Remarks" indicate he following destinations. E2 : Europe model E1C : China model

AUDIO/POWER PCB ASSY

| Γ | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|----------|----------------------|---------------|------------------------------------|--------|---------------------------|------|-----|
| SEN | ICONDUCTORS | GROUP | | 1 | | | |
| | IC31 | 00D2623629002 | IC AL24 PROCESSING DSP | | CVI00D2623629002-DM | | |
| | IC32 | 00D2622376903 | IC | | HVITC74HCT7007F | | |
| | IC33 | 90M-HC700560R | IC VCO | | HVITC74LS628 | | |
| | IC34 | 00D2623077900 | IC HEX INVERTER | | HVITC74VHCU04FT | | |
| | IC35 | 00D2623601907 | IC 2CH MULTIPLXER | | HVITC7WH157FU | | |
| | IC36 | 00MHC1022521Z | IC DUAL PLL SYNTHESIZER | | BVIBU2630FV-E2 | | |
| | IC37 | 00D2623199901 | IC D FLIP FLOP | | CVITC7WH74FU | | |
| | IC38 | 00D2623489909 | IC INVERTER(CMOS) | | CVITC7WHU04FU | | |
| | IC39 | | IC D/A CONVERTER | | HVIPCM1791ADBR | | |
| | IC40 | 00D2631243001 | I.C REGULATOR | | CVIKIA78D33PI | | |
| | IC41,42 | 00D2630896909 | IC OP AMP | | HVINJM2068MDTE1 | | |
| | IC43 | 943239006810S | IC SIGNAL INVERTER (USV) | | CVIKIC7SH04FU | | |
| | IC71 | | REMOCON SENSOR | | CRVKSM603TH2E | | |
| | IC72 | | I.C HEADPHONE | | HVINJM4556AL | | |
| | IC81 | | I.C REGULATOR +6V | | HVIKIA7806API | | |
| | IC82 | | I.C -6V REGULATOR (TO220IS) | | CVIKIA7906PI | | |
| | IC83 | | I.C REGULATOR +6V | | HVIKIA7806API | | |
| | IC84 | 00D2631285001 | | | CVISI8001FFE | | |
| | IC85 | 00D2631263001 | | | CVIKIA78D33PI | | |
| | IC86 | | IC REGULATOR (3.3V SMD) | | BVIBA33BC0FP | | |
| | 1000 | 0002022944940 | IC RECOLATOR (3.57 SIND) | | DVIDASSDC011 | | |
| | Q301,302 | 00D2730477901 | | | HVTKRA107S | | |
| | Q301,302 Q303,304 | 00D2730460905 | | | HVTKTC2875B | | |
| | Q303,304 Q307 | 00D2730480905 | | | HVTKRC107S | | |
| | Q307 Q701,702 | 00D9630121808 | | | HVTKRA102S | | |
| | Q701,702 Q703-706 | | | | | | |
| | | 00D2690192902 | CHIP TR (MUTE) | | HVTKTD1304T HVTKRC102S | | |
| A | Q711,712 Q801 | 943219006820S | TR | | CVTKTC1027YT | | |
| | Q801 Q802 | 00D2730464901 | | | HVTKTC3875SYRTK | | |
| | Q802 Q805,806 | 00D2730464901 | | | HVTKTC3875SYRTK | | |
| | 0000,000 | 00D2730404901 | | | | | |
| | D302,303 | 00D9430182502 | | | CVD1N4003T | | |
| | D302,303 D701 | 00D9430182302 | | | HVDSML1216W | | |
| | - | | DIODE 1SS355T | | | | |
| | D704-706 | | | | HVD1SS355T | | |
| | D707 | | DIODE ESD PROTECTION USC | | CVDPG05GBUSCRTKP | | |
| | D708 | | DIODE 1SS355T | | HVD1SS355T | | |
| | D709 | | DIODE ESD PROTECTION USC | | CVDPG05GBUSCRTKP | | |
| | D710 | | DIODE 1SS355T | | HVD1SS355T | | |
| | D712 | | DIODE 1SS355T | | HVD1SS355T | | |
| | D713 | | | | HVD1SS355T | | |
| | D800 | | DIODE, SCHOTTKY (40V,3A, DO-214AC) | | CVDSS34SR | | |
| | D801-804 | 00D9430182502 | | | CVD1N4003T | | |
| | D805,806 | | DIODE 1SS133T-77 | | HVD1SS133MT | | |
| | D807 | 90M-HE200390R | | | HVDGBJ1006 | | |
| | D808,809 | | DIODE 1SS133T-77 | | HVD1SS133MT | | |
| | D810,811 | 00D9430182502 | | | CVD1N4003T | | |
| | D812 | | DIODE MTZJ20B 1/2W | | HVDMTZJ20BT | | |
| | D813 | | DIODE MTZJ27B 1/2W | | HVDMTZJ27BT | | |
| | D814 | | DIODE MTZJ3.6B 1/2W | | HVDMTZJ3.6BT | | |
| | D815 | | DIODE 1SS355T | | HVD1SS355T | | |
| | D816 | 943204006850S | DIODE , SCHOTTKEY BARRIER HK | | HVDRB160L60TE25 | | |
| | D818-821 | 943204006850S | DIODE, SCHOTTKEY BARRIER HK | | HVDRB160L60TE25 | | |



| | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|-----|---------------|---------------|--------------------------------|--------|------------------|------|-----|
| | D822-824 | 00D9430086404 | DIODE 1SS133T-77 | | HVD1SS133MT | | |
| | D910 | 00D2760717903 | DIODE 1SS355T | | HVD1SS355T | | |
| | D998,999 | 943209006830S | DIODE , ESD PROTECTION USC | | CVDPG05GBUSCRTKP | | |
| | | | | | | | |
| RES | SISTORS GROUI | | | | | | |
| | R417 | | METAL OXIDE RES 100 ohm 1W J | | KRG1SANJ101RT | | |
| | R721,722 | 00D9430092906 | FUSE RES 10 ohm (5%) | | KRQ12AJ100RT | | |
| | R808 | nsp | CHIP RES 1% | | CRJ10DF2002T | | |
| | R809 | nsp | CHIP RES 1% 820 ohm | | CRJ10DF8200T | | |
| | VR71 | 00D9430196908 | VARIABLE RES | | CVV2J02B103Z | | |
| CAF | ACITORS GRO | JP | | | | | |
| | C301-303 | nsp | CHIP CAP | | CCUS1H101JA | | |
| | C304 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C308 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C309 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C310 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C311 | nsp | CHIP CAP | | CCUS1H220JA | | |
| | C312-316 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C317 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C321 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C322 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C323 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C324,325 | nsp | CHIP CAP | | CCUS1H100JA | | |
| | C326 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C327 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C328,329 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C330,331 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C332 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C336,337 | | CHIP CAP | | CCUS1H104KC | | |
| | | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C338 | nsp | | | | | |
| | C339 | nsp | | | CCUS1H104KC | | |
| | C340-342 | nsp | | | CCUS1H103KC | | |
| | C343 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | | |
| | C344 | nsp | MYLAR CAP | | HCQI1H182JZT | | |
| | C345 | nsp | MYLAR CAP | | HCQI1H272JZT | | |
| | C347,348 | nsp | PP CAP (100V/680pF) | | CCMP2A681JN09T | | |
| | C349 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | | |
| | C350 | nsp | PP CAP (100V/680pF) | | CCMP2A681JN09T | | |
| | C351 | nsp | MYLAR CAP | | HCQI1H182JZT | | |
| | C352 | nsp | MYLAR CAP | | HCQI1H272JZT | | |
| | C354 | nsp | PP CAP (100V/680pF) | | CCMP2A681JN09T | | |
| | C355 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | | |
| | C356,357 | nsp | PP CAP (100V/2700pF) | | CCMP2A272JN09T | | |
| | C358 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C361 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C376 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C377 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C378 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C379 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C380 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C381 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C382,383 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C384 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C389 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | | |
| | C401 | • | ELECT CAP (ELNA RFO 100uF/25V) | | CCEA1ERFO101T | | |
| | C402 | | ELEC CAP ELNA RFO 100uF/50V | | CCEA1HRFO101T | t | t |
| | C403-405 | nsp | ELECT CAP | | CCEA1CH101T | | |
| | C406 | | EIECT CAP (220uF/16V, ROA) | | CCEA1CROA221E | | |
| | | | ELECT CAP | 1 | CCEA1HH1R0T | 1 | 1 |

| | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|---|------------------|--------------------------------|-------------------------------------|--------|--------------------------------|------|-----|
| | C408 | nsp | ELECT CAP | | CCEA1CH101T | | |
| - | C409 | 00MOA227016R0 | EIECT CAP (220uF/16V, ROA) | | CCEA1CROA221E | | |
| | C410 | 00D2544693939 | ELEC CAP ELNA RFO 100uF/50V | | CCEA1HRFO101T | | |
| | C411 | 00D2544750704 | ELECT CAP (470uF/50V, RFO, 12.5X20) | | CCEA1HRFO471E | | |
| | C412 | | ELECT CAP (47uF/50V, ROA, 10X16) | | CCEA1HROA470T | | |
| | C413,414 | | ELEC CAP ELNA RFO 100uF/50V | | CCEA1HRFO101T | | |
| | C415 | | ELECT CAP (ELNA ROA 50V/100uF) | | CCEA1HROA101E | | |
| | C416 | | ELECT CAP (470uF/6.3V,RFO) | | CCEA0JRFO471T | | |
| | C417,418 | | ELECT CAP (47uF/50V, ROA, 10X16) | | CCEA1HROA470T | | |
| | C419,420 | | ELECT CAP (ELNA ROA 50V/100uF) | | CCEA1HROA101E | | |
| | C419,420 C421 | 943134005040S | ELECT CAP (ELNA RFOII 50V/220uF) | | CCEA1HRFOII221E | | |
| | C421 C428 | 00D2544693939 | ELEC CAP ELNA RFO SERIES 100uF/50V | | CCEA1HRF0101T | | |
| | C428 C429,430 | 00D2544693939 00D2544693926 | | | CCEA1HRF01011 CCEA1HRF0470T | | |
| | | | ELECT CAP (ELNA, RFO, 50V/47UF) | | | | |
| | C437 | nsp | | | CCUS1H104KC | | |
| | C438 | nsp | | | CCUS1H103KC | | |
| | C439 | nsp | | | CCBS1H103ZFT | | |
| | C450 | nsp | | | CCBS1H104ZFT | | |
| | C451 | nsp | | | CCBS1H103ZFT | 1 | |
| | C452 | nsp | CERAMIC CAP | | CCBS1H104ZFT | 1 | |
| | C453 | nsp | CERAMIC CAP | | CCBS1H103ZFT | 1 | |
| | C454 | nsp | CERAMIC CAP | | CCBS1H104ZFT | | |
| | C455 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C456 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C701-703 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C704-406 | nsp | CHIP CAP | | CCUS1H101JA | | |
| | C707-714 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C715 | nsp | CHIP CAP | | CCUS1H150JA | | |
| | C716 | nsp | CERAMIC CAP | | CCBS1H104ZFT | | |
| | C717 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C718 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C721 | nsp | CHIP CAP | | CCUS1H151JA | | |
| | C722,723 | nsp | CHIP CAP | | CCUS1H181JA | | |
| | C724 | nsp | CHIP CAP | | CCUS1H151JA | | |
| | C725,726 | nsp | CHIP CAP | | CCUS1H181JA | | |
| | C727 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C728 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C729 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C733,734 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C735 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C736 | nsp | CERAMIC CAP | | CCBS1H104ZFT | 1 | |
| | C737 | nsp | CERAMIC CAP | | CCBS1H103ZFT | 1 | |
| | C738 | nsp | CHIP CAP | | CCUS1H104KC | + | |
| | C739 | nsp | CHIP CAP | | CCUS1H103KC | 1 | |
| | C759 C751 | - | ELECT CAP | | CCEA1CKS470T | 1 | |
| | C751 C752 | nsp | ELECT CAP | | CCEA1HKS100T | 1 | |
| | | nsp | | | | 1 | |
| | C753,754 | nsp | | | CCEA1CKS470T | 1 | |
| | C755 | nsp | | | CCEA1HKS100T | 1 | |
| | C756 | nsp | | | CCEA1CKS101T | 1 | |
| | C761-764 | 00D2544693939 | CAP, ELEC ELNA RFO 100uF/50V | | CCEA1HRFO101T | 1 | |
| | C801,802 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | 1 | |
| | C803-806 | nsp | CHIP CAP | | CCUS1H104KC | 1 | |
| | C807,808 | nsp | PP CAP (100V/0.01uF) | | CCMP2A103JN09T | 1 | |
| | C809,810 | nsp | CHIP CAP | | CCUS1H103KC | 1 | |
| | C817-819 | nsp | CAP , METALLIZED FILM | | CCME2A104JXT | 1 | |
| | C820,821 | nsp | CHIP CAP | | CCUS1H104KC | 1 | |
| | C822 | nsp | CHIP CAP | | CCUS1H103KC | L | |
| | C823,824 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C825 | nsp | CHIP CAP | | CCUS1H103KC | 1 | |
| | C826 | nsp | CHIP CAP | | CCUS1H104KC | 1 | |
| | C827 | nsp | CHIP CAP | | CCUS1H103KC | 1 | |
| • | C828-830 | nsp | CHIP CAP | | CCUS1H104KC | 1 | |

| | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|-----------|---------------|---------------|--|--------|------------------|------|-----|
| | C831 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C832 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C838 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C839 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C840 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C841 | nsp | CAP , MYLAR | | HCQI1H104JZT | | |
| | C844 | nsp | CAP , CERAMIC(X1/Y2/SC) | | KCKDKS472ME | | |
| | C845,846 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C847 | nsp | CHIP CAP | | CCUS1H102KC | | |
| ^ | C848.849 | | | | KCKDKS472ME | | |
| _ | , | nsp | CAP, CERAMIC(X1/Y2/SC) | | | | |
| | C851,852 | | ELECT CAP (3300uF/35V, RFA) | | CCEA1VRFA332E | | |
| | C853,854 | | CAP , ELECT (ELNA RFG, 50V/220uF, 85'C, 10X16) | | CCEA1HRFY221E | | |
| | C855 | | EIECT CAP (220uF/50V, RA3) | | CCEA1HRA3221E | | |
| | C856 | | CAP, ELECT (1000uF/35V, RA3, 12.5X20) | | CCEA1VRA3102E | | |
| | C857 | | CAP , ELECT (ELNA RFO 35V/3300uF) | | CCEA1VRFO332E | | |
| | C858 | 943134006960S | CAP , ELECT(470uF/35V, RJH, 10X20) | | CCEA1VRJH471E | | |
| | C859,860 | nsp | ELECT , CAP (1000uF/6.3V, 10*20, KJH) | | CCEA0JKJH102ES | | |
| | C861,862 | 00D2544694912 | EIECT CAP (220uF/25V, RFO) | | CCEA1ERFO221T | | |
| | C863 | 00D2544692943 | ELECT , CAP (1000uF/6.3V, 10*20, KJH) | | CCEA0JKJH102ES | | |
| | C864 | | EIECT CAP (100uF/63V, RA3) | | CCEA1JRA3101T | | |
| | C865 | 00D2544692943 | CAP , ELECT(100uF/63V, RFO, 10X12.5) | | CCEA1JRFO101E | 1 | t i |
| | C866 | 00D2544749715 | | | CCEA1ERFO102E | | |
| | C867 | 00D2544693939 | CAP, ELEC ELNA RFO 100uF/50V | | CCEA1HRFO101T | | |
| | C868 | | CAP, ELECT | | CCEA1HH4R7T | | |
| | C8887 | nsp | | | | | |
| | | nsp | CAP , METALLIZED FILM | | CCME2A104JXT | | |
| | C888 | nsp | | | CCUS1H104KC | | |
| | C889 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C890 | nsp | CAP , METALLIZED FILM | | CCME2A104JXT | | |
| | C905 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C906 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C907 | nsp | CAP , METALLIZED FILM | | CCME2A104JXT | | |
| AT | | | | | | | |
| отн | IERS PARTS GR | | | | | r | T |
| | BK11 | nsp | PCB BRACKET (A) | | CMD1A188 | | |
| | BK12 | nsp | PCB BRACKET | | CMD1A569 | | |
| | BK13 | nsp | PCB BRACKET | | CMD1A569 | | |
| | BK15 | nsp | PCB BRACKET | | CMD1A629 | | |
| | BK31 | nsp | FLT BRACKET | | CMD1A468 | | |
| | BK32 | nsp | FLT BRACKET | | CMD1A468 | | |
| | BN31 | nsp | WAFER 7PIN | | CJP07GI236ZW | | |
| | BN33 | nsp | WIRE ASS'Y(9P, 2.0MM, 400MM, SHIELD) | | CWB1C909400EN001 | | 1 |
| | BN35 | nsp | WIRE ASS'Y(3P, 2.0MM, 120MM, SHIELD) | | CWB1C203120EE001 | | |
| | BN51 | nsp | WIRE ASS'Y(5P, 2.0MM, 250MM, SHIELD) | | CWB1C205250EG001 | | |
| | BN53 | nsp | WIRE ASSY(11P, 2.0MM, 50MM) | | CWB1C911050EG | | |
| | | | | | | | |
| | CN31 | nsp | WIRE ASS'Y (7P,2.0MM,200MM) | | CWB1C90720047 | | - |
| | CN33 | nsp | WAFER 9PIN | | CJP09GA19ZY | | |
| | | | | | | | |
| | CN74 | nsp | WAFER 2PIN | | CJP02KA060ZY | | |
| | CN75 | nsp | WAFER 2PIN | | CJP02GA89ZY | | |
| | CN76 | nsp | WAFER 3PIN | | CJP03GA01ZY | | |
| | CN77 | nsp | WAFER 5PIN | | CJP05GA01ZY | | |
| | CW31 | nsp | WAFER 3PIN | | CJP03GI236ZW | | |
| | CX73 | nsp | WAFER 2PIN | | CJP02GA89ZY | | |
| | | | | | | 1 | |
| | CY11 | nsp | WAFER CARD CABLE | | CJP21GA117XY | | |
| | CY12 | nsp | WAFER 15PIN | | CJP15GB113ZY | | |
| | CY73 | nsp | WIRE ASSY(2P, 350MM) | | CWB4D932350UZ | | |
| | 0175 | | | | | | |

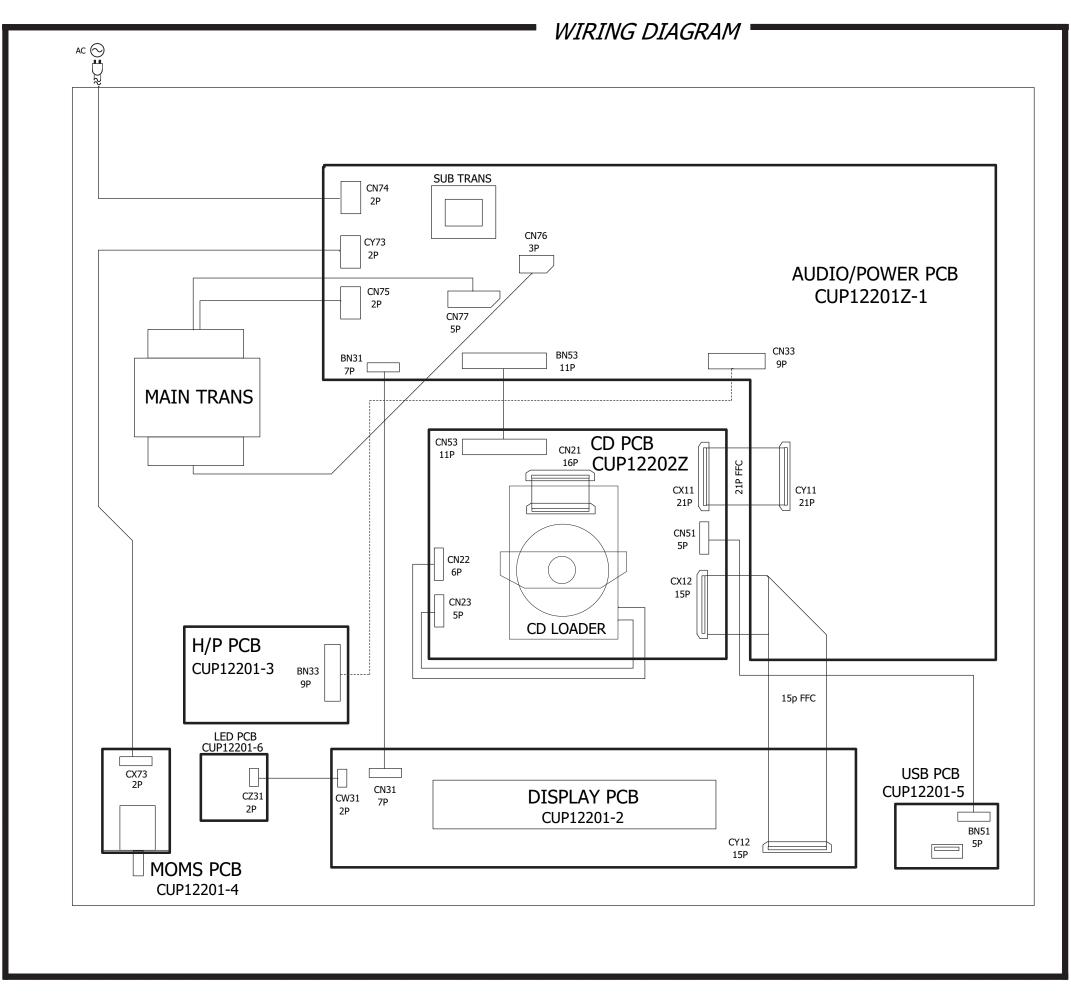
| | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|-------------|----------|---------------|-----------------------------------|--------|----------------|------|-----|
| | | | | | | | |
| | ET21,22 | nsp | | | CMC1A111 | | |
| | ET41 | nsp | EARTH PLATE | | HJT1A025 | | |
| | ET44 | nsp | EARTH PLATE | | HJT1A025 | | |
| | F701 | 943172006990D | VFD HCA-18MS03T | | CFLHCA-18MS03T | | |
| | F801-804 | nsp | FUSE HOLDER | | KJCFC5S | | |
| | JK31 | 00D9430183103 | OPTICAL MODULE (TX) | | HJSTOTX177L | | |
| | JK32 | | JACK (2P GOLD) | | CJJ4N067Z | | |
| | JK71 | 943643001320S | JACK USB STRAIGHT(BLACK) | | CJJ9X006Z | | |
| | JW02 | nsp | WIRE ASS'Y | | CWE8202150RV | | |
| | JW03 | nsp | WIRE ASS'Y | | CWE8202150RV | | |
| | JW91 | nsp | WIRE 1P BLACK(120MM) | | CWE8202120AA | | |
| | JW93 | nsp | WIRE 1P BLACK(120MM) | | CWE8202120AA | | |
| | L301-303 | nsp | CHIP FERRITE BEAD(60ohm, 2012) | | CLZ9R001Z | | |
| | L701,702 | nsp | CHIP FERRITE BEAD(60ohm, 2012) | | CLZ9R001Z | | |
| | L802 | nsp | CHIP FERRITE BEAD(60ohm, 2012) | | CLZ9R001Z | | |
| | L801 | 943111006890S | TOROIDAL COIL 100uH | | CLZ9Z100Z | | |
| | PH71 | 00D9430181600 | JACK | | CJJ2E020Z | | |
| | RL81 | 943682006880S | RELAY , DC5V, 0.15W, DLS5D1-0.15W | | CSL1C004ZE | | |
| | S701-708 | 00D9430004402 | TACT SW | | CST1A012ZT | | |
| \triangle | S801 | 00D9430140609 | CSH1A010ZV(SDL1P-B) | | CSH1A010ZV | | |
| | T801 | 943101006870D | SUB TRANS 5V (EUR) DCD710AE2/E1C | | CLT5I013ZE | | |
| | X301 | 943141003500S | CRYSTAL | | HOX16934E120C | | |
| \triangle | F901 | 00D9430199109 | FUSE 2.5A 250V | E2 | KBA2C2500TLEY | | |
| \triangle | F901 | 00D9430199109 | FUSE 2.5A 250V | E1C | KBA2C2500TLEY | 1 | |
| \triangle | F902 | 00D9430199109 | FUSE 2.5A 250V | E2 | KBA2C2500TLEY | | |
| | F902 | - | OPEN | E1C | | | |

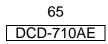
MAIN PCB ASSY

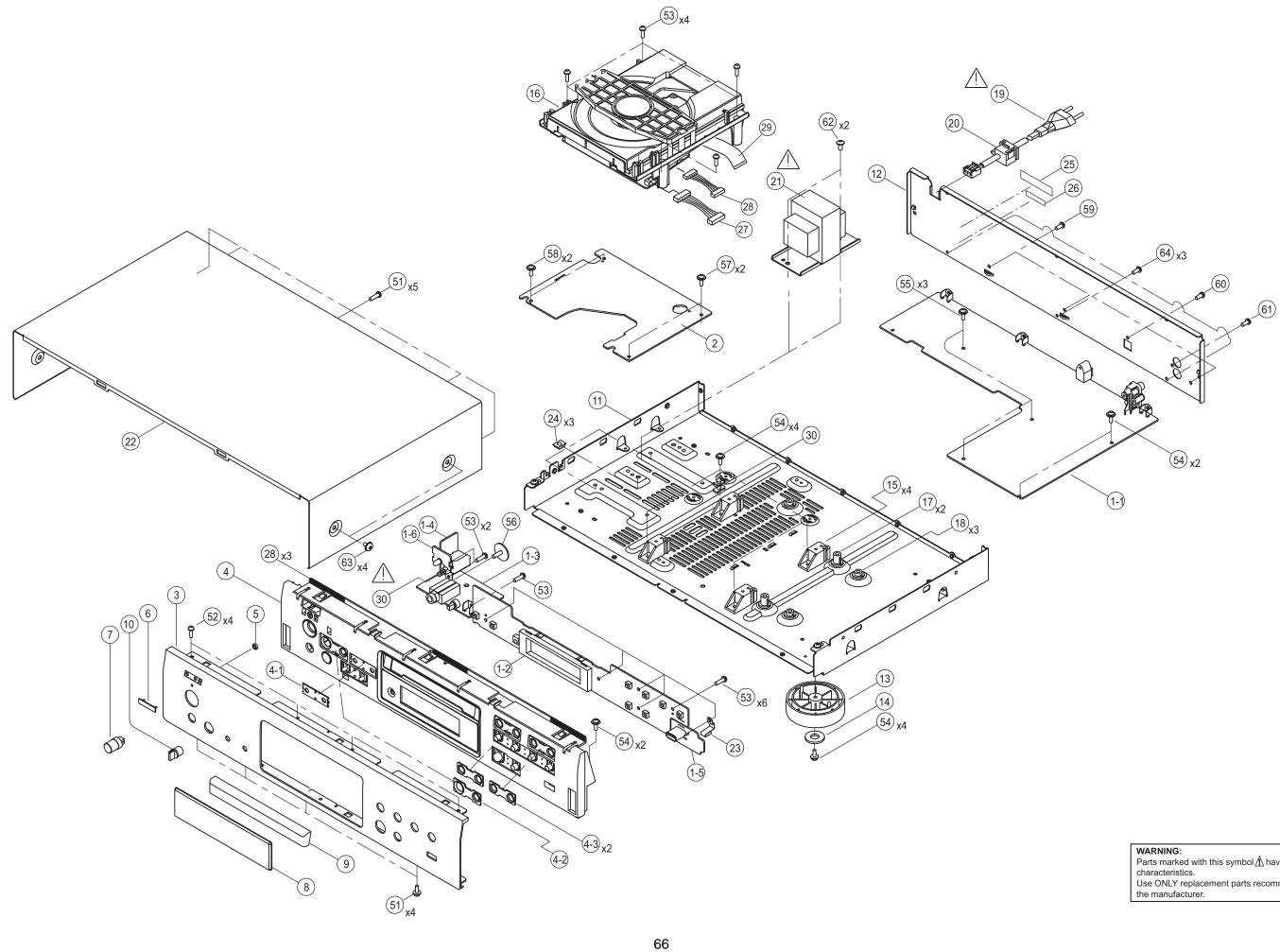
| | Ref No. | Part No. | Part Name | Remark | | Q'ty | New |
|-----|--------------|---------------|---|--------|---|------|-----|
| SEN | | GROUP | ۱ <u>ــــــــــــــــــــــــــــــــــــ</u> | | 4 | · | |
| - | IC11 | 943243006920D | MCU IC(512K,LQFP-100P) | | CVIANAM1494C | | * |
| | IC13 | | EEPROM (4K,SO8-8P) | | CVIM95040-WMN6TP | | |
| | IC14 | | 5-CH MOTOR DRIVE IC (WITH REG,SSOP-28P) | | CVIIP4001CRLTF | | |
| | IC15 | | USB DECODER FLASH IC(100PIN, QFP) | | CVIANAM1495C | | * |
| | IC15 | | | | | | |
| | | 236710022502S | CP CHIP IC(MF134IS2161,20P,FROM MARANTZ) | | CVI236710022502S_M | | * |
| | IC17 | 943245006980S | CD DSP IC(SERVO,AMPLIFIER,DSP,LQFP-80P) | | CVITC94A92FG | | |
| | IC18 | | 2CH MULTIPLXER IC | | HVITC7WH157FU | | |
| | IC19 | | SIGNAL INVERTER IC(USV) | | CVIKIC7SH04FU | | |
| | IC20 | 90M-HC900160R | IC LM1117S15 REG. (SOT-223) | | CVIKIA1117S15 | | |
| | Q202 | 00D9430058908 | CHIP TR | | HVTKTA1504SYRTK | | |
| | D201 | 00D9430060404 | SCHOTTKEY BARRIER DIODE | | HVDRB160L60TE25 | | |
| | D202 | | SCHOTTKEY BARRIER DIODE | | HVDRB160L60TE25 | | |
| | D202 | 943209001080S | CHIP DIODE | | CVD1SS355T | | |
| | D200 | 9432090010803 | | | 010100001 | | |
| CAF | PACITORS GRO | UP | 1 J | | ۱ <u>ــــــــــــــــــــــــــــــــــــ</u> | · | · |
| | C101,102 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C103-105 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C106 | nsp | CHIP CAP | | CCUS1H102KC | İ | 1 |
| | C107,108 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C109 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C110 | nsp | CHIP CAP | | CCUS1H180JA | | |
| | C111 | nsp | CHIP CAP | | CCUS1H220JA | | |
| | C112 | | | | | | |
| | | nsp | | | CCUS1H104KC | | |
| | C113,114 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C115-117 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C118 | nsp | CHIP CAP | | CCUS1A105KC | | |
| | C151 | nsp | CHIP ELECT CAP | | HCEC0JRV2101T | | |
| | C201 | nsp | CHIP CAP | | CCUS1H222KC | | |
| | C202,203 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C204 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | C205 | nsp | CHIP CAP | | CCUS1H472KC | | |
| | C206 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C207 | nsp | CHIP CAP | | CCUS1H153KC | | |
| | C208 | | CHIP CAP | | CCUS1H104KC | | |
| | | nsp | | | | | |
| | C210,211 | nsp | | | CCUS1H104KC | | |
| | C213 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C214 | nsp | CHIP CAP | | CCUS1H333KC | | |
| | C215 | nsp | CHIP CAP | | CCUS1H562KC | | |
| | C216 | nsp | CHIP CAP | | CCUS1H471JA | | |
| | C217 | nsp | CHIP CAP | | CCUS1H473KC | | |
| | C219 | nsp | CHIP CAP | | CCUS1H473KC | | |
| | C220-222 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C223 | nsp | CHIP CAP | | CCUS1H103KC | | 1 |
| | C224 | nsp | CHIP CAP | | CCUS1H470JA | | |
| | C225 | nsp | CHIP CAP | | CCUS1H153KC | | |
| | C225 | | CHIP CAP | | CCUS1H103KC | | |
| | | nsp | | | | | |
| | C227 | nsp | | | CCUS1H471JA | | |
| | C231 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C232,233 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C234 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C235 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C236 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C237 | nsp | CHIP CAP | | CCUS1H104KC | | |
| | C238 | nsp | CHIP CAP | | CCUS1H102KC | | |
| | C239 | nsp | CHIP CAP | | CCUS1H103KC | | |
| | | | CHIP CAP | | CCUS1H104KC | | |
| | C240 | nsp | | | | | |
| | C243-247 | nsp | CHIP CAP | | CCUS1H104KC | | |

| C265 C268 C281 C284 C290 C501 C512 C513 C514 C534 C536 C536 C536 C536 C537 C538 C539 C540 C542 C541 C601 C602 C603 C604 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 8-273 1,282 4-289 0-296 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP | CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T HCEC0JRV2101T CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC | | |
|--|---|--|--|--|---|----------|
| C268 C281 C284 C290 C501 C512 C513 C514 C534 C536 C536 C537 C538 C536 C537 C538 C540 C540 C540 C540 C541 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C613 C614 C615 C616 C617 C618 C616 C617 C618 | 8-273 1,282 4-289 0-296 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP | CCUS1H104KC HCEC0JRV2101T HCEC1CRV2101T CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC | | |
| C281 C284 C290 C501 C512 C513 C514 C534 C536 C536 C537 C538 C536 C537 C538 C537 C538 C539 C540 C542 C541 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 1,282 4-289 0-296 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP | HCEC0JRV2101T HCEC1CRV2101T CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC | | |
| C284 C290 C501 C512 C513 C514 C534 C536 C537 C538 C539 C542 C603 C604 C605 C606 C611 C612 C613 C614 C615 C616 C617 C618 | 4-289 0-296 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | HCEC0JRV2101T HCEC1CRV2101T CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC | | |
| C290 C501 C512 C513 C514 C534 C534 C536 C536 C536 C537 C538 C539 C540 C542 C542 C542 C541 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 0-296 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 1 5 7 1 2 3 4 5 6 1 2 1 5 7 1 2 3 4 5 1 5 7 1 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 7 1 2 3 4 5 5 7 1 1 5 7 1 5 7 1 5 7 1 1 5 7 1 5 7 1 5 7 1 5 7 1 5 7 1 5 7 1 5 7 1 5 7 5 7 1 5 7 5 1 5 7 5 7 5 7 5 7 5 7 5 7 5 7 5 7 5 7 5 7 5 7 7 7 7 7 7 7 7 7 7 7 7 7 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP ELECT CAP CHIP CAP | HCEC1CRV2101T CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H1220JA CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H102KC CCUS1H102KC CCUS1H102KC CCUS1H104KC | | |
| C501 C512 C513 C514 C534 C534 C536 C536 C536 C536 C537 C538 C539 C542 C542 C542 C542 C542 C542 C544 C603 C604 C603 C604 C605 C606 C611 C612 C613 C614 C615 C616 C617 C618 | 1-508 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H104KC CCUS1H1220JA CCUS1H180JA CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H103KC CCUS1H104KC | | |
| C510 C512 C513 C514 C534 C536 C536 C536 C537 C538 C539 C540 C542 C540 C542 C551 C601 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 0,511 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H220JA CCUS1H180JA CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H102KC CCUS1H102KC CCUS1H104KC | | |
| C512 C513 C514 C534 C535 C536 C537 C538 C537 C538 C539 C540 C542 C541 C601 C602 C603 C604 C604 C611 C612 C613 C614 C615 C616 C617 C618 | 2 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H220JA CCUS1H180JA CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H102KC CCUS1H102KC CCUS1H103KC | | |
| C513 C514 C534 C535 C536 C537 C538 C539 C540 C542 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 3 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H180JA CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H102KC CCUS1H102KC CCUS1H103KC | | |
| C514 C531 C534 C535 C536 C537 C538 C539 C540 C542 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 4-520 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H102KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C531 C534 C535 C536 C537 C538 C539 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 1-533 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H104KC CCUS1H103KC CCUS1H102KC CCUS1H102KC CCUS1H103KC | | |
| C534 C535 C536 C537 C538 C539 C540 C542 C542 C542 C542 C542 C542 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 4 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C535 C536 C537 C538 C539 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 5 6 7 8 9 0,541 2 1-557 1 2 3 4 5 6 0 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C536 C537 C538 C542 C542 C542 C541 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 6 7 8 9 0,541 <u>2</u> 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C537 C538 C539 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 7 8 9 0,541 2 1-557 1 2 3 4 5 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T CCUS1H104KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C537 C538 C539 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 7 8 9 0,541 2 1-557 1 2 3 4 5 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H103KC CCUS1H103KC CCUS1H103KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H103KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C538 C539 C542 C542 C551 C601 C602 C603 C604 C605 C606 C611 C612 C613 C614 C615 C616 C617 C618 | 8 9 0,541 2 1-557 1 2 3 4 5 6 0 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC CCUS1H104KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H103KC CCUS1H102KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C539 C540 C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 9 0,541 2 1-557 1 2 3 4 5 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H103KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C540 C542 C551 C601 C602 C603 C604 C605 C606 C611 C612 C613 C614 C615 C616 C617 C618 | 0,541 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C542 C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 2 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H104KC HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C551 C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 1-557 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp nsp | CHIP ELECT CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | HCEC0JRV2101T CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C601 C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 1 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H103KC | | |
| C602 C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 2 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H104KC CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C603 C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 3 4 5 6 0 1 | nsp nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP CHIP CAP | CCUS1H102KC CCUS1H103KC CCUS1H104KC | | |
| C604 C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 4 5 6 0 1 | nsp nsp nsp nsp nsp | CHIP CAP CHIP CAP CHIP CAP | CCUS1H103KC CCUS1H104KC | | |
| C605 C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 5 6 0 1 | nsp nsp nsp nsp | CHIP CAP CHIP CAP | CCUS1H104KC | | i |
| C606 C610 C611 C612 C613 C614 C615 C616 C617 C618 | 6 0 1 | nsp nsp nsp | CHIP CAP | | | |
| C610 C611 C612 C613 C614 C615 C616 C617 C618 | 0 1 | nsp nsp | | | | l |
| C611 C612 C613 C614 C615 C616 C617 C618 | 1 | nsp | CHIP CAP | CCUS1H102KC | | 1 |
| C612 C613 C614 C615 C616 C617 C617 | | • | | CCUS1H103KC | | 1 |
| C613 C614 C615 C616 C617 C617 | 2 | | | CCUS1H104KC | | |
| C614 C615 C616 C617 C618 | <u>^</u> | nsp | | CCUS1H102KC | _ | |
| C615 C616 C617 C618 | | nsp | | CCUS1H103KC | | |
| C616 C617 C618 | | nsp | CHIP CAP | CCUS1H104KC | | |
| C617 C618 | | nsp | CHIP CAP | CCUS1H102KC | | |
| C618 | | nsp | | CCUS1H103KC | | |
| | | nsp | CHIP CAP | CCUS1H104KC | | |
| 10040 | | nsp | CHIP CAP | CCUS1H102KC | | |
| C619 | | nsp | CHIP CAP | CCUS1H103KC | | |
| C620 | | nsp | CHIP CAP | CCUS1H104KC | | 1 |
| C621 | 1 | nsp | CHIP CAP | CCUS1H102KC | | 1 |
| OTHERS | PARTS GR | OUP | | | | I |
| CN12 | 2 | nsp | WAFER | CJP06GA208ZY | | |
| CN2 | 21 | nsp | WAFER CARD CABLE | CJP16GA193ZY | | ĺ |
| CN22 | 2 | nsp | WAFER | CJP06GA208ZY | | ĺ |
| CN23 | 3 | nsp | WAFER | CJP05GA208ZY | | ĺ |
| CN5 ² | 51 | nsp | WAFER | CJP05GA208ZY | | |
| CN52 | 52 | nsp | WAFER | CJP06GA208ZY | | ĺ |
| CN53 | | nsp | WAFER | CJP11GA208ZY | | |
| CX11 | 1 | nsp | WAFER | CJP21GA193ZY | | |
| CX12 | | nsp | WAFER CARD CABLE | CJP15GA193ZY | | |
| ⊥ F101 | 1 | 943661006910S | POLY SWITCH (1.6A, 8V) | CBA5H1600PSUYT | | |
| L101 | 1 | 943119005010S | CHIP FERRITE BEAD(60ohm, 2012) | CLZ9R001Z | | |
| | 1-206 | | CHIP FERRITE BEAD(60ohm, 2012) | CLZ9R001Z | | |
| L207 | | nsp | AXAIL COIL | HLQ02C101JT | | ĺ |
| L501 | | 943119005010S | CHIP FERRITE BEAD(60ohm, 2012) | CLZ9R001Z | | |
| Vici | | 0421440040000 | | | | |
| X101 X501 | | | CRYSTAL , SMD(9MHZ, HC-49/SMD, 5PF) CRYSTAL , SMD(9MHZ, HC-49/SMD, 5PF) | COX09000E150S COX09000E150S | | |









DCD-710AE

WARNING: Parts marked with this symbol A have critical Use ONLY replacement parts recommended by the manufacturer.

PARTS LIST OF EXPLODED VIEW

 \ast Parts for which "nsp" is indicated on this table cannot be supplied.

* P.W.B. ASS'Y for which "nsp" is indicated on this table cannot be supplied. When repairing the P.W.B. ASS'Y, check the board parts table and order replacement parts.

st The parts listed below are for maintenance only, might differ from the parts used in the unit in appearances or dimensions.

Note: The symbols in the column "Remarks" indicate he following destinations.

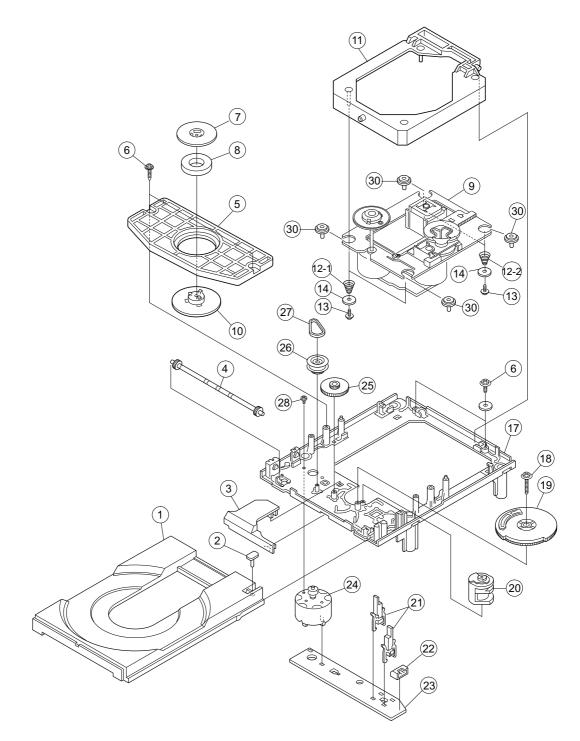
E2 : Europe model E1C : China model

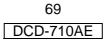
BK : (Black model SP : (Premium Silver model

| | Ref No. | Part No. | Part Name | Remarks | | Q'ty | New |
|-------------|----------|----------------------|--------------------------|------------|-------------------------|------|-----|
| | 1 | nsp | AUDIO/POWER PCB ASSY | | CUP12201B | 1 | * |
| | 1-1 | - | AUDIO/POWER PCB | | | | |
| | 1-2 | - | DISPLAY PCB | | | | |
| | <u> </u> | - | H/P PCB | | | | |
| | 1-4 | - | MOMS PCB | | | | |
| | 1-5 | - | USB PCB | | | | |
| | 1-6 | - | LED PCB | | | | |
| | 2 | nsp | MAIN PCB ASSY | | CUP12202B | 1 | * |
| | 3 | 943402006350D | FRONT PANEL | BKE2 | CKM1A213YC45 | 1 | * |
| | 3 | 943402006360D | FRONT PANEL | SPE2/SPE1C | CKM1A213ZC62 | 1 | * |
| | 4 | 943443006390D | INNER PANEL | BKE2 | CGW1A480B28 | 1 | * |
| | 4 | 943443006400D | INNER PANEL | SPE2/SPE1C | CGW1A480RGG45 | 1 | * |
| | 4-1 | - | KNOB GUIDE | | CGW1A480-1 | 1 | |
| | 4-2 | - | KNOB GUIDE | | CGW1A480-2 | 1 | |
| | 4-3 | - | KNOB GUIDE | | CGW1A480-3 | 1 | |
| | 5 | 00D9430189903 | LENS | | CGL1A254 | 1 | |
| | 6 | 00D1310158049 | DENON BADGE | BKE2 | CGB1A140U | 1 | |
| | 6 | 00D1310158052 | DENON BADGE | SPE2/SPE1C | CGB1A140T | 1 | |
| | 7 | 00D9430179502 | POWER KNOB | BKE2 | CGK1A124ZA | 1 | |
| | 7 | 00D9430179609 | POWER KNOB | SPE2/SPE1C | CGK1A124YA | 1 | |
| | 8 | 943416006410D | FIP WINDOW | | CGU1A397Y | 1 | * |
| | 9 | 00D9430180708 | DOOR | BKE2 | CGR2A404WB28 | 1 | |
| | 9 | 00D9430180805 | DOOR | SPE2/SPE1C | CGR2A404RGYG45 | 1 | |
| | 10 | 00D9430180902 | LEVEL KNOB | BKE2 | CBC1A157B28 | 1 | |
| | 10 | 00D9430181008 | LEVEL KNOB | SPE2/SPE1C | CBC1A157RGG45 | 1 | |
| | 11 | nsp | BOTTOM CHASSIS | | CUA5A269 | 1 | * |
| | 12 | 943406006670D | REAR PANEL | E2 | CKF3A316Z | 1 | * |
| | 12 | 943406006680D | REAR PANEL | E1C | CKF3A316X | 1 | * |
| | 13 | nsp | FOOT | | CKL1A093 | 4 | |
| | 14 | nsp | FOOT CUSHION | | CHG2A289 | 4 | |
| | 15 | nsp | MECHA SUPPORT | | CMH2A259 | 4 | |
| | 16 | 943302000090D | CD MECHANISM ASSY | | CJDWSL11TCNA | 1 | |
| | 17 | nsp | PCB HOLDER | | CHE170 | 2 | |
| | 18 | nsp | PCB HOLDER | | CHE2A030 | 3 | |
| | 19 | | AC CORD | E2 | CJA2B043ZA | 1 | |
| | 19 | 943611006700S | | E1C | CJA2N047ZA | 1 | |
| | 20 | 00D9430095505 | | | KHR1A028 | | * |
| | 21 | 943101006510D | | PKE2 | CLT5M039ZE | 1 | |
| | 22 | 00D9430181105 | | BKE2 | CKC1A175S56 | 1 | |
| - | 22 23 | 00D9430181202 | TOP COVER EARTH PLATE | SPE2/SPE1C | CKC1A175S55 CMC1A369 | 1 | * |
| | 23 24 | nsp nsp | RUBBER | | CHG1A113 | 3 | |
| | 24 25 | nsp | SERIAL NO LABEL | | CQB1A622 | 1 | |
| | 25 27 | nsp 00D9430201000 | FERRITE CORE | | CQBTA022 CLZ9W003Z | 1 | |
| | 27 | nsp | HEMELON TAPE | | CHS1A032 | 5 | |
| | 20 | nsp | CLAMPER | | CHR301 | 5 | |
| \triangle | 29 30 | 00D9430140609 | CSH1A010ZV(SDL1P-B) | | CSH1A010ZV | 1 | |
| 44 | | 0000-00140009 | | | | | |
| | | | | | | | |

| | Ref No. | Part No. | Part Name | Remarks | | Q'ty | New |
|-----|---------|----------|---------------|---------|-------------|------|-----|
| SCF | REWS | | | | | | |
| | 51 | nsp | DOT SCREW 3X8 | BK | CTBD3+8JFZR | 9 | |
| | 51 | nsp | DOT SCREW 3X8 | SP | CTBD3+8JFN | 9 | |
| | 52 | nsp | SCREW 3X8 | вк | CTB3+8JFZR | 4 | |
| | 52 | nsp | SCREW 3X8 | SP | CTB3+8GFN | 4 | |
| | 53 | nsp | SCREW 3X10 | | CTB3+10JR | 13 | |
| | 54 | nsp | SCREW 3X6 | | CTW3+6JR | 12 | |
| | 55 | nsp | SCREW 3X12 | | CTB3+12JR | 3 | |
| | 56 | nsp | SCREW 3X10 | | CTWS3+10GR | 1 | |
| | 57 | nsp | SCREW 3X18 | | CTW3+18JR | 2 | |
| | 58 | nsp | SCREW 3X22 | | CTW3+22JR | 2 | |
| | 59 | nsp | SCREW 3X6 | | CTB3+6FFZR | 3 | |
| | 60 | nsp | SCREW 3X8 | | CTB3+8JFZR | 1 | |
| | 61 | nsp | SCREW 3X10 | | CTB3+10JFZR | 1 | |
| | 62 | nsp | SCREW 4X6 | | CTB4+8FR | 2 | |
| | 63 | nsp | SCREW 4X6 | вк | CTWD4+6FFZR | 4 | |
| | 63 | nsp | SCREW 4X6 | SP | CTWD4+6FFN | 4 | |
| | 64 | nsp | SCREW 3X8 | | CTBD3+8JFZR | 3 | |

EXPLODED VIEW OF CD MECHANISM UNIT





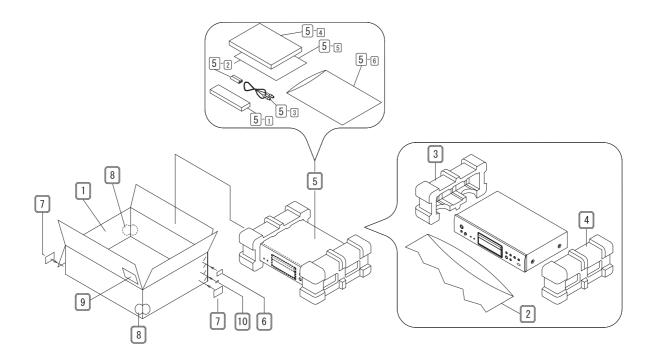
PARTS LIST OF CD MECHANISM UNIT

 $\ast\,$ Parts for which "nsp" is indicated on this table cannot be supplied.

* The parts listed below are for maintenance only, might differ from the parts used in the unit in appearances or dimensions.

| Ref. No. | Part No. | Part Name | Remarks | Q'ty | New |
|----------|---------------|-------------------------|--|------|-----|
| 1 | 00DS264629001 | Tray (C) | | 1 | |
| 2 | - | - | This part (No.2)doesn't belong to the tray. Take it down from old tray and use again when changing the tray. | | |
| 3 | 00DS262554401 | Gear cover(S) | | 1 | |
| 4 | 00DS262553501 | Tray gear(S) | | 1 | |
| 5 | 00DS262554601 | Chucking plate | | 1 | |
| 6 | nsp | Screw 2.6 x 7 +PTPWH | | 4 | |
| 7 | nsp | Chucking yoke | | 1 | |
| 8 | nsp | Magnet | | 1 | |
| 9 | 00D9640011007 | MECHA DA11T3CN | | 1 | |
| 10 | nsp | Chucking pulley | | 1 | |
| 11 | nsp | Sub chassis Ass'y | | 1 | |
| 12-1 | 00DS262723601 | Coil spring(front) | | 2 | |
| 12-2 | 00DS262723501 | Coil spring(back) | | 2 | |
| 13 | nsp | Screw 2.6 x 10 +P | No slit type2 | 4 | |
| 14 | nsp | Washer 2130 | | 4 | |
| 17 | nsp | Outsert main chassis(S) | | 1 | |
| 18 | nsp | Screw 2.6 x 16 +PTPWH | | 1 | |
| 19 | 00DS262554701 | Drive gear(S) | | 1 | |
| 20 | 00DS262554504 | Contorol cam(S) | | 1 | |
| 21 | 00DS169266711 | Leaf switch | | 2 | |
| 22 | nsp | 5P connector | | 1 | |
| 23 | nsp | Loading P.W.B | | 1 | |
| 24 | 00DSX26251171 | Loading motor Ass'Y | | 1 | |
| 25 | 00DS262553402 | Middle gear | | 1 | |
| 26 | 00DS262553602 | Loading pulley | | 1 | |
| 27 | 00DS365338700 | LM belt | | 1 | |
| 28 | nsp | Screw 2.6 x 2.5 +B | | 1 | |
| 30 | 00DS262723401 | Insulator | | 4 | |

PACKING VIEW



PARTS LIST OF PACKING & ACCESSORIES

 $\ast\,$ Parts for which "nsp" is indicated on this table cannot be supplied.

* The parts listed below are for maintenance only, might differ from the parts used in the unit in appearances or dimensions.

| Note: The symbols in the column "Rer | narks" indicate he following destinations. |
|--------------------------------------|--|
| E2 : Europe model | E1C : China model |
| BK : (Black model | SP : (Premium Silver model |

| Ref No. | Part No. | Part Name | Remarks | | Q'ty | New |
|---------|---------------|----------------------|------------|----------------|------|-----|
| 1 | 943531006720D | CARTON BOX | E2 | CPG2A819U | 1 | * |
| 1 | 943531006730D | CARTON BOX | E1C | CPG2A819S | 1 | * |
| 2 | nsp | POLY BAG(Set) | | CPB1A013Y | 1 | |
| 3 | 00D9430177902 | SNOW PAD | | CPS1A748 | 1 | |
| 4 | 00D9430178008 | SNOW PAD | | CPS1A749 | 1 | |
| 5-1 | 943307006790D | REMOCON | BKE2 | CARTDCD710AEBK | 1 | * |
| 5-1 | 943307006800D | REMOCON | SPE2/SPE1C | CARTDCD710AESP | 1 | * |
| 5-2 | nsp | BATTERY (SIZE 'AAA') | | CABR03PPB | 2 | |
| 5-3 | nsp | PIN CORD | | CJS4N014Z | | |
| 5-4 | 943541006760D | INSTRUCTION MANUAL | E2 | CQX1A1445Z | 1 | * |
| 5-4 | 943541006770D | INSTRUCTION MANUAL | E1C | CQX1A1447Z | 1 | * |
| 5-5 | nsp | S.S.LIST(EX) | | CQE1A226Q | 1 | |
| 5-6 | nsp | POLY BAG | | CPB1061W | 1 | |
| 6 | nsp | POS LABEL | BKE2 | CQB1A772S | 1 | * |
| 6 | nsp | POS LABEL | SPE2 | CQB1A772T | 1 | * |
| 6 | nsp | POS LABEL | SPE1C | CQB1A772P | 1 | * |
| 7 | nsp | CONTROL LABEL | | CQB1A627 | 2 | * |
| 8 | 00D9430194804 | COLOR LABEL | SPE2/SPE1C | CQB1A676 | 2 | |
| 9 | nsp | DATE LABEL | E1C | CQB1A622 | 1 | |
| 10 | nsp | CARTON LABEL(C) | E1C | CQB1A940Z | 1 | * |

NOTE FOR SCHEMATIC DIAGRAM

WARNING:

Parts marked with this symbol \triangle have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.

CAUTION:

Before returning the unit to the customer, make sure you make either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 milliamps, or if the resistance from chassis to either side of the power cord is less than 460 kohms, the unit is defective.

WARNING:

DO NOT return the unit to the customer until the problem is located and corrected.

NOTICE:

ALL RESISTANCE VALUES IN OHM. k=1,000 OHM M=1,000,000 OHM ALL CAPACITANCE VALUES IN MICRO FARAD. P=MICRO-MICRO FARAD EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL INPUT CONDITION. CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.

