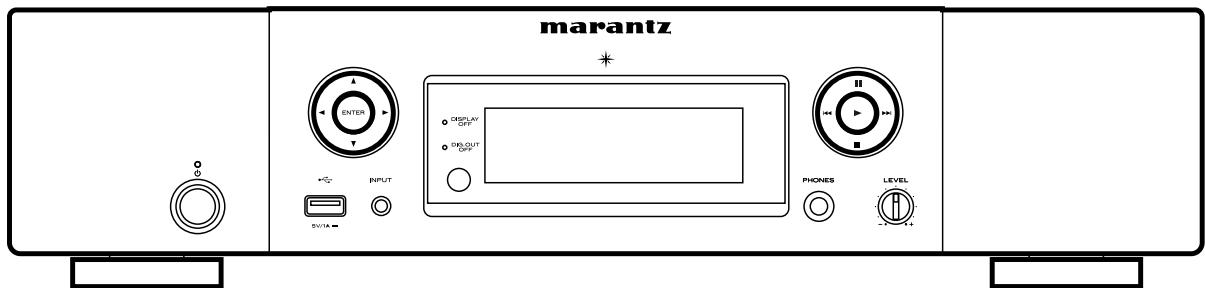


# Service Manual

NA8005 /N1B, N1SG, U1B, K1B, FN

Network Audio Player



• For purposes of improvement, specifications and design are subject to change without notice.

• Please use this service manual with referring to the operating instructions without fail.

• Some illustrations using in this service manual are slightly different from the actual set.

**marantz®**

**NA8005**

**Ver. 1**

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# ABOUT THIS MANUAL

Read the following information before using the service manual.

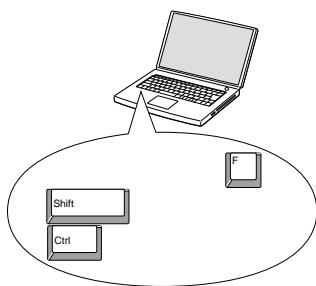
## What you can do with this manual

### Search for a Ref. No. (phrase) (Ctrl+Shift+F)

You can use the search function in Acrobat Reader to search for a Ref. No. in schematic diagrams, block diagrams, and parts lists.

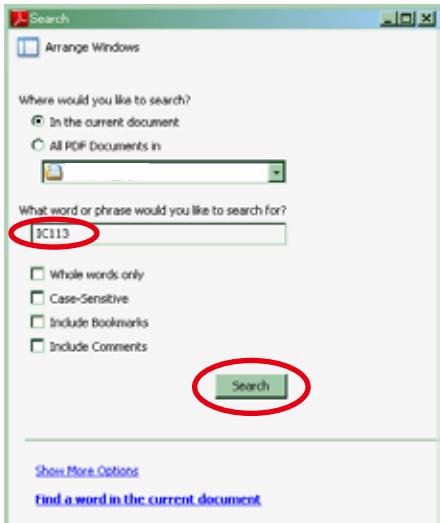
1.Press **Ctrl+Shift+F** on the keyboard.

- The Search window appears.



2.Enter the Ref. No. you want to search for in the Search window, and then click the **Search** button.

- A list of search results appears.



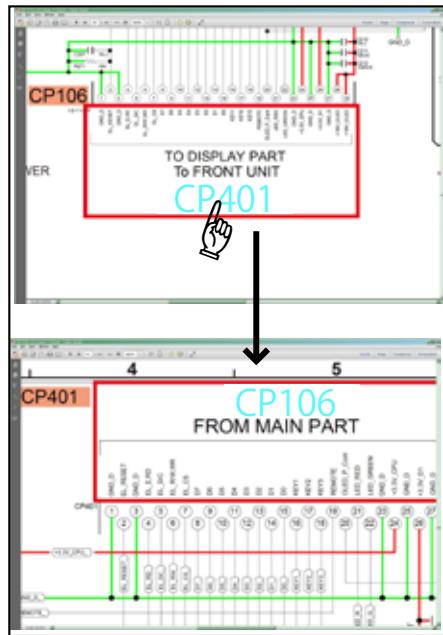
3.Click an item on the list.

- The screen jumps to the page for that item, and the search phrase is displayed.

### Jump to the target of a schematic diagram connector

Click the Ref. No. of the target connector in the red box around a schematic diagram connector.

- The screen jumps to the target connector.



- Page magnification stays the same as before the jump.

## Using Adobe Reader (Windows version)

### Add notes to this data (Sign)

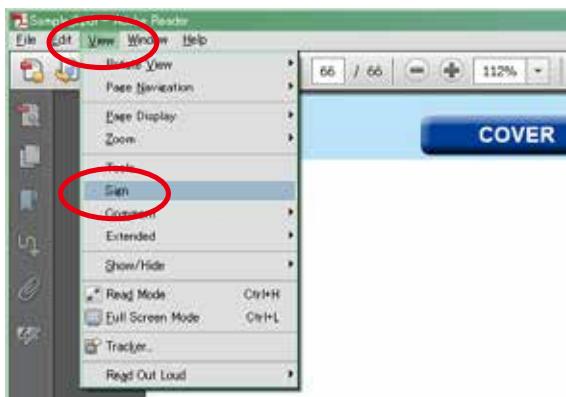
The Sign function lets you add notes to the data in this manual.

Save the file once you have finished adding notes.

#### [Example using Adobe Reader X]

On the "View" menu, click "Sign".

- The Sign pane appears.



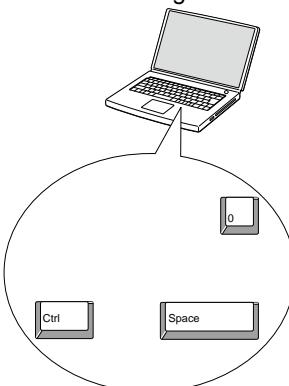
#### [Example using Adobe Reader 9]

On the "Document" menu, click "Sign".

### Magnify schematic / printed wiring board diagrams - 1 (Ctrl+Space, mouse operation)

Press **Ctrl+Space** on the keyboard and drag the mouse to select the area you want to view.

- The selected area is magnified.

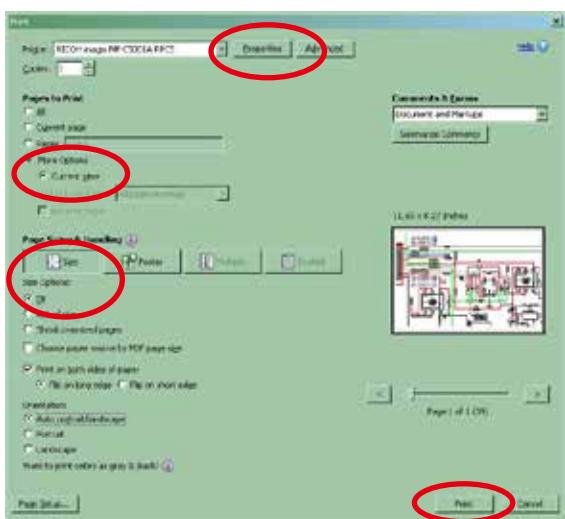


- When you want to move the area shown, hold down **Space** and drag the mouse.
- When you want to show a full page view, press **Ctrl+0** on the keyboard.

### Print a magnified part of the manual

The Properties dialog box and functions will vary depending on your printer.

- Drag the mouse to magnify the part you want to print.
- On the "File" menu, click "Print".
- Configure the following settings in the Print dialog box.



- Click the **Print** button to start printing.

#### Properties

Click this button and check that the printer is set to a suitable paper size.

#### Page to print

Select the following checkbox.

"More Options" : "Current View"

#### Page Sizing & Handling

Select the following checkbox.

"Size" / "Size Options" : "Fit"

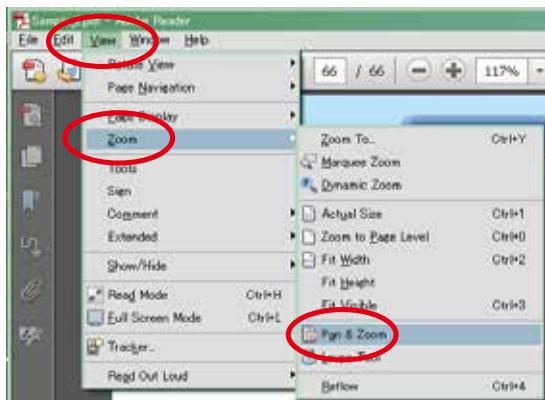
## Magnify schematic / printed wiring board diagrams - 2

### (Pan & Zoom function)

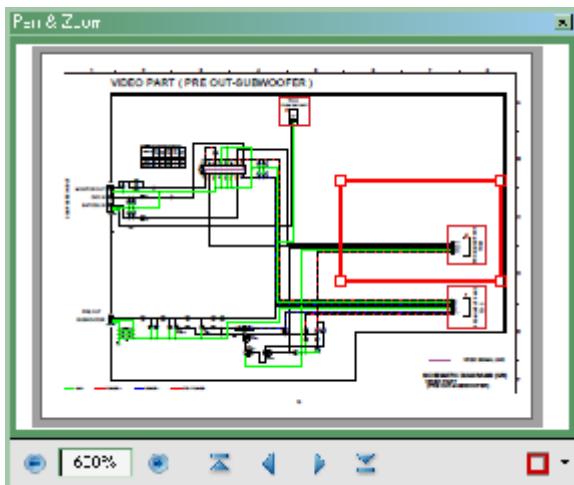
The Pan & Zoom function lets you see which part of a magnified diagram is being shown in a separate window.

#### [Example using Adobe Reader X]

On the "View" menu, point to "Zoom", and then click "Pan & Zoom".



- The Pan & Zoom window appears on the screen.



#### [Example using Adobe Reader 9]

On the "Tools" menu, point to "Select & Zoom", and then click "Pan & Zoom Window".

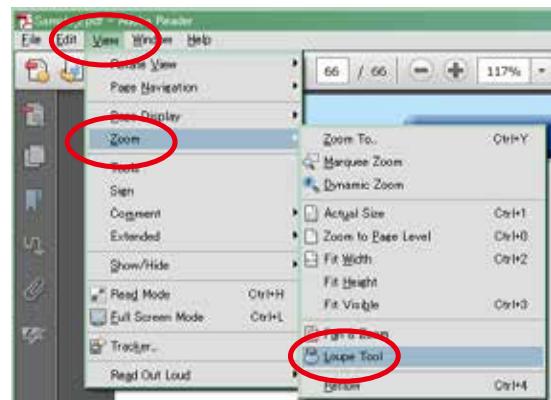
## Magnify schematic / printed wiring board diagrams - 3

### (Loupe Tool function)

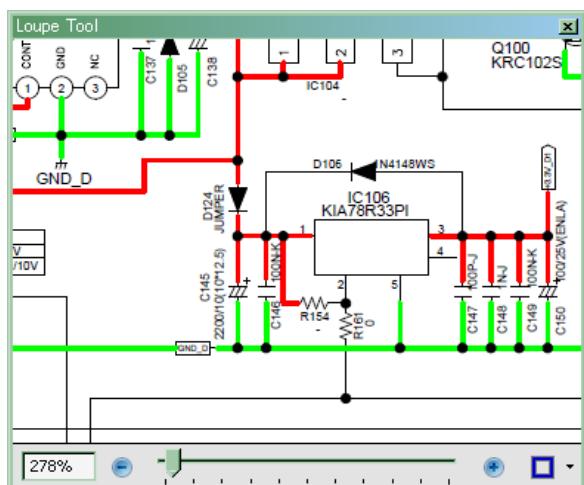
The Loupe Tool function lets you magnify a specific part of a diagram in a separate window.

#### [Example using Adobe Reader X]

On the "View" menu, point to "Zoom", and then click "Loupe Tool".



- The Loupe Tool window appears on the screen.



#### [Example using Adobe Reader 9]

On the "Tools" menu, point to "Select & Zoom", and then click "Loupe Tool Window".

## SAFETY PRECAUTIONS

The following items should be checked for continued protection of the customer and the service technician.

### leakage current check

Before returning the set to the customer, be sure to carry out either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 millamps, or if the resistance from chassis to either side of the power cord is less than 460 kohms, the set is defective.

Be sure to test for leakage current with the AC plug in both polarities, in addition, when the set's power is in each state (on, off and standby mode), if applicable.

### **CAUTION** Please heed the following cautions and instructions during servicing and inspection.

#### ○ Heed the cautions!

Cautions which are delicate in particular for servicing are labeled on the cabinets, the parts and the chassis, etc. Be sure to heed these cautions and the cautions described in the handling instructions.

#### ○ Cautions concerning electric shock!

- (1) An AC voltage is impressed on this set, so if you touch internal metal parts when the set is energized, you may get an electric shock. Avoid getting an electric shock, by using an isolating transformer and wearing gloves when servicing while the set is energized, or by unplugging the power cord when replacing parts, for example.
- (2) There are high voltage parts inside. Handle with extra care when the set is energized.

#### ○ Caution concerning disassembly and assembly!

Through great care is taken when parts were manufactured from sheet metal, there may be burrs on the edges of parts. The burrs could cause injury if fingers are moved across them in some rare cases. Wear gloves to protect your hands.

#### ○ Use only designated parts!

The set's parts have specific safety properties (fire resistance, voltage resistance, etc.). Be sure to use parts which have the same properties for replacement. The burrs have the same properties. In particular, for the important safety parts that are indicated by the  mark on schematic diagrams and parts lists, be sure to use the designated parts.

#### ○ Be sure to mount parts and arrange the wires as they were originally placed!

For safety seasons, some parts use tapes, tubes or other insulating materials, and some parts are mounted away from the surface of printed circuit boards. Care is also taken with the positions of the wires by arranging them and using clamps to keep them away from heating and high voltage parts, so be sure to set everything back as it was originally placed.

#### ○ Make a safety check after servicing!

Check that all screws, parts and wires removed or disconnected when servicing have been put back in their original positions, check that no serviced parts have deteriorate the area around. Then make an insulation check on the external metal connectors and between the blades of the power plug, and otherwise check that safety is ensured.

(Insulation check procedure)

Unplug the power cord from the power outlet, disconnect the antenna, plugs, etc., and on the power. Using a 500V insulation resistance tester, check that the insulation resistance value between the inplug and the externally exposed metal parts (antenna terminal, headphones terminal, input terminal, etc.) is 1MΩ or greater. If it is less, the set must be inspected and repaired.

### **CAUTION** Concerning important safety parts

Many of the electric and the structural parts used in the set have special safety properties. In most cases these properties are difficult to distinguish by sight, and the use of replacement parts with higher ratings (rated power and withstand voltage) does not necessarily guarantee that safety performance will be preserved. Parts with safety properties are indicated as shown below on the wiring diagrams and the parts list in this service manual. Be sure to replace them with the parts which have the designated part number.

- (1) Schematic diagrams.....Indicated by the  mark.
- (2) Parts lists.....Indicated by the  mark.

The use of parts other than the designated parts could cause electric shocks, fires or other dangerous situations.

## NOTE FOR SCHEMATIC DIAGRAM

### WARNING:

Parts indicated by the  mark have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.

### CAUTION:

Before returning the set to the customer, be sure to carry out either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 millamps, or if the resistance from chassis to either side of the power cord is less than 460 kohms, the set is defective.

### WARNING:

DO NOT return the set to the customer unless the problem is identified and remedied.

### NOTICE:

ALL RESISTANCE VALUES IN OHM.  $k=1,000$  OHM /  $M=1,000,000$  OHM

ALL CAPACITANCE VALUES ARE EXPRESSED IN MICRO FARAD, UNLESS OTHERWISE INDICATED. P INDICATES MICRO-MICRO FARAD. EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL INPUT CONDITION. CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.

## NOTE FOR PARTS LIST

1. Parts indicated by "nsp" on this table cannot be supplied.
2. When ordering a part, make a clear distinction between "1" and "I" (i) to avoid mis-supplying.
3. A part ordered without specifying its part number can not be supplied.
4. Part indicated by "★" mark is not illustrated in the exploded view.

**WARNING:** Parts indicated by the  mark have critical characteristics. Use ONLY replacement parts recommended by the manufacturer.

## INSTRUCTIONS FOR HANDLING SEMI-CONDUCTORS AND OPTICAL UNIT

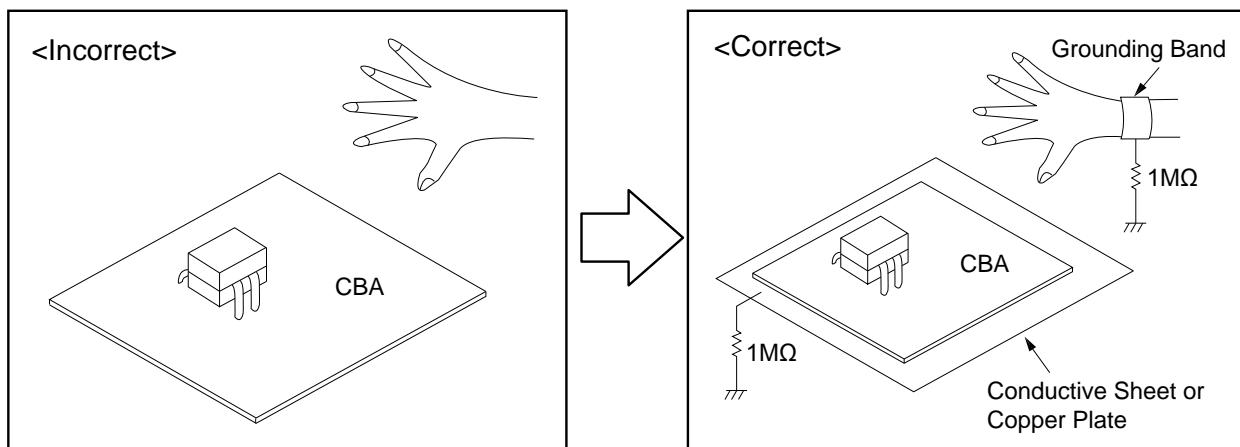
Electrostatic breakdown of the semi-conductors or optical pickup may occur due to a potential difference caused by electrostatic charge during unpacking or repair work.

### 1. Ground for Human Body

Be sure to wear a grounding band ( $1 M\Omega$ ) that is properly grounded to remove any static electricity that may be charged on the body.

### 2. Ground for Workbench

Be sure to place a conductive sheet or copper plate with proper grounding ( $1 M\Omega$ ) on the workbench or other surface, where the semi-conductors are to be placed. Because the static electricity charge on clothing will not escape through the body grounding band, be careful to avoid contacting semi-conductors with your clothing



### Personal notes:

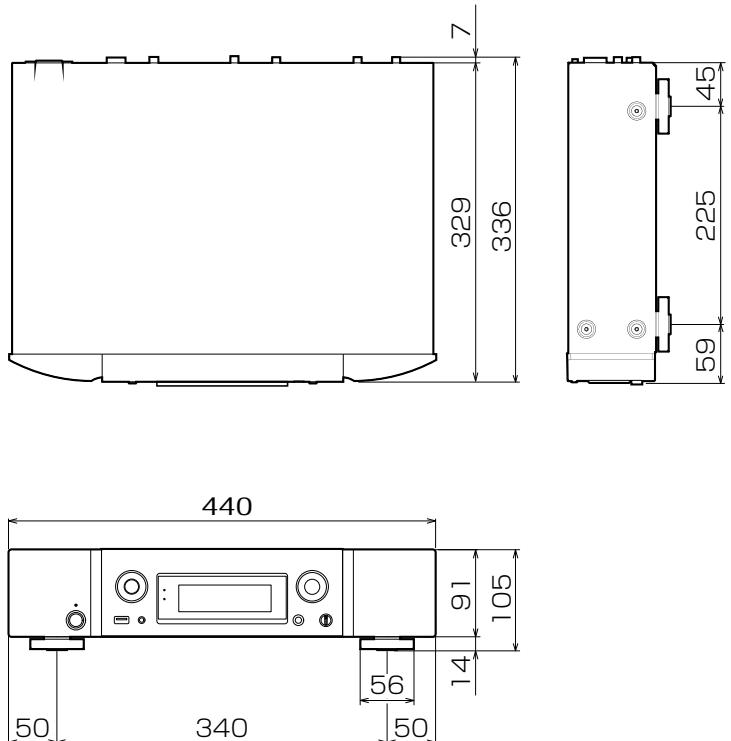
# TECHNICAL SPECIFICATIONS

<input type="checkbox"/> <b>Audio performance</b>	
• <b>Analog output</b>	
<b>Channels:</b>	2-channels
<b>Playable frequency range:</b>	2 Hz - 96 kHz
<b>Playable frequency response:</b>	"2 Hz - 50 kHz (-3 dB) (DSD mode, PCM sampling frequency: 192 kHz) 2 Hz -20 kHz (PCM sampling frequency: 44.1 kHz)"
<b>S/N:</b>	110 dB (Audible range)
<b>Dynamic range:</b>	106 dB (DSD/192 kHz) (Audible range) 101 dB (44.1 kHz)
<b>Harmonic distortion:</b>	0.0012 % (1 kHz, Audible range)
<b>Output level</b>	
<b>Unbalanced output:</b>	2.3 V RMS (PCM) 1.7 V RMS (DSD)
<b>Headphone output:</b>	30 mW/32 Ω/ohms (variable maximum)
• <b>Digital output</b>	
<b>Coaxial:</b>	0.5 Vp-p / 75 Ω/ohms
<b>Optical :</b>	-15 - -21 dBm
• <b>Digital input</b>	
<b>Coaxial:</b>	0.5 Vp-p / 75 Ω/ohms
<b>Optical :</b>	-27 dBm or later
<b>USB (Front):</b>	USB Type A (USB 2.0 High speed)
<b>USB (Rear):</b>	USB Type B (USB 2.0 High speed)
□ <b>General</b>	
<b>Power supply voltage/ frequency: F</b>	AC 100V, 50/60Hz
<b>Power supply voltage/ frequency: N</b>	AC 230 V, 50/60 Hz
<b>Power supply voltage/ frequency: U</b>	AC 120 V, 60 Hz
<b>Power supply voltage/ frequency: K</b>	AC 220 V, 50 Hz
<b>Power consumption (EN60065) :</b>	30W
<b>Power consumption in standby mode:</b>	0.4W
<b>Power consumption in "Network Control" - "On" mode:</b>	4W

## DIMENSION

### o Dimensions

Unit : mm



### o Weight : 7.2 kg

# PRECAUTIONS DURING SERVICE

## Initializing This Unit

Initialize this unit if you have replaced the microcomputer, one of the parts around the microcomputer.

1. Disconnect the AC plug of this unit to turn the power off.
2. When executing the initialization (User Reset) described in the Owner's Manual.  
Press the "INPUT" and "ENTER" buttons simultaneously while inserting the AC plug to turn the power on.  
"INITIALIZING" appears on the display.

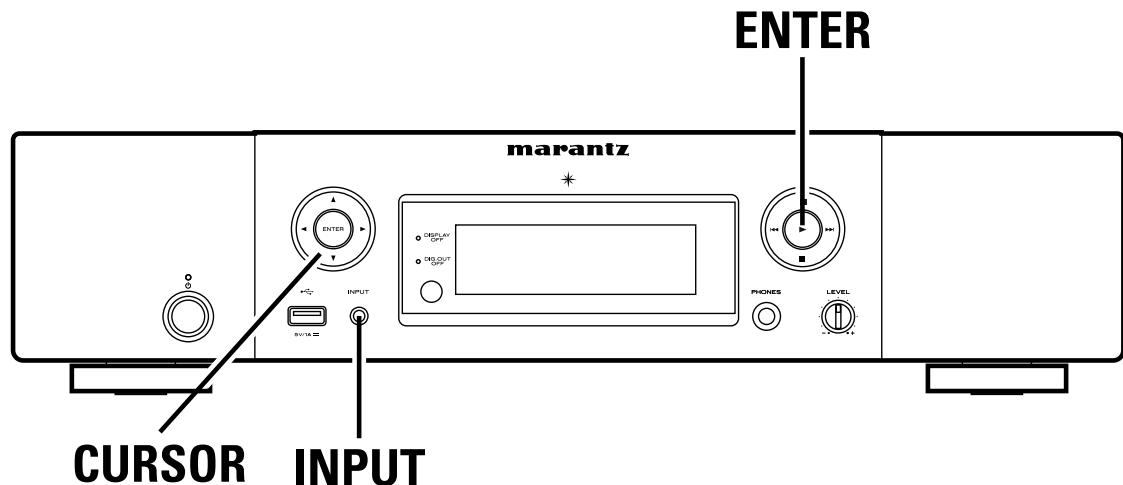
When executing the initialization Factory Initialization (Factory Reset).

Press the "INPUT" and CURSOR DOWN "▼" buttons simultaneously while inserting the AC plug to turn the power on.  
"Factory Reset" appears on the display.

See "[SPECIAL MODE](#)" on page 11 for details on the differences between the different types of initialization.

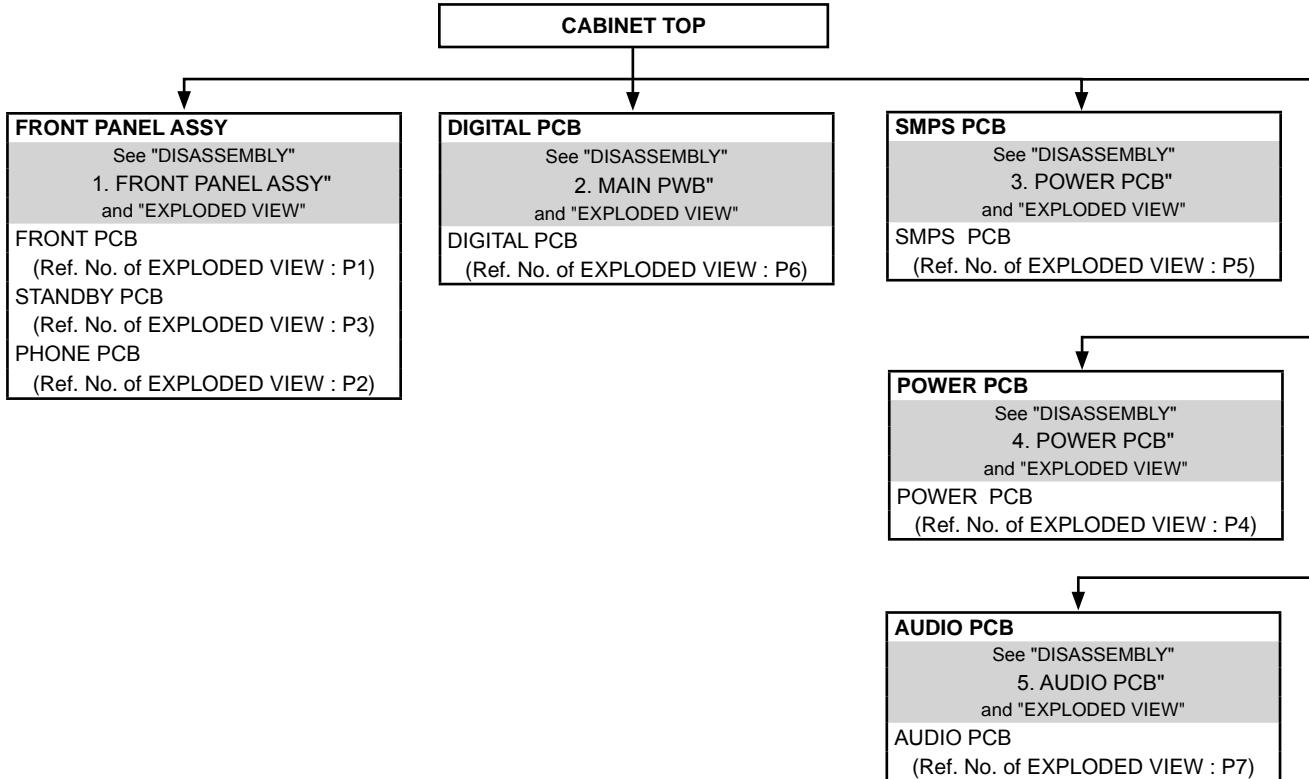
3. The unit then changes to the normal mode.

**NOTE :** • If the status in step 3 does not occur, start again from step 1.  
• Initializing the device restores settings configured by the user to the factory settings. Take note of your settings beforehand and reconfigure them after initialization.



# DISASSEMBLY

- Remove each part in the order of the arrows below.
- Reassemble removed parts in the reverse order.
- Read "Precautions During Work" before reassembling removed parts.
- If wire bundles are removed or moved during adjustment or part replacement, reshape the wires after completing the work. Failure to shape the wires correctly may cause problems such as noise.

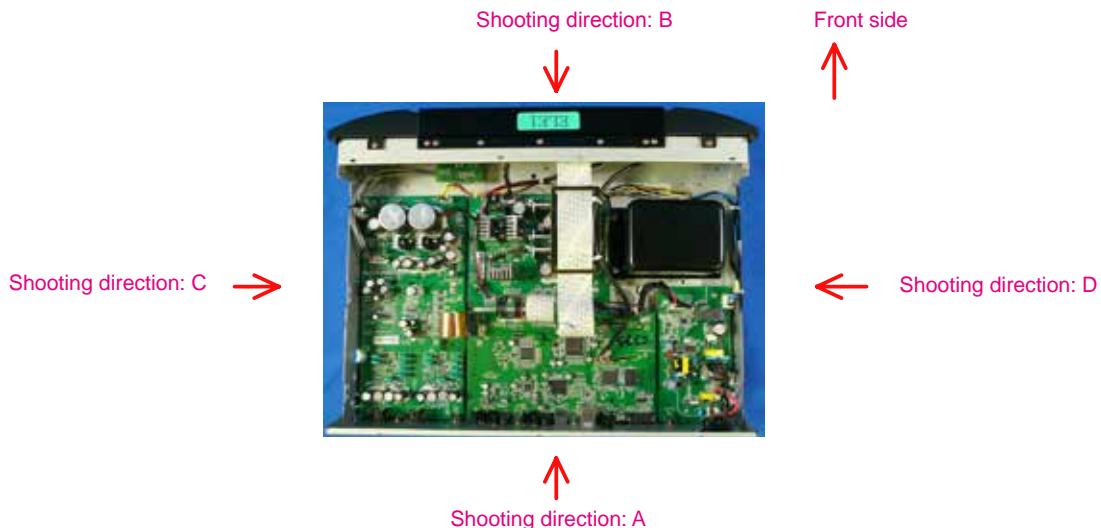


## Explanatory Photos for DISASSEMBLY

- The angles from which the photos are taken are shown by "Photo angle : A, B, C, D".
- See the diagram below about the shooting direction of each photograph.
- Photographs with no shooting direction indicated were taken from the top of the unit.
- The photograph is NA8005U1B model.

The viewpoint of each photograph

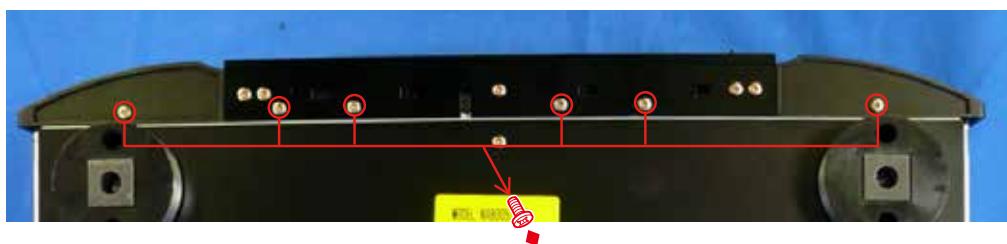
(Shooting direction:X) □ View from the top]



## 1. FRONT PANEL ASSY

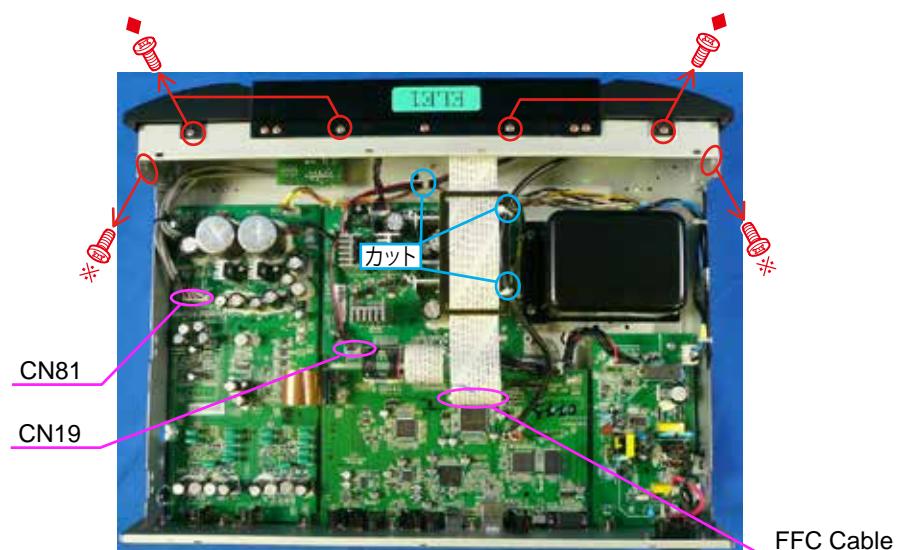
Proceeding: CABINET TOP → FRONT PANEL ASSY

- (1) Remove the screws.



Bottom side

- (3) Remove the connector wires and FFC. Remove the screws.



See "EXPLODED VIEW" for instructions on how to remove each PCB of the FRONT PANEL ASSY.

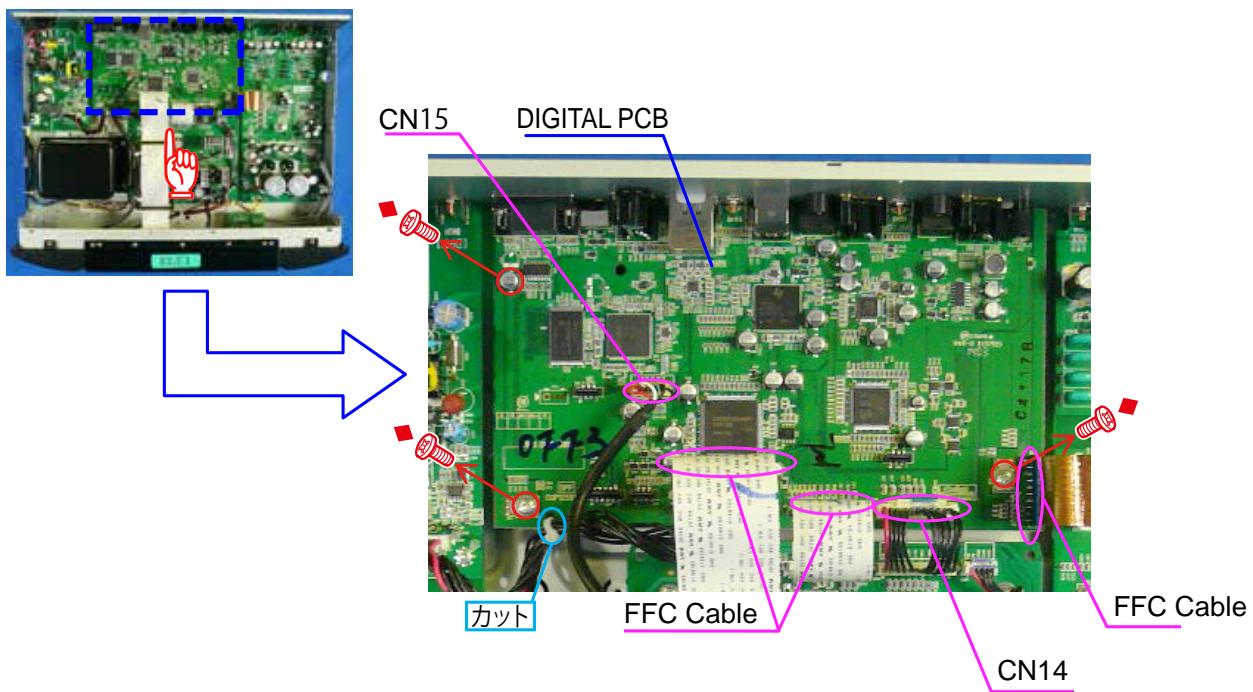
## 2. DIGITAL PCB

Proceeding: CABINET TOP → DIGITAL PCB

- (1) Remove the screws.



- (2) Remove the screws. Remove the connector wires and FFC.



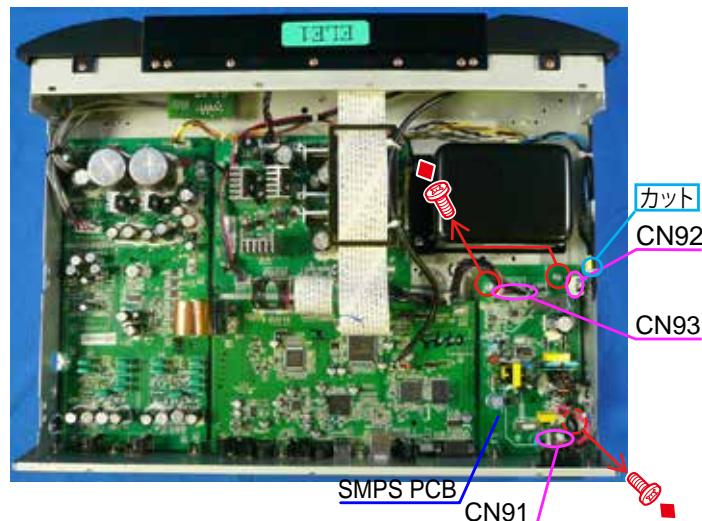
### 3. SMPS PCB

Proceeding: CABINET TOP → SMPS PCB

- (1) Remove the screw.



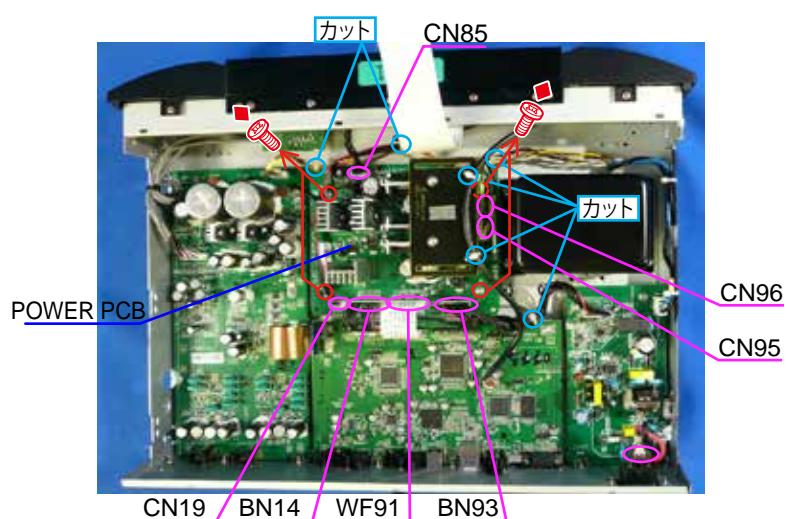
- (2) Remove the screws. Remove the connector wires and FFC.



### 4. POWER PCB

Proceeding: CABINET TOP → POWER PCB

- (1) Remove the screws. Remove the connector wires and FFC.



## 5. AUDIO PCB

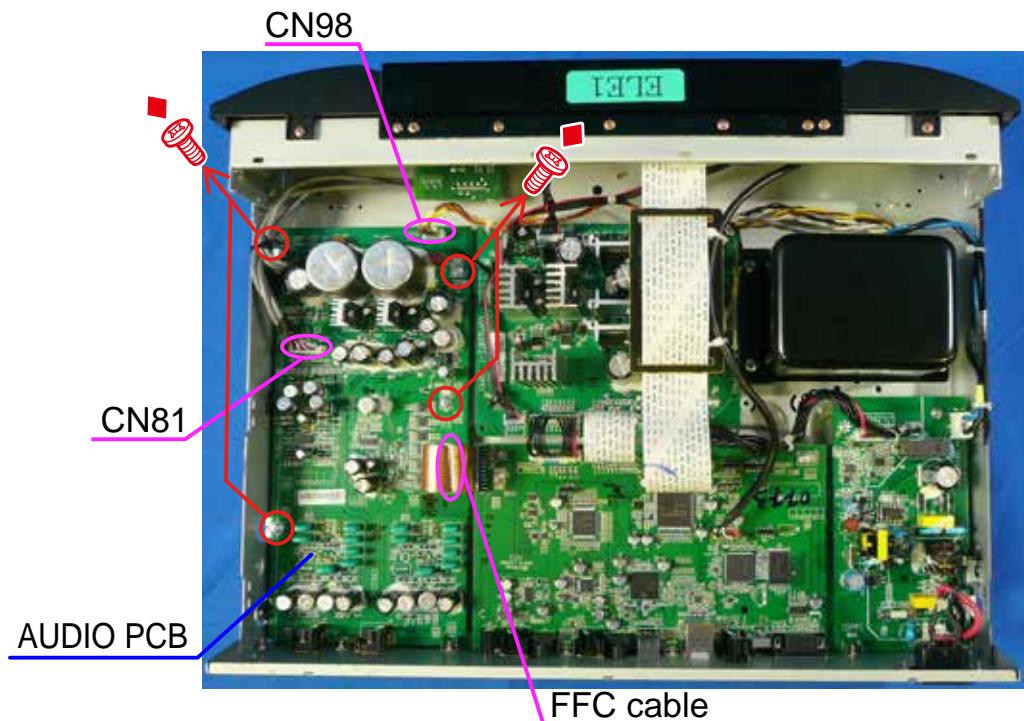
Proceeding: CABINET TOP → AUDIO PCB

- (1) Remove the screws.



Shooting direction A

- (2) Remove the connector wires and remove the screws.



## SPECIAL MODE

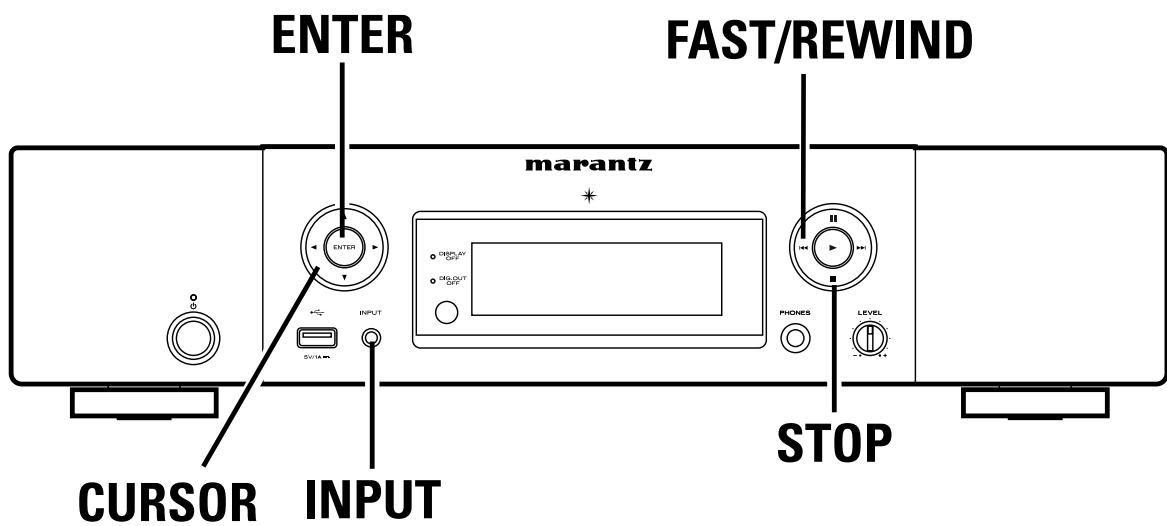
### Special Mode Configuration Buttons

No.1 - No.10 : Turn on the AC plug while pressing the button of A and B at the same time.

No.11:Hold down buttons A and B for at least 3 seconds while the power is on.

Each button continue to press until the lit of ON/STANDBY LED.

No.	Mode	Button A	Button B	Contents
1	Factory Initialization (Factory Reset)	INPUT	▼:CURSOR DOWN	Defaults to the setting value.
2	Initialization ( User Reset)	INPUT	ENTER	Except that it does not clear the version up information and the history of protection. See details "2. <a href="#">Updating by DPMS</a> " on page 29.
3	Version display	▲:CURSOR UP	-	Version Display
4	Product mode 2	INPUT	◀:CURSOR LEFT	Factory use.
5	Protection History Display	INPUT	▲:CURSOR UP	Latest view of Protection history.
6	DPMS force update mode	◀◀: FAST/REWIND	▲:CURSOR LEFT	DPMS update.
7	Update (by RS232C)	◀◀: FAST/REWIND	▼:CURSOR UP	Development/Factory use.
8	MAC Address rewrite	◀◀: FAST/REWIND	▶:CURSOR RIGHT	Development/Factory use.
9	Access to development server	◀◀: FAST/REWIND	ENTER	Development/Factory use.
10	Update (by USB)	◀◀: FAST/REWIND	◀:CURSOR LEFT	Updating by USB memory.
11	Control 4 Identify	■:STOP	▼:CURSOR DOWN	N/U Only. Function when a Control 4 compatible device is connected. (Identify function)



## 1. Factory Initialization Mode (Factory Reset)

Backup data initialization is carried out. Refer to Initialization Items (Default setting). After initialization, move on to normal mode.

\*Can't erase the Recently Played List. Recently Played List erase with User Reset.

Refer to [SPCIALMODE "2.Initialization moder (User Reset)"]

### CAUTION

Clear the Version information (such as rewriting failed log) .

Clear the history of protection.

### Startup display

All lights on display. And light the **STANDBY LED** (Orange), **DISPLAY OFF LED** (Red), **DIGI.OUT OFF LED** (Red). 2 seconds.



All lights on display. And light the **STANBY LED**(Orange). 2 seconds.



"Factory Reset" displayed for 5 seconds.



### Initialization Items (Default setting)

	Default
INPUT	Internet Radio
DIMMER	100%
Favorite list	Clear all
iPod mode	Direct mode
AUTO STANDBY	N : ON Others : OFF
Protection history	NO PROTECT
Network setting	DHCP (On)
Network Standby	OFF
Friendly Name	Marantz NA8005
Digital Out	ON
232C STANDBY	OFF

## 2. Initialization Mode (User Reset)

Backup data initialization is carried out. Refer to Initialization Items. After initialization, move on to normal mode.  
\*Can erase the Recently Played List.

### CAUTION

The difference is the following points.

- Version information (such as rewriting failed log) not cleared.
- History of protection does not cleared.
- Setting of the "Audio Out" is not changed.

### Startup display

"Initialized" is displayed for 5 seconds.



## 3. Version Display Mode

Menu items appear in the Add Version. Otherwise, normal operation.  
To exit this mode, unplug the power cord.

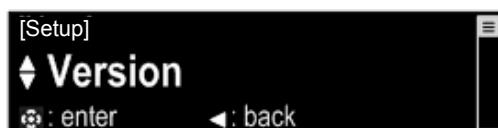
### Startup display

"Version" is displayed for 5 seconds.



### How to Display Version

Press INPUT button and Cursor △ / Cursor ▽ to select the Setup. Then press ENTER button.

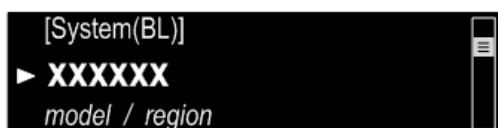


Press ENTER button.



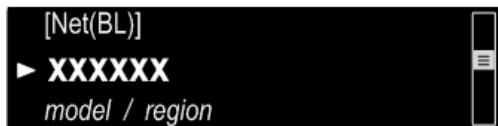
System u-com version is displayed.

Press Cursor ▽ button.



Boot loader version for the system microprocessor is displayed.

Press Cursor ▽ button.



Boot loader version for the networkc microprocessor is displayed.

Press **Cursor**  $\nabla$  button.



Image version for the network microprocessor is displayed.

Press **Cursor**  $\nabla$  button.



USB-DAC version is displayed.

Press **Cursor**  $\nabla$  button.



Sireal number is displayed. (Serial: MZ\_bccdddddd)

## 4. Product Mode2

Startup display

"Product Mode2" is displayed for 5 seconds.



The following settings for checking operation when the unit is manufactured are configured automatically.

- Sleep setting : 4 minutes
- Auto standby timer setting : 4 minutes

A 4-minute timer operates when the sleep setting is configured.

When auto standby is set, standby is performed under the following conditions.

Auto Standby Conditions

USB/iPod : No Connection or Unsupported Data or continue no operation and Stop state.

Network : No Connection or Unsupported Disc or continue no operation and Stop state.

Digital In : No Input(unlock)

To exit this mode, unplug the power cord.

## 5. Protection History Display Mode

Startup display

"Product Mode2" is displayed for 5 seconds.



To exit this mode, unplug the power cord.

- No history found



- DC protection occurred.



Case: +B/-B was short circuit.(+12V\_D, +12V\_A, -12V\_A or +29V was failed)

### How to delete Protection history (backup)

Protection history is deleted by pressing the **Cursor**  $\Delta$  button for more than 5 seconds when the protection history is displayed.



"No Protection" is displayed after the protection history is deleted.



Protection history is also deleted by factory initialization.

## 6. DPMS Force Update Mode

Updating the firmware by DPMS.

See "[2. Updating by DPMS](#)" on page 29.

### Error code table

- Preparation operation rewritten, Update error code checking. (Check ETHERNET unit)

Error Code	Details of Error code	Coping strategies
01	Login failed(DPMS Access Login Incorrect notification)	Reset and update again. Carry out the update in an environment that has little network load.
02	Login failed(DPMS Access Server Busy information)	Carry out the update in an environment that has little network load.
03	Login failed(DPMS Access link failure information)	Check the network connection. Carry out the update in an environment that has little network load.
04	Firm Info response acquisition error received	Check the network connection. Carry out the update in an environment that has little network load.
05	Firm Info response acquisition TimeOut	Check the network connection. Carry out the update in an environment that has little network load.
06	All Firm Info response acquisition error received	Check the network connection. Carry out the update in an environment that has little network load.
07	All Firm Info response acquisitionTimeOut	Check the network connection. Carry out the update in an environment that has little network load.
08	Main Firm Info response acquisition error received	Check the network connection. Carry out the update in an environment that has little network load.
09	Main Firm Info response acquisition TimeOut	Check the network connection. Carry out the update in an environment that has little network load.
0A	DownLoad failed ((NG)information received)	Check the network connection. Carry out the update in an environment that has little network load.
0B	DownLoad failed((ServerBusy) information received)	Check the network connection. Carry out the update in an environment that has little network load.
0C	DownLoad failed((connection failed)information received)	Check the network connection. Carry out the update in an environment that has little network load.

- Firm error codes at the main microprocessor rewritten. (Check the main microprocessor )

Error Code	Details of Error code	Coping strategies
10	Firm Info response acquisition TimeOut(Main rewrite Firmware received failure(TimeOut))	Turn off and on the power. Updating starts automatically.
11	Firm Info response acquisition received error(Main rewrite Firmware received failure(Error))	Turn off and on the power. Updating starts automatically.
12	Firm Info response acquisition received error (Main rewrite Firmware received data incorrect(CheckSumError))	Turn off and on the power. Updating starts automatically.
13	Rewrite failure (BlockErase failed before Main rewriting )	Turn off and on the power. Updating starts automatically.
14	Rewrite failure (BlockWrite failed before Main rewriting)	Turn off and on the power. Updating starts automatically.
15	Rewrite failure (Verify incorrect after Main rewriting)	Turn off and on the power. Updating starts automatically.
20	Failed to acquire the IP Address after transitioning to the Boot Loader Mode (AutoIP)	Carry out the update in an environment that has little network load.
21	Failed to acquire the IP Address after transitioning to the Boot Loader Mode (AutoIP)	Carry out the update in an environment that has little network load.
22	Login failed (DPMS Access Login Incorrect notification), after moved BootLoaderMode. (AutoIP)	Carry out the update in an environment that has little network load.
23	Received "Server congestion" notification, after moved BootLoaderMode. (AutoIP)	Carry out the update in an environment that has little network load.
24	Received "connection failed", after moved BootLoaderMode. (AutoIP)	Carry out the update in an environment that has little network load.
36	Login failure(DPMSAccess Login incorrect information)	Carry out the update in an environment that has little network load.
37	Login failure(DPMSAccess Server busy information)	Carry out the update in an environment that has little network load.
38	Login failure(DPMSAccess connection failed information)	Check the network connection. Carry out the update in an environment that has little network load.
39	Login failure(DPMSAccess access TimeOut)	Check the network connection. Carry out the update in an environment that has little network load.
3A	DownLoad failure(Download error (NG)information received)	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
3B	DownLoad failure(Download error (ServerBusy) information received)	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.

3C	DownLoad failure(Download error (connection failed) information received)	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
3D	Failed to acquire the IP Address after transitioning to the Boot Loader Mode (AutoIP)	"Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load."
3E	Failed to acquire the IP Address after transitioning to the Boot Loader Mode (TimeOut)	"Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load."

- CX870 system Firmware error codes when rewriting. (Check the ETHERNET unit)

Error Code	Details of Error code	Coping strategies
A0	Net not connected	Check the network connection. Carry out the update in an environment that has little network load.
A1	Net Connection TimeOut can not get status	Check the network connection. Carry out the update in an environment that has little network load.
A2	Login failed	Check the network connection. Carry out the update in an environment that has little network load.
A3	Login failed	Check the network connection. Carry out the update in an environment that has little network load.
A4	Login failed	Check the network connection. Carry out the update in an environment that has little network load.
A6	Error receiving response FirmInfo acquisition	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
A7	FirmInfo Get Response TimeOut	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
A8	Net not connected	Check the network connection. Carry out the update in an environment that has little network load.
A9	Net Connection TimeOut can not get status	Check the network connection. Carry out the update in an environment that has little network load.
AA	After download request, Login Failed	Check the network connection. Carry out the update in an environment that has little network load.
AB	After download request, Login Failed	Check the network connection. Carry out the update in an environment that has little network load.
AC	After download request, Login Failed	Check the network connection. Carry out the update in an environment that has little network load.
AE	Failure of DownLoad	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
AF	Failure of DownLoad	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
B0	Failure of DownLoad	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.
B2	Update error	Turn off and on the power. Updating starts automatically. Carry out the update in an environment that has little network load.

Failure to update, after the move again DM860 itselfe display retry processing

Update retry  
Please wait...

## **7. Update Mode (by RS232C)**

The firmware update using PC via RS232C.

Upgrade by DPMS. No display.

To exit this mode, unplug the power cord.

## **8. MAC Address Rewrite Mode**

Rewriting the MAC address mode.

Production / development for, there is no detailed description.

To exit this mode, unplug the power cord.

## **9. Access to Development Server Mode**

Production / development for, there is no detailed description.

To exit this mode, unplug the power cord.

## **10. USB Update Mode (by USB)**

Turn on the AC plug while pressing the button of FAST/REWIND "◀◀" and CURSOR LEFT "◀" at the same time.

See details "[1. Updating by USB](#)" on page 30".

## **11. Control 4 Identify Mode**

Hold down buttons STOP "■" and CURSOR DOWN "▼" for at least 3 seconds while the power is on.

The "Identify" function for a Control4 compatible device is executed.

The following is displayed before returning to the normal display.



### **CAUTION**

This only operates for N/U.

Wait for a short time after this unit has started, and configure this setting after the DM860 module has started.

### Personal notes:

## PROCEDURE AFTER REPLACING THE MICROPROCESSOR, ETC.

The procedure after replacing the u-COM (microprocessor), flash ROM, etc. is as follows.

PCB Name	Ref. No.	Description	Procedure after Replacement	備考
MAIN	IC11	R5F56108VNFP	B	Main
MAIN	IC24	H27U1G8F2BTR-BC	B	Network
MAIN	IC32	MX25L4006EM1I-12G	B	USB

Procedure after Replacement

**A** : The software has been written. The software is not written at the time of replacement.

**B** : The software has been written. The software may need to be rewritten by version updates. Check the version.

**C** : The software has not been written. The software needs to be written after replacement.

See "**Firmware Update Procedure**" for information on writing the software.

**D** : The software has been written. Be sure to rewrite with the latest software for your service region.

See "**Firmware Update Procedure**" for information on writing the software.

## 2. Updating by DPMS

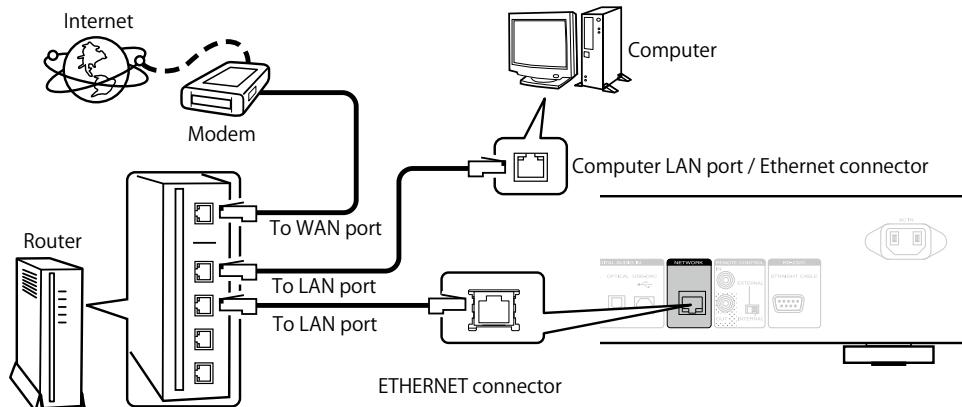
Download the latest firmware from the internet and update the firmware.

### 2.1. Network Connection

#### (1) System Requirements

- A broadband internet connection
- Modem
- Router
- Ethernet cable (CAT-5 or greater recommended)

#### (2) Settings



### 2.2. Check and update the firmware

Check whether new firmware is available. It is also possible to check approximately how long the update will take.

- (1) Turn on the power pressing ON/STANDBY button.
- (2) Press INPUT and Cursor  $\Delta$ /  $\nabla$  buttons. Select the Setup, then press ENTER button.
  - Press Cursor  $\Delta$ /  $\nabla$  buttons select to General. Press ENTER button.
  - Press Cursor  $\Delta$ /  $\nabla$  buttons select to Firmware. Press ENTER button.
  - Press Cursor  $\Delta$ /  $\nabla$  buttons select to Update. Press ENTER button.
  - Press Cursor  $\Delta$ /  $\nabla$  buttons select to Check for Update. Press ENTER button.
- (3) Press the ENTER button.
  - The latest version of the firmware uploaded to the web is displayed.
  - If the latest firmware version is on the web, proceed to (4).
  - If the latest firmware is already installed, press the INPUT button to close the Update menu.
- (4) Press ENTER button. Select "YES", then press ENTER button.
- (5) Firmware Update will be started.

#### --- Precautions for Updates ---

- The environment and settings must allow connection to broadband Internet for updates.
- Never turn off the power before an update is completed.
- It takes around 1 hour to complete the update.

Once an update is started, normal operations cannot be performed until it is completed.

The GUI menu settings and image adjustment settings of this unit may be initialized.

Take note of your settings beforehand and reconfigure them after the update.

# FIRMWARE UPDATE PROCEDURE

## 1. Updating by USB

The latest firmware can be downloaded to a USB memory for updates.

### 1.1. Connecting to the USB Memory

#### (1) Preparation

- USB format: Prepare a USB memory formatted in FAT16 or FAT32.
- Do not run the USB memory through a hub.
- Do not connect a computer to the USB port of this unit using a USB cable.
- Do not use an extension cable when connecting the USB unit.

### 1.2. Update and prepare for the update file

- Copy the update files to the USB memory.
- Insert the USB memory in the USB port. Should be connected to the USB connector on the front of the unit.



- Turn on the AC plug while pressing the button of FAST/REWIND "8" and CURSOR LEFT "0" at the same time.
- The following message appears on the display:



- Press the "ENTER" button, and firmware update starts on this unit.
  - if updating all devices, hold down buttons "INPUT" for at least 3 seconds.
- The following message appears on the display:



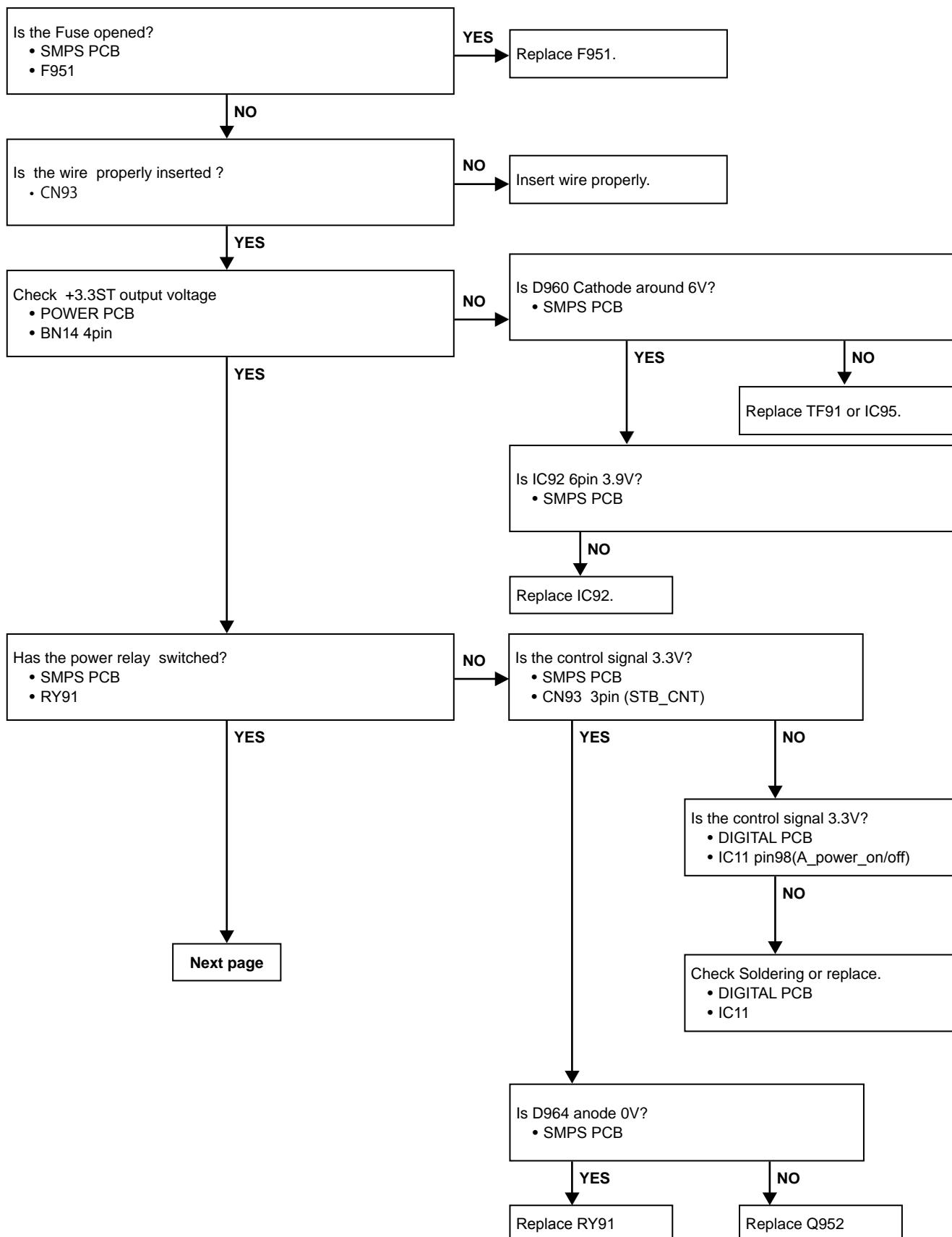
- This unit is turned off after the update is completed. • Disconnect the AC plug of this unit to turn the power off.

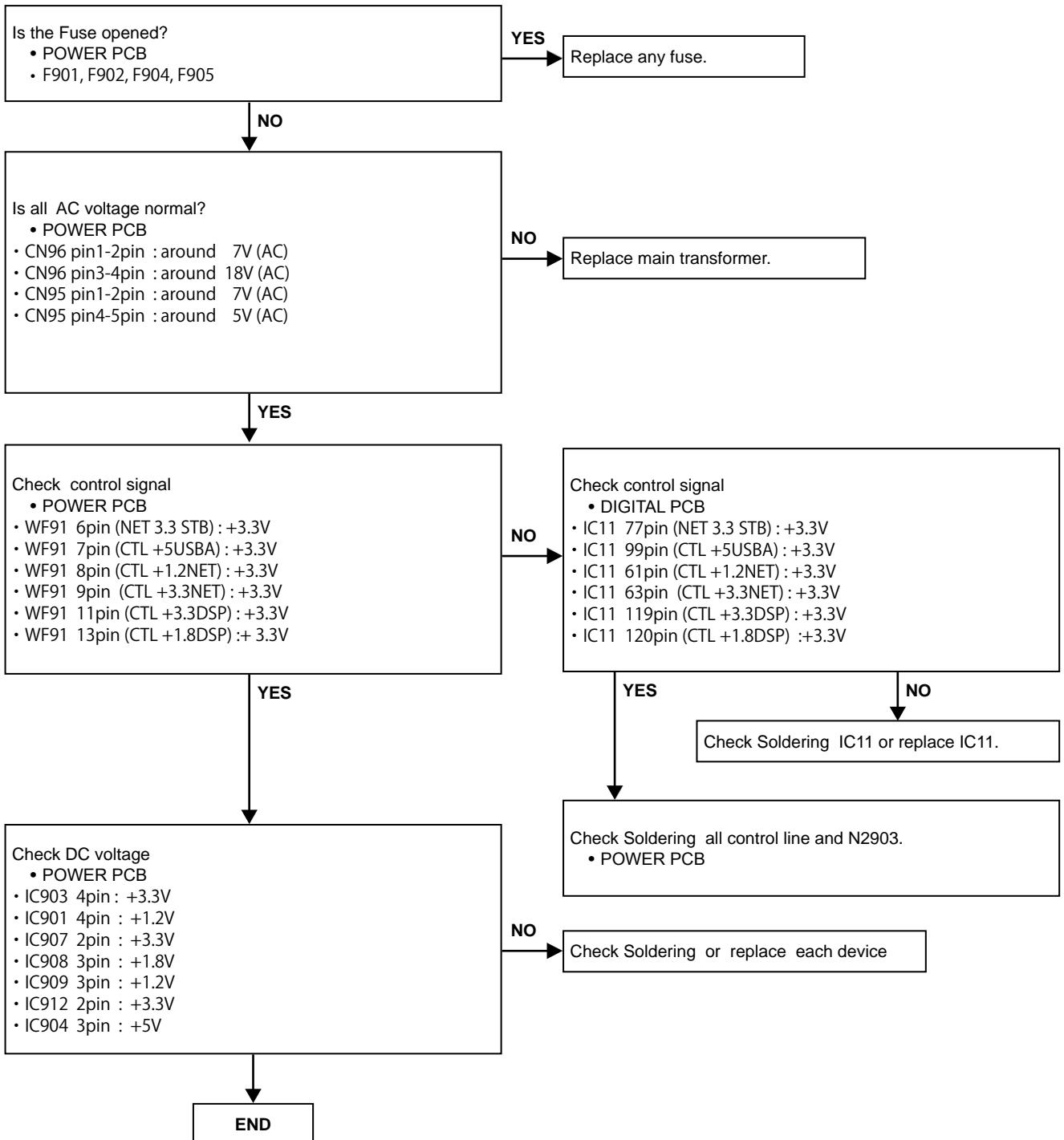
#### --- Precautions for Updates ---

- Never remove the USB memory before the update is finished.
  - Never turn off the power before an update is completed.
- Once an update is started, normal operations cannot be performed until it is completed.

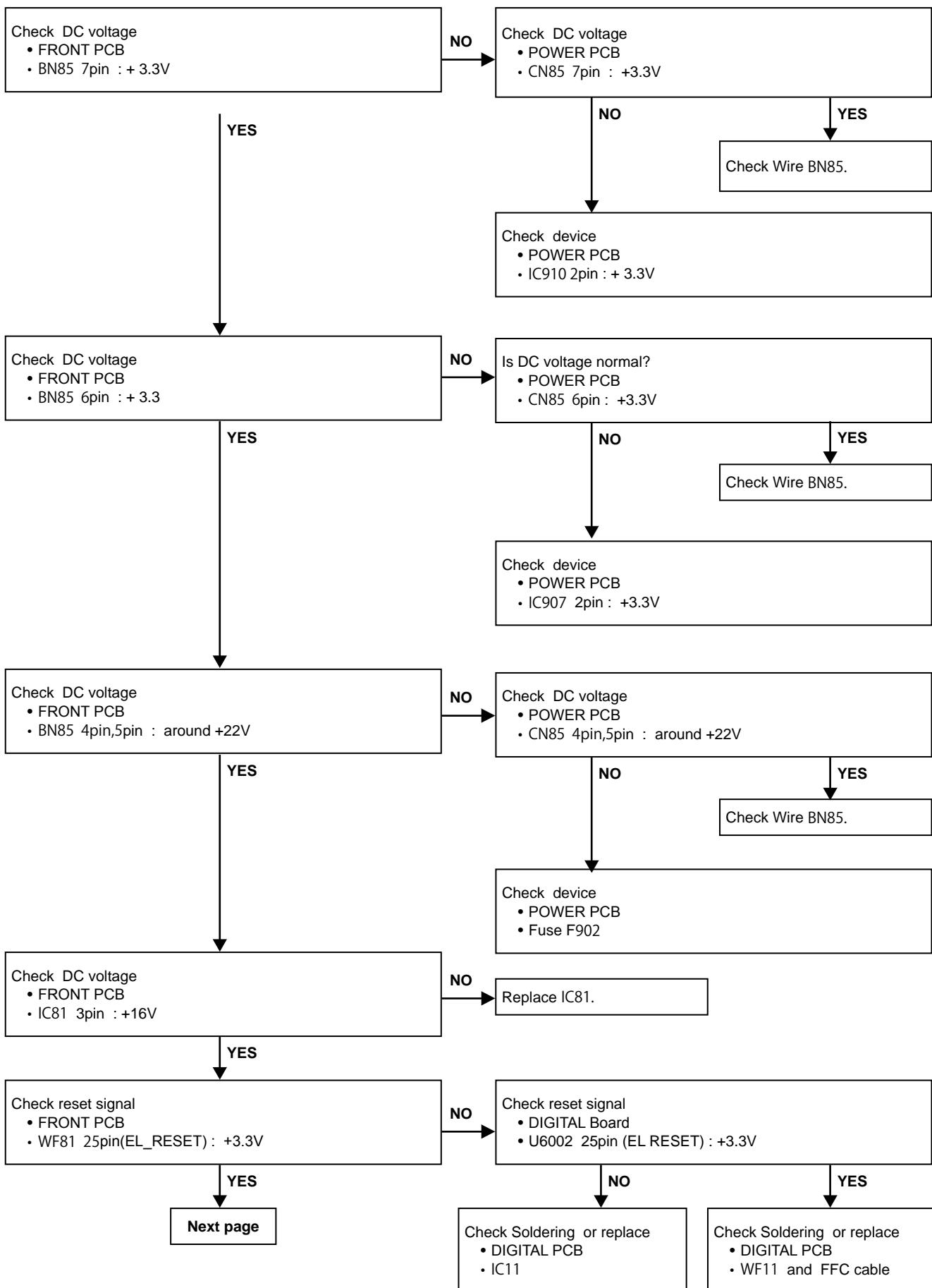
# TROUBLE SHOOTING

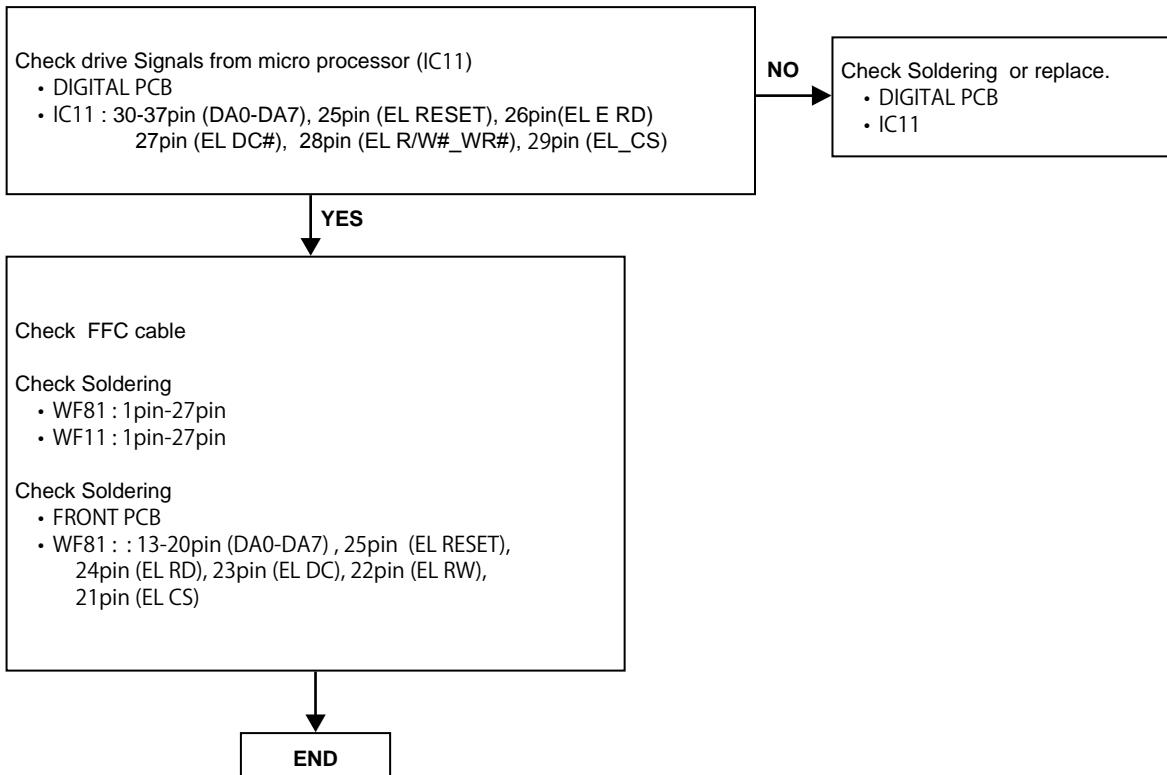
## 1. Power not turn on.





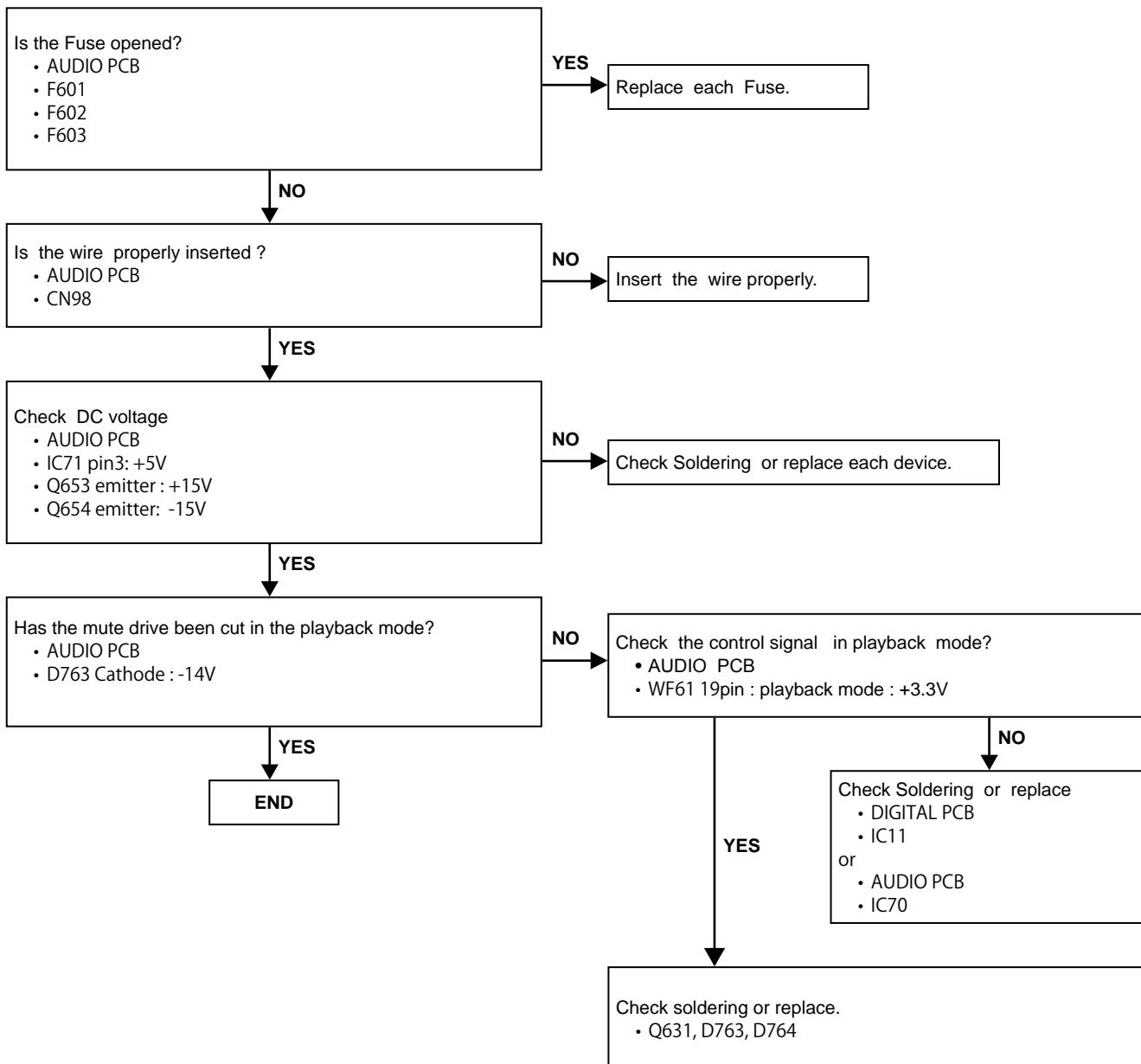
## 2. OLED doesn't Light



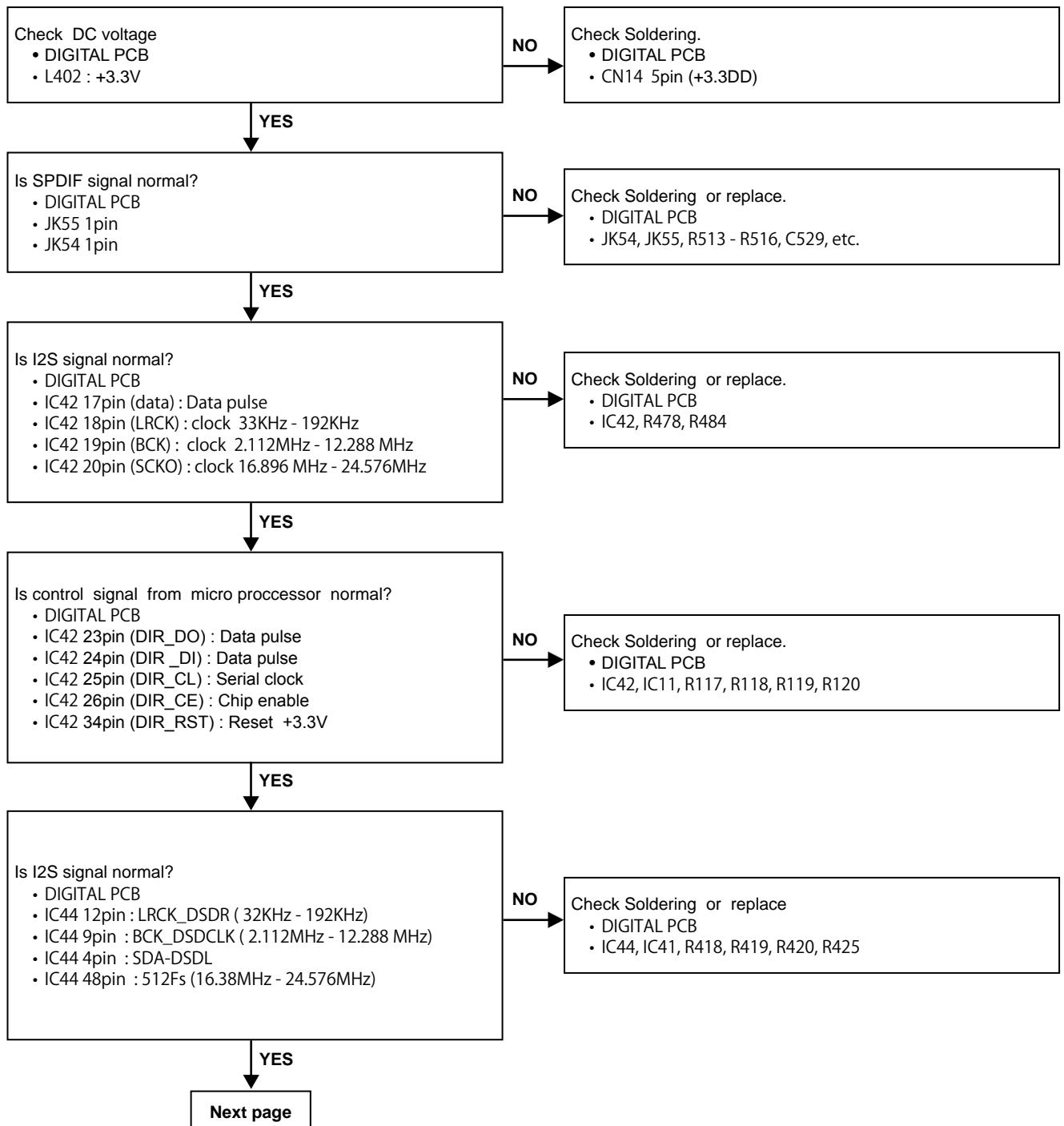


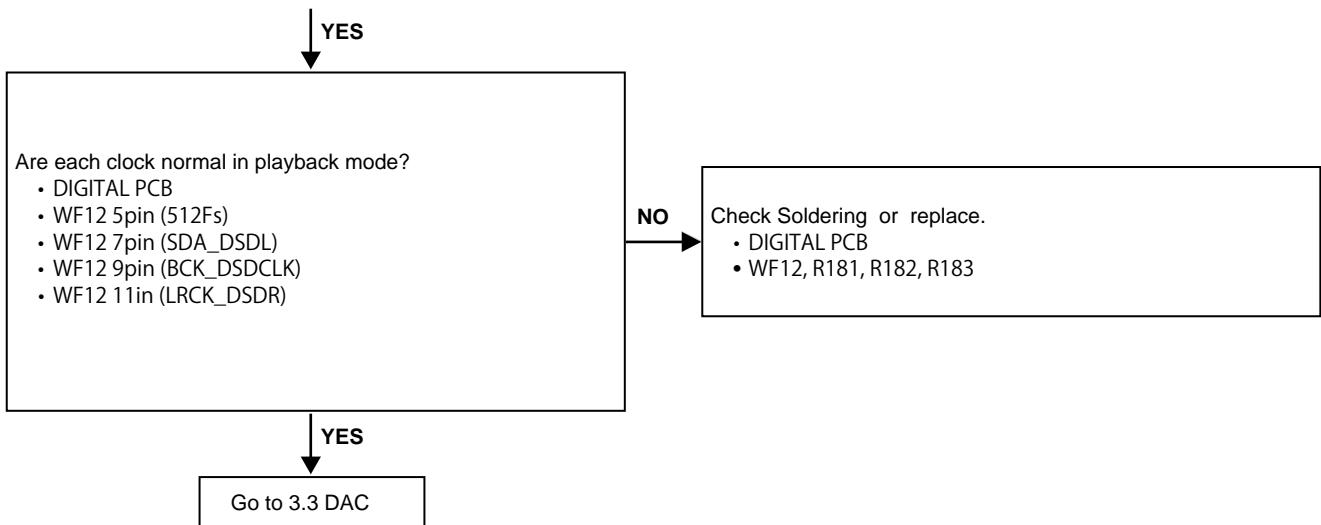
### 3. No Sound, Noise generated

#### 3.1. COMMON

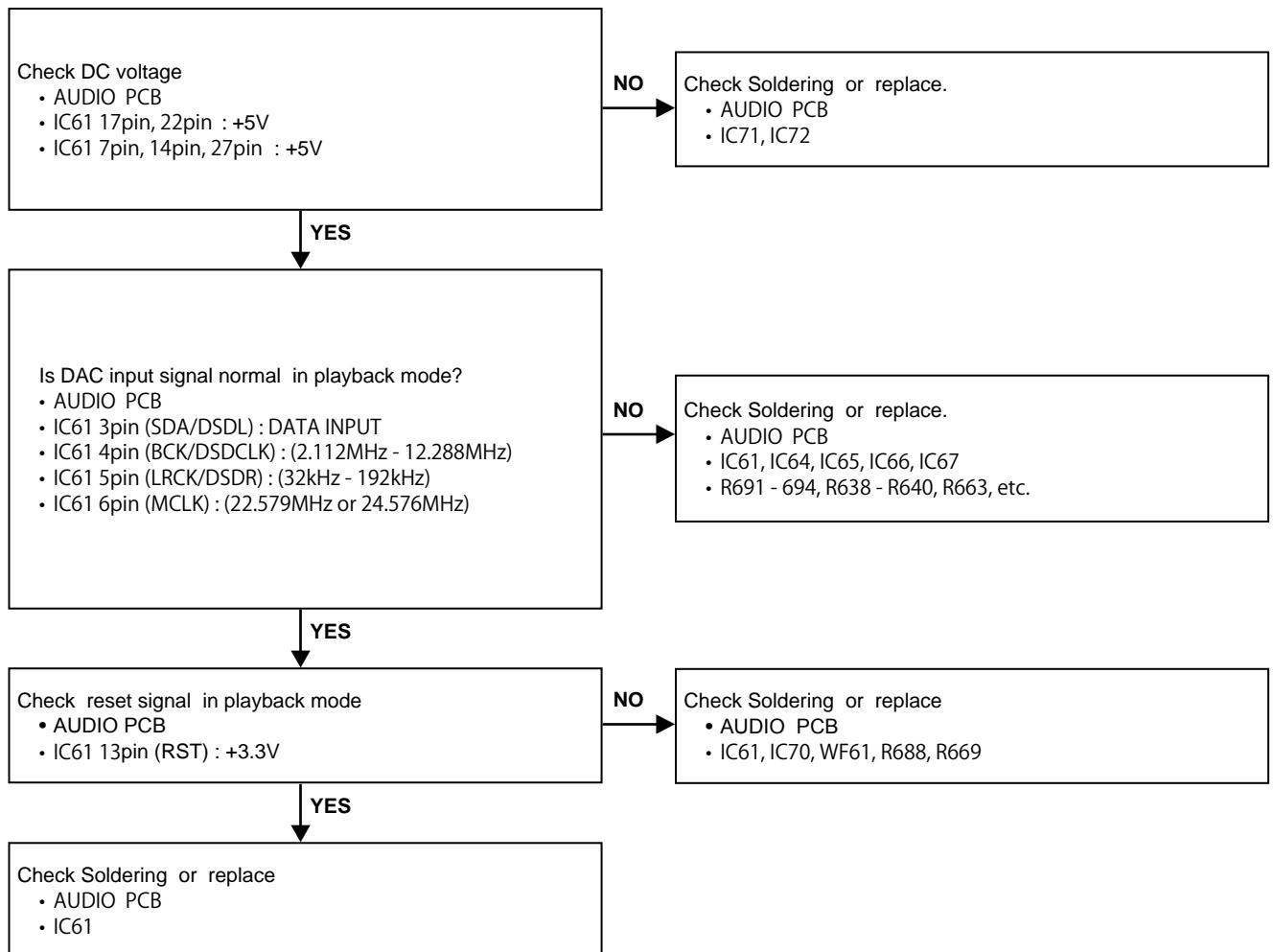


### 3.2. COAXAL,OPTICAL

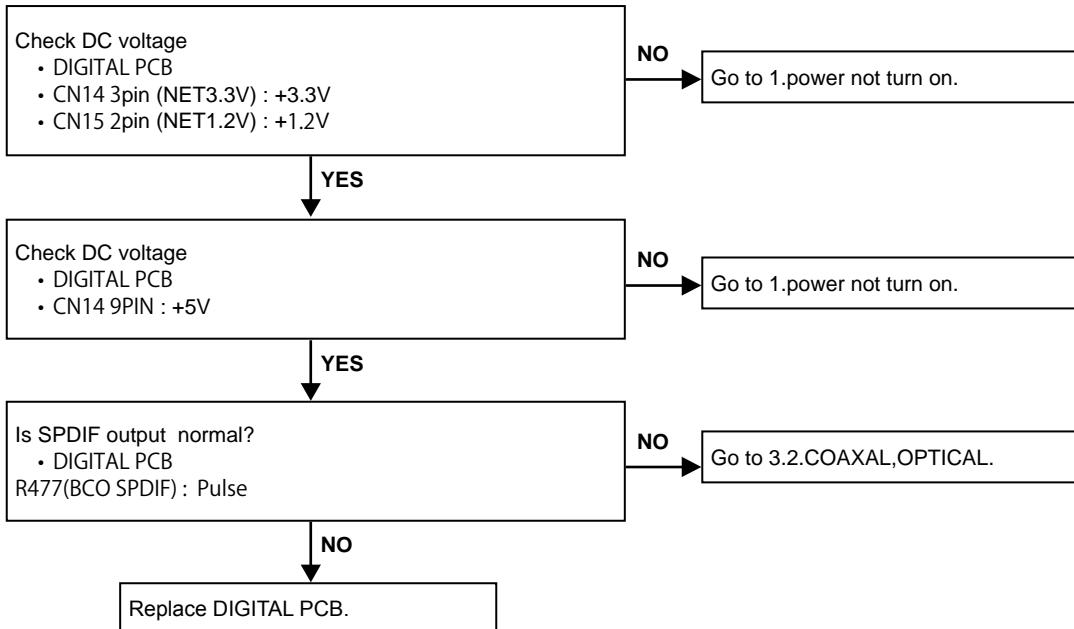




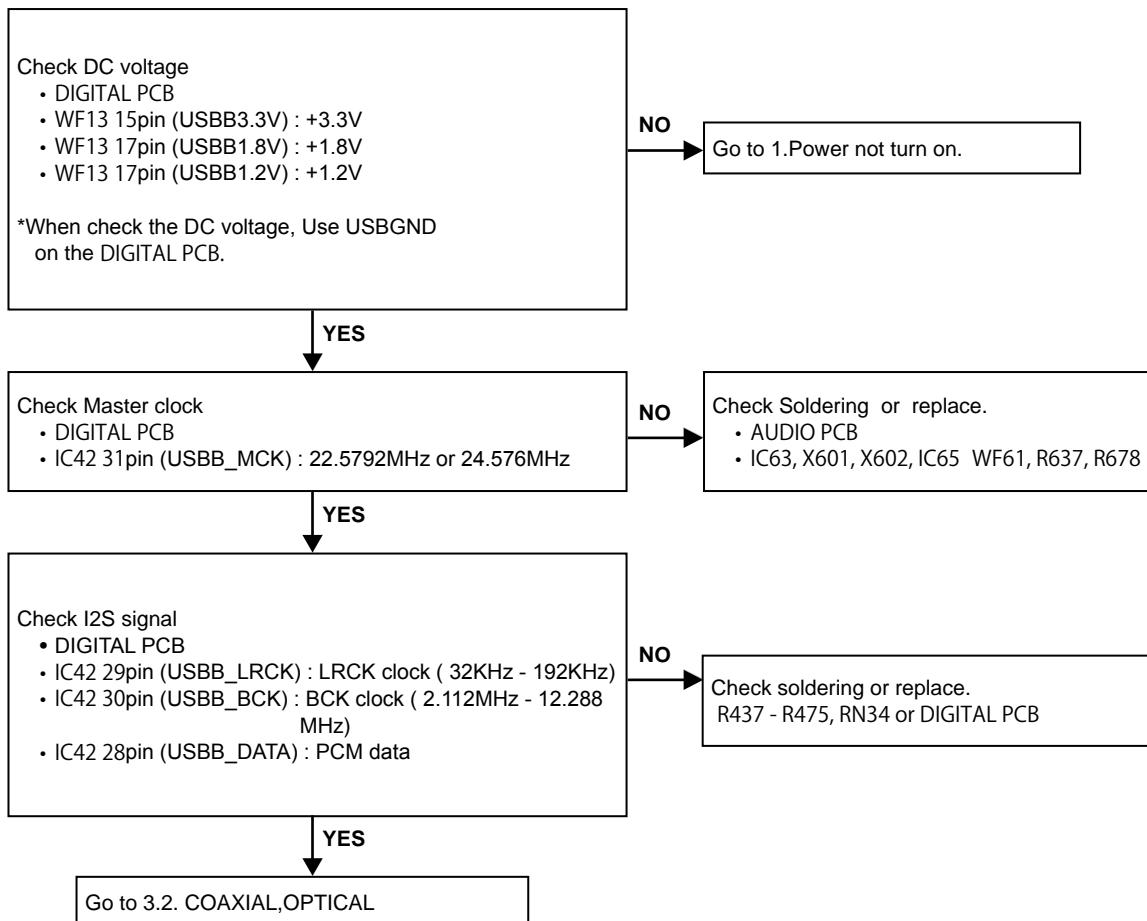
### 3.3. DAC



### 3.4. USB A/ETHERNET



### 3.5. USB B



Personal notes:

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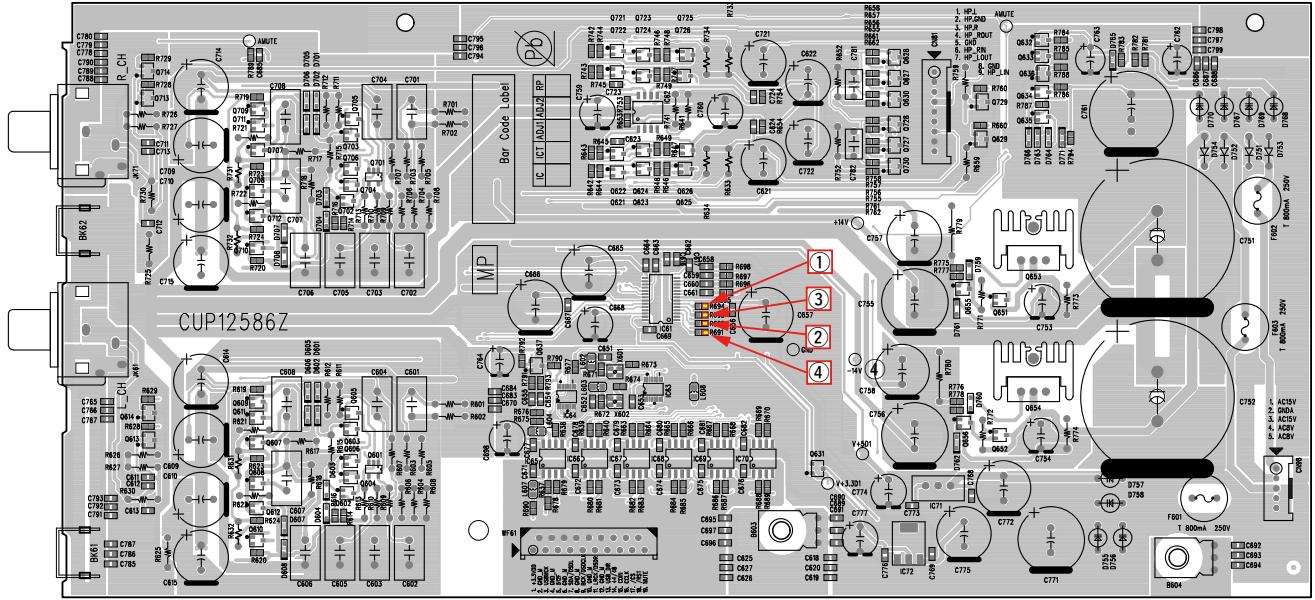
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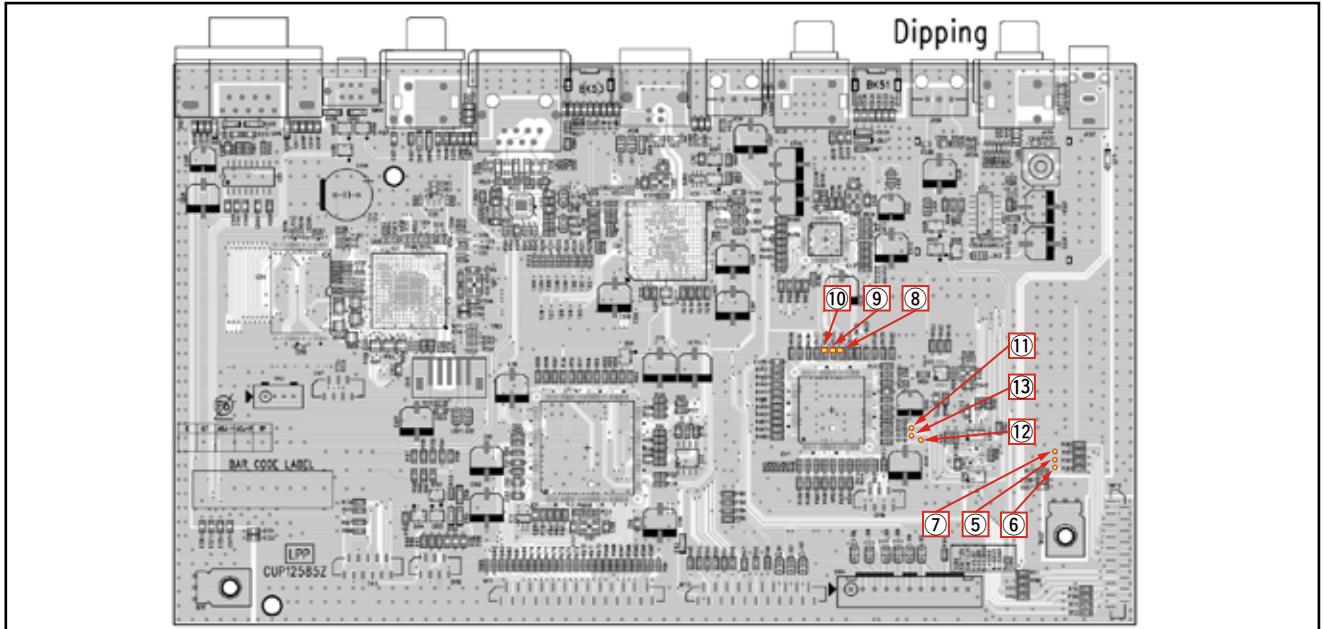
## MEASURING METHOD AND WAVEFORMS

(It is better to use wires for extending between the probe and test points.)

### CUP12586Z AUDIO PCB: TEST POINT



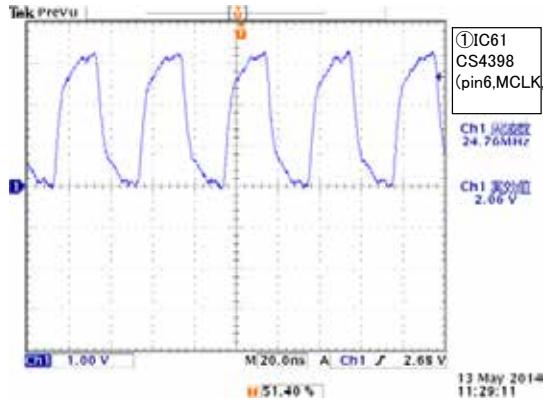
### CUP12585Z DIGITAL PCB: TEST POINT



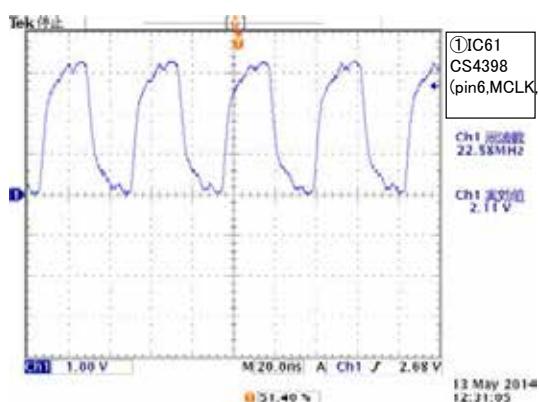
①	MCLK	R694
②	SCLK	R692
③	LRCK	R693
④	SDIN	R691
⑤	BCK_DSDCLK	R182
⑥	LRCK_DSDR	R183
⑦	SDA_DSDL	R181
⑧	DIR_BCK	R434
⑨	DIR_LRCK	R435
⑩	DIR_DATA	R436
⑪	DAC_BCK	R420
⑫	DAC_LRCK	R418
⑬	DAC_DATA	R419

## WAVEFORMS

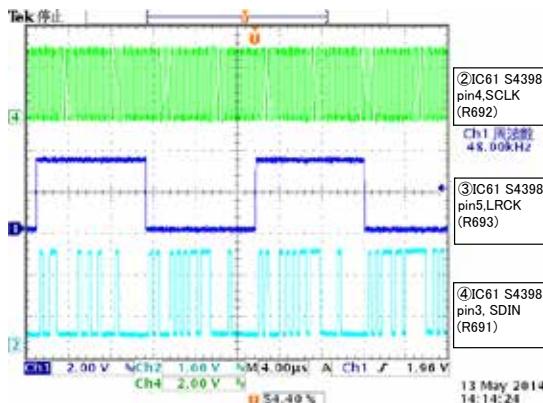
1. MASTER CLOCK (ex. PCM Playback from COAX IN, FS=48K)



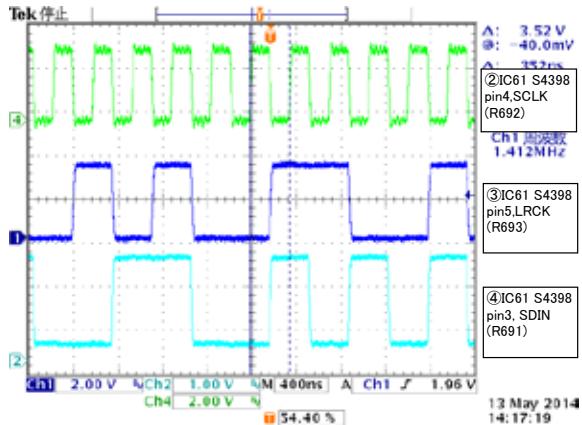
2. MASTER CLOCK (ex. DSD64 Playback from USBB)



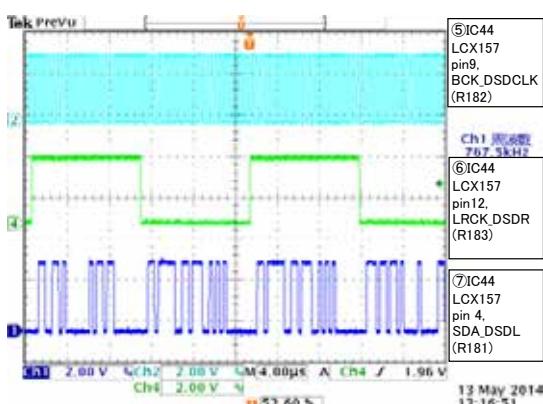
3. I<sup>I</sup>2 Input to DAC, CS4398 (ex. PCM Playback from COAX IN, FS=48K)



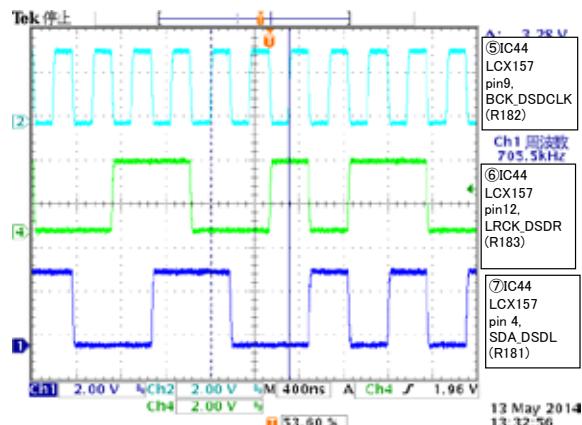
4. DSD64 Input to DAC, CS4398 (ex. DSD64 Playback from USBB)



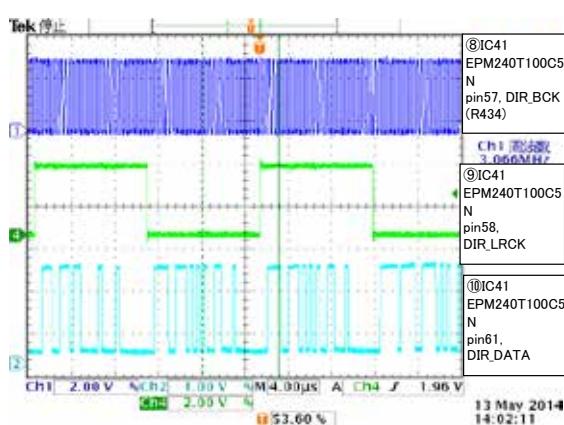
5. IIS Signal OUTPUT from DIGITAL PWB (ex. PCM from COAX IN, FS=48K)



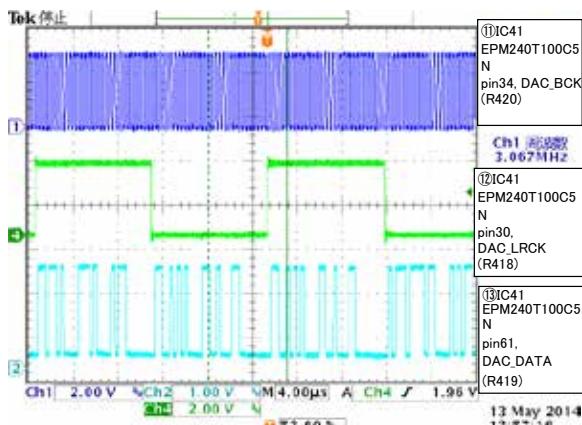
6. DSD64 Signal OUTPUT from DIGITAL PWB (ex. DSD64 Playback from USBB)



7. PLD INPUT I<sup>I</sup>2 (ex. PCM from COAX IN, FS=48K)

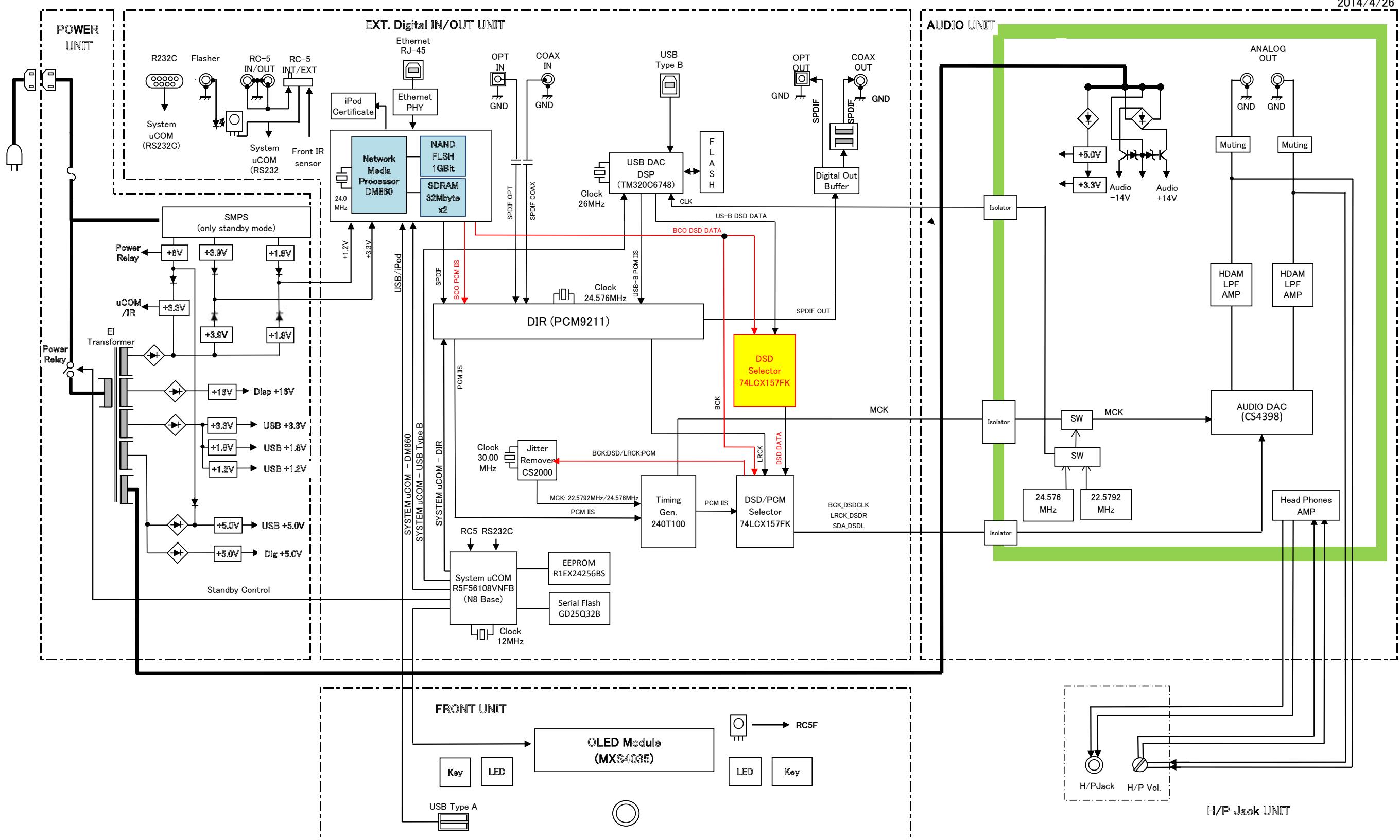


8. PLD OUTPUT I<sup>I</sup>2 (ex. PCM from COAX IN, FS=48K)

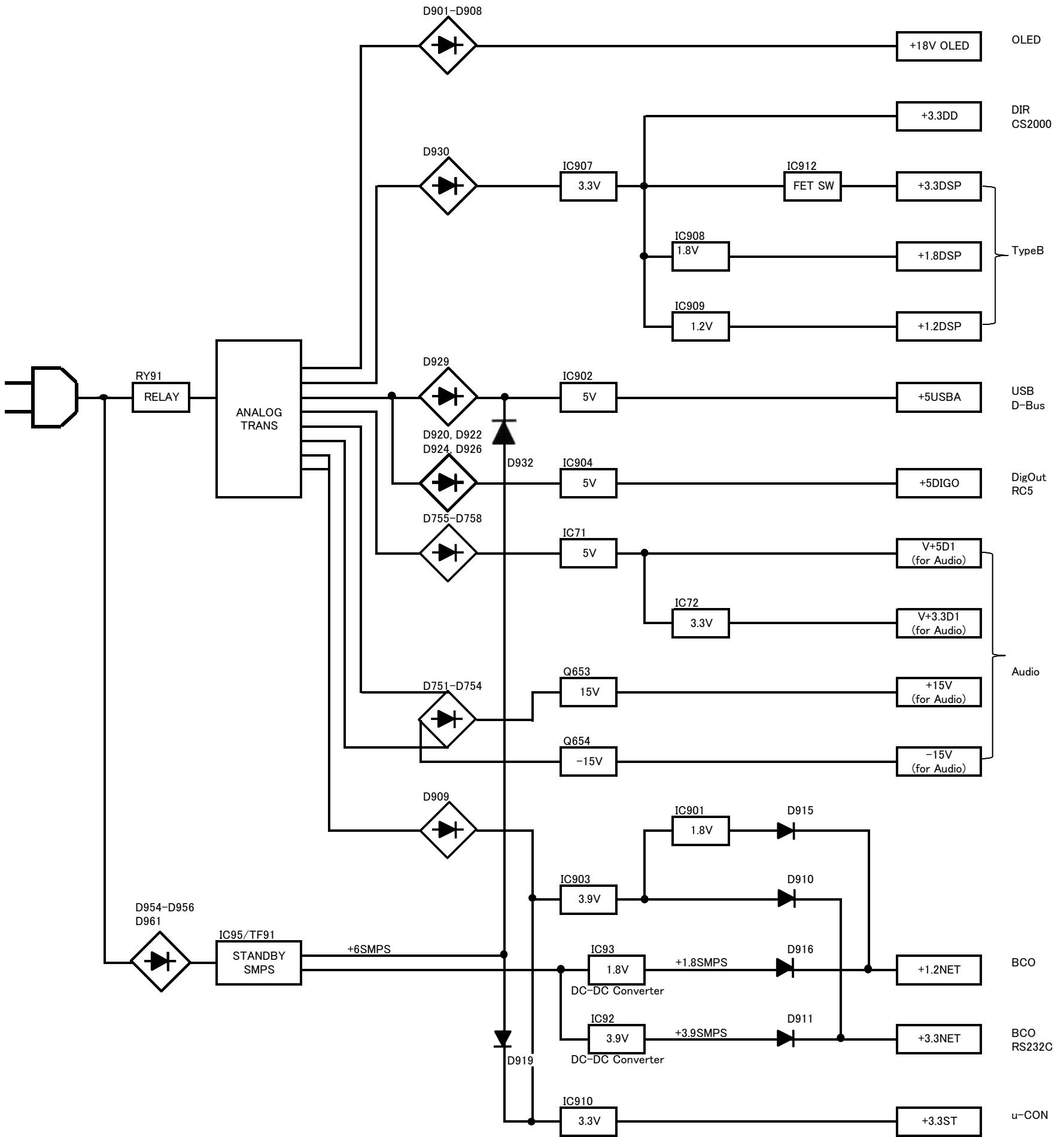


# BLOCK DIAGRAM

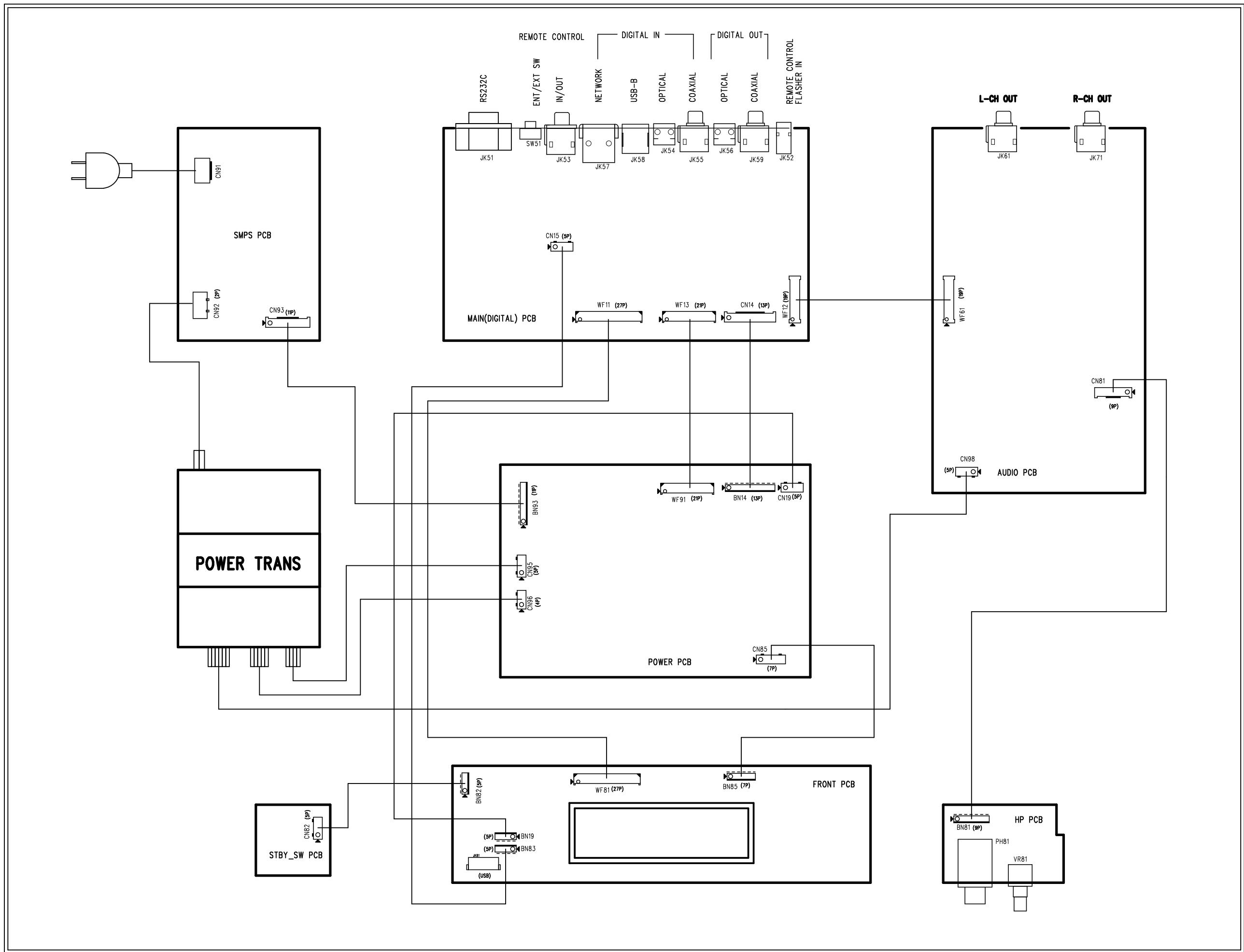
2014/4/26



## POWER DIAGRAM



## WIRING DIAGRAM

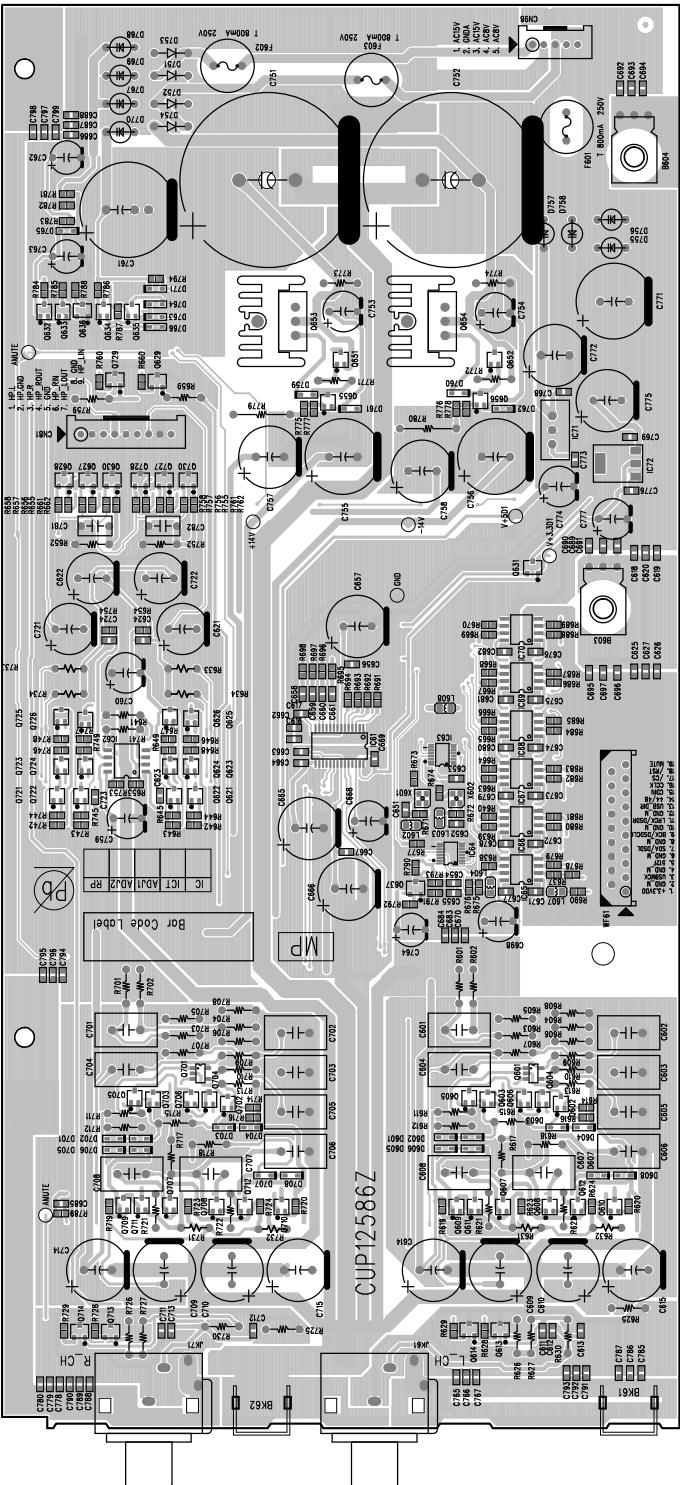


# PRINTED CIRCUIT BOARDS

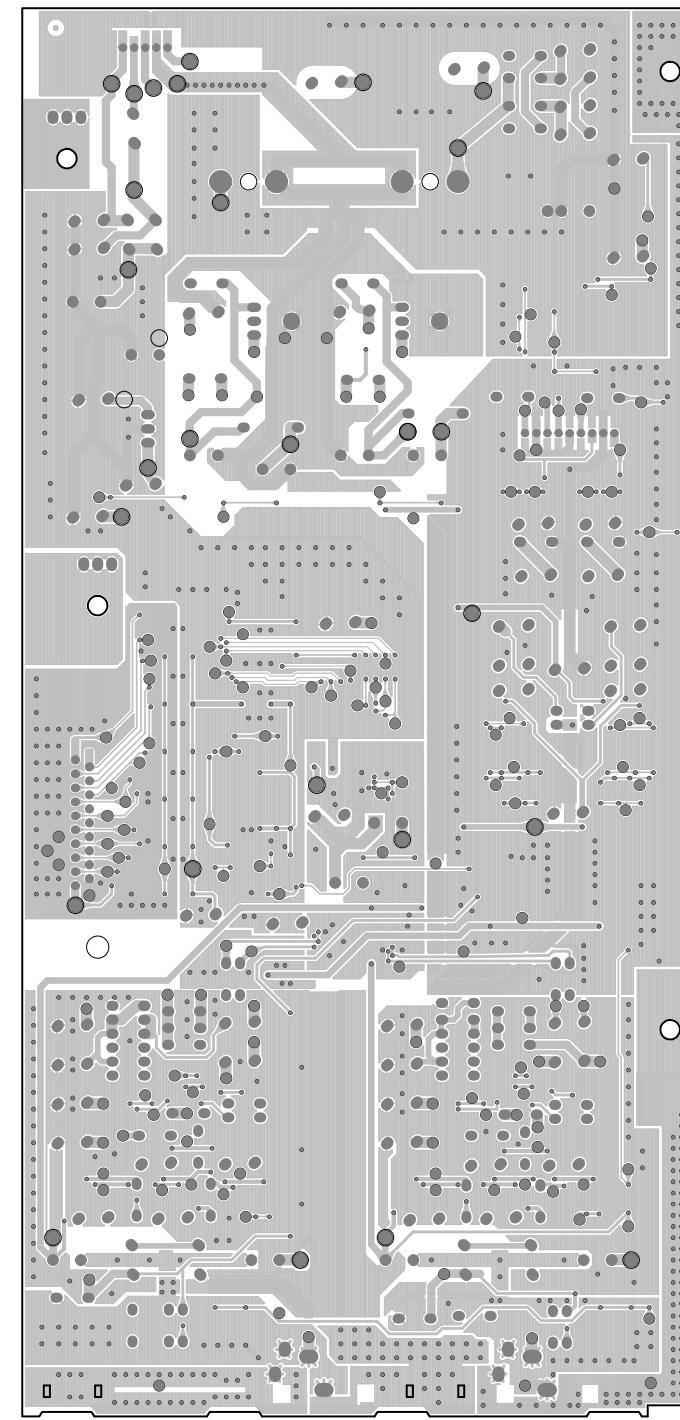
## Lead-free Solder

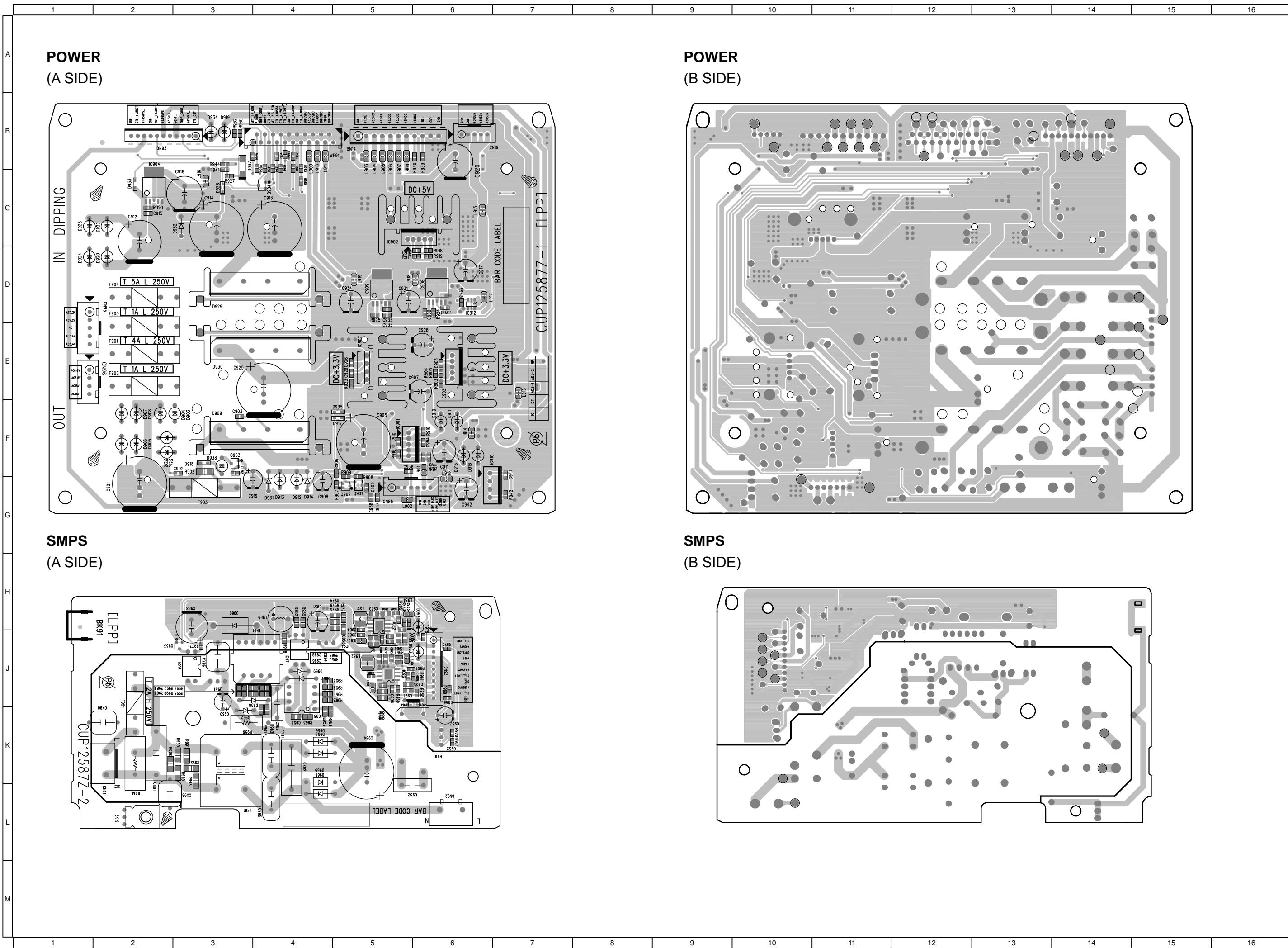
When soldering, use the Lead-free Solder (Sn-Ag-Cu).

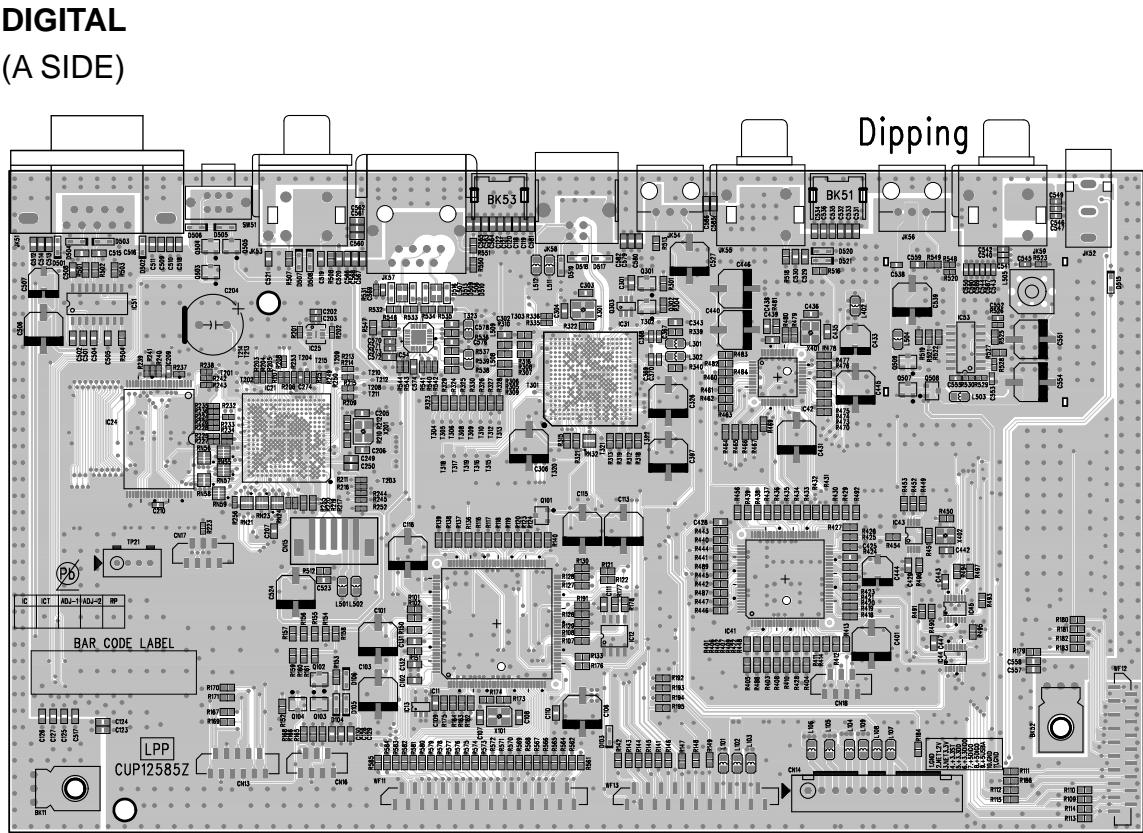
AUDIO  
(A SIDE)



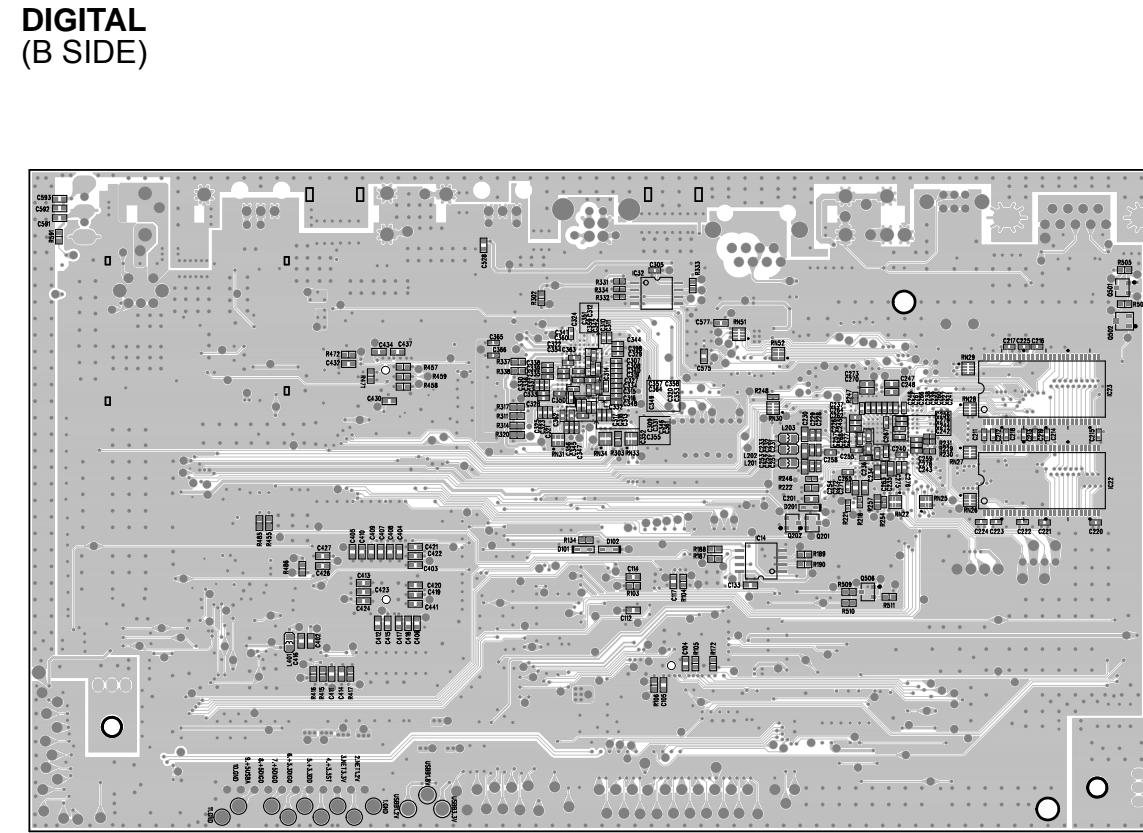
AUDIO  
(B SIDE)



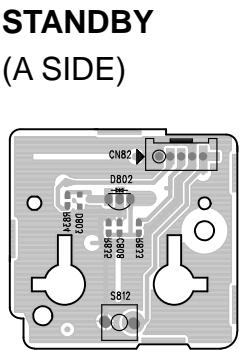




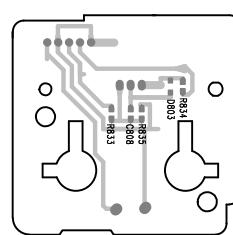
**DIGITAL**  
**(A SIDE)**



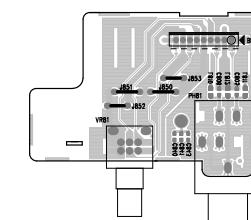
**DIGITAL  
(B SIDE)**



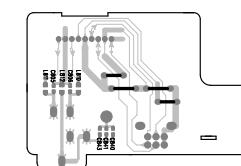
**STANDBY**  
**(A SIDE)**



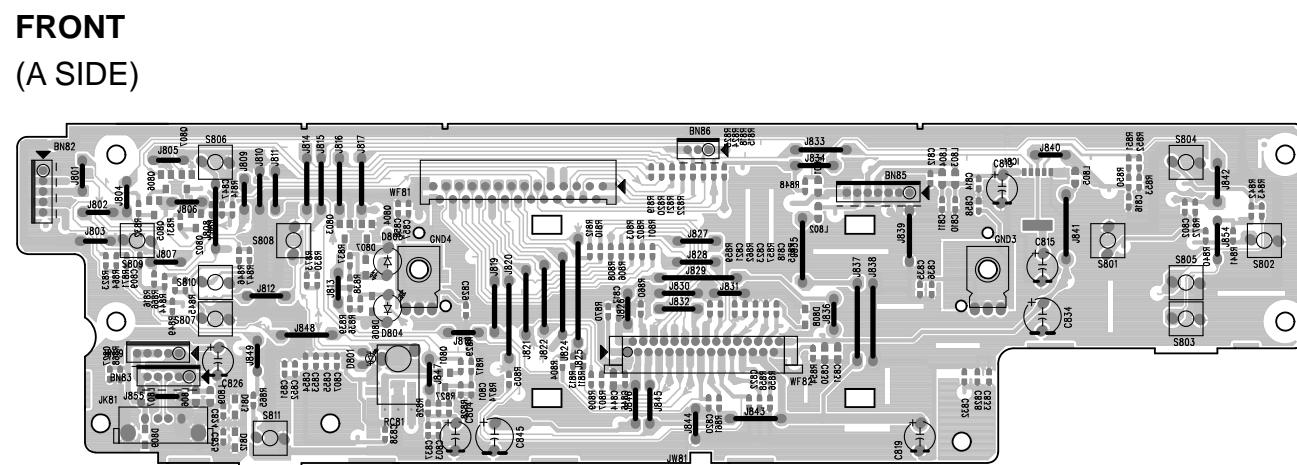
**STANDBY**  
**(B SIDE)**



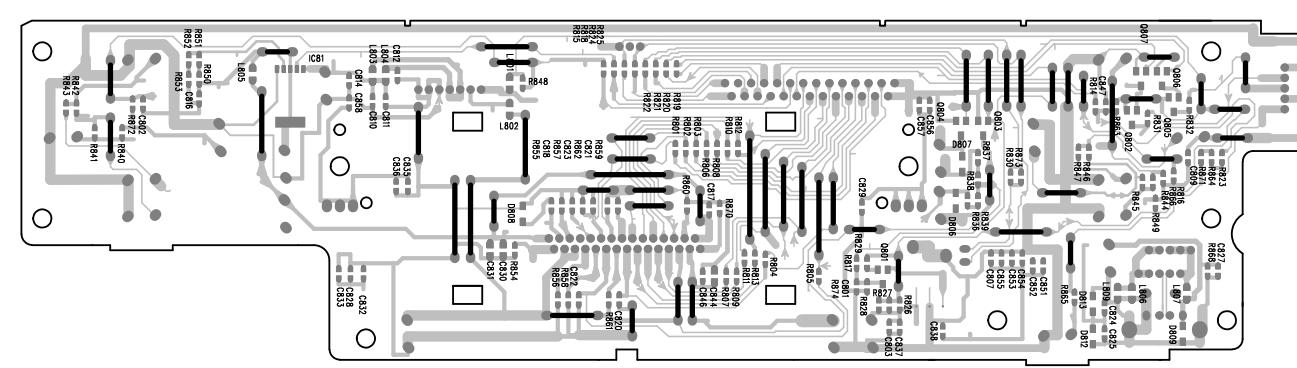
**PHONE**  
**(A SIDE)**



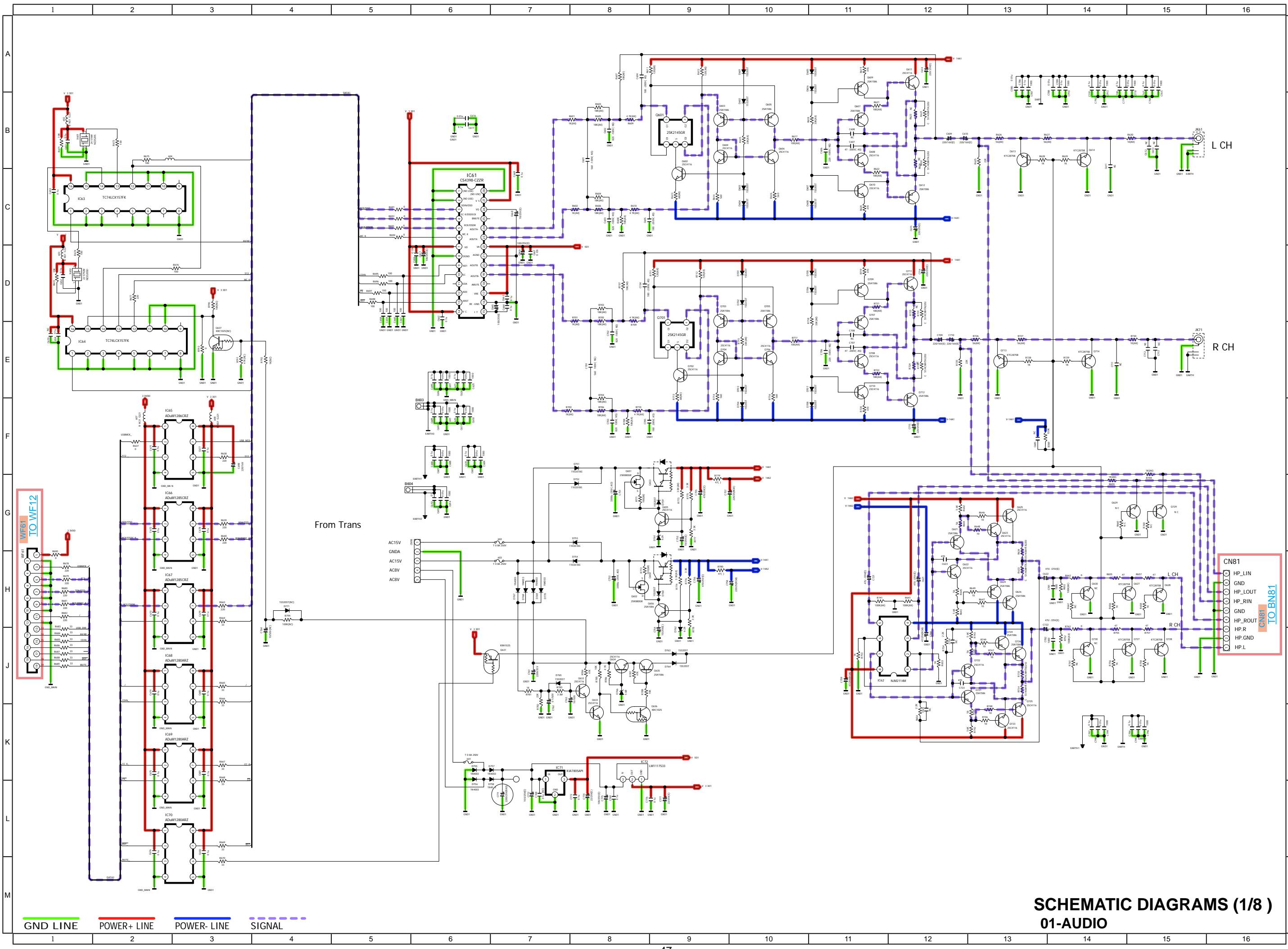
# PHONE (B SIDE)

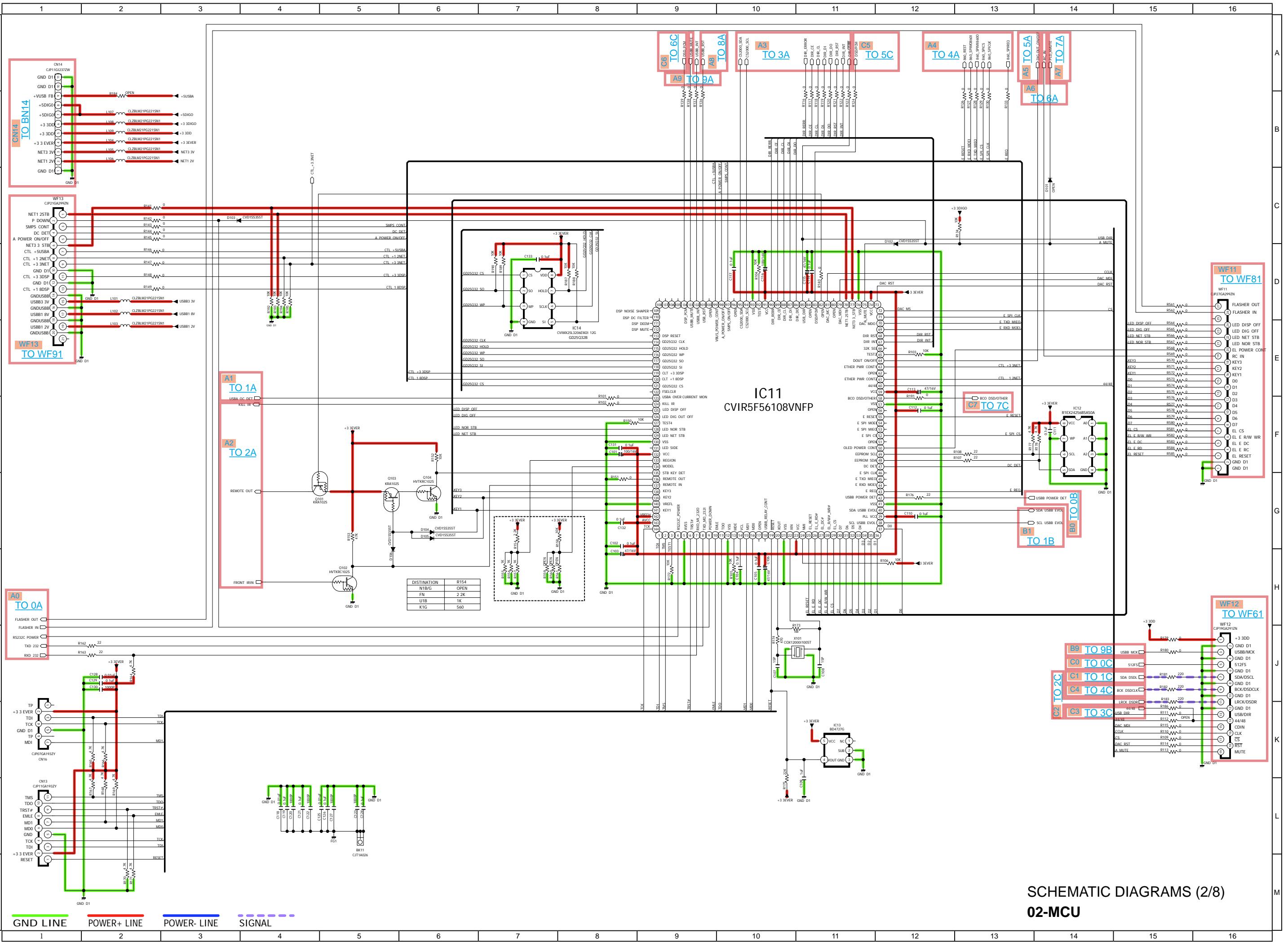


**FRONT**  
**(A SIDE)**

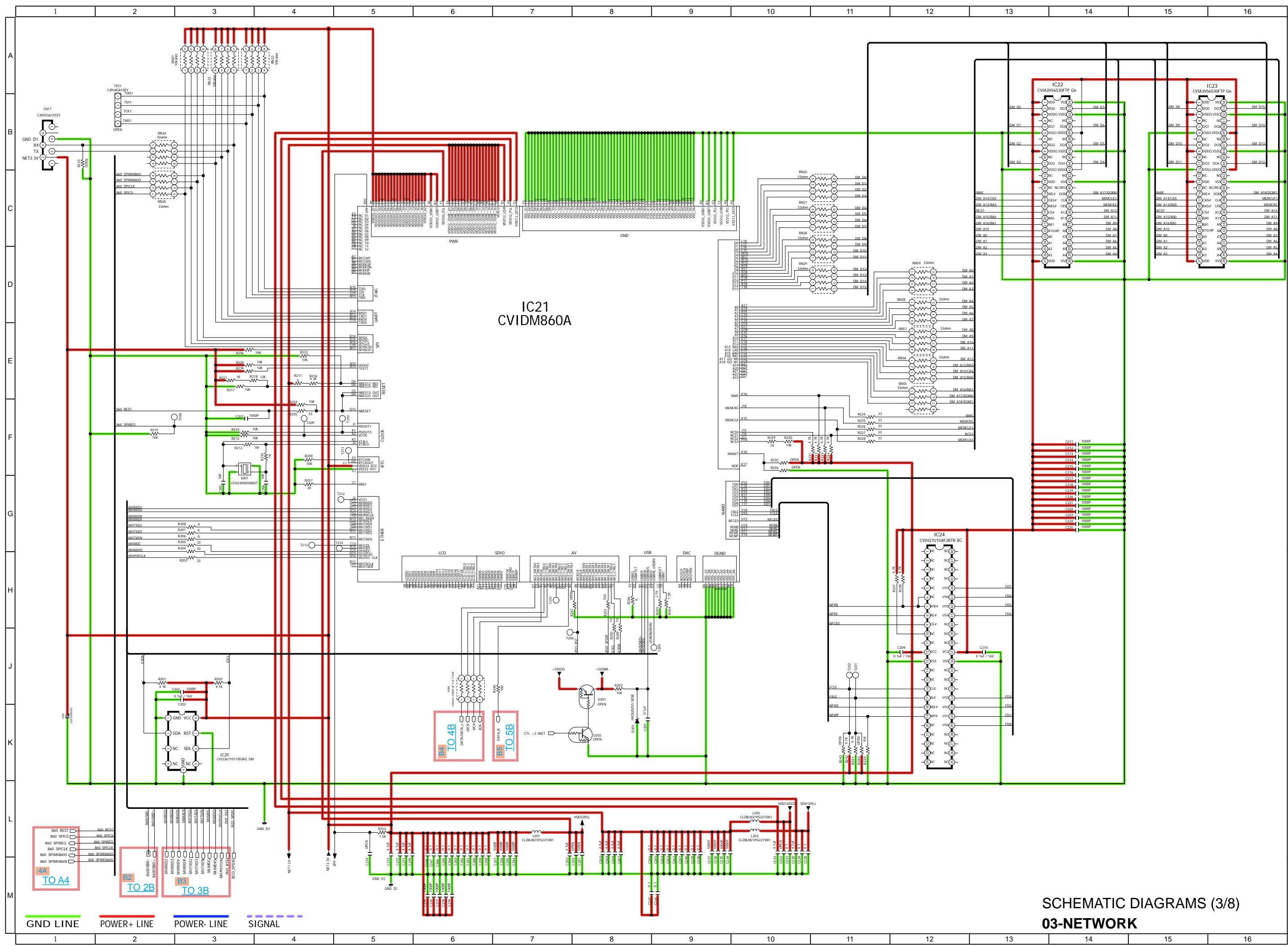


**FRONT**  
**(B SIDE)**

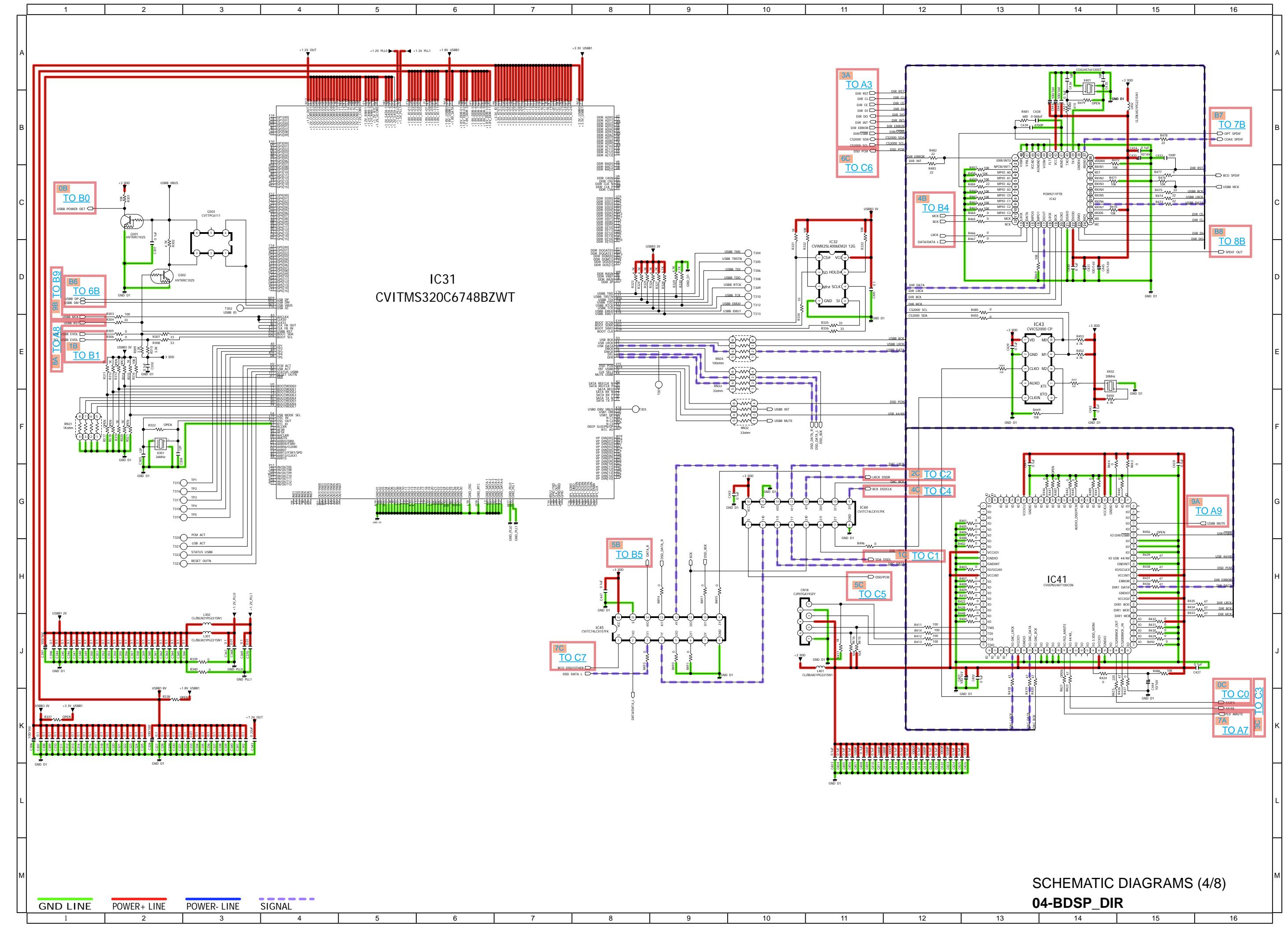


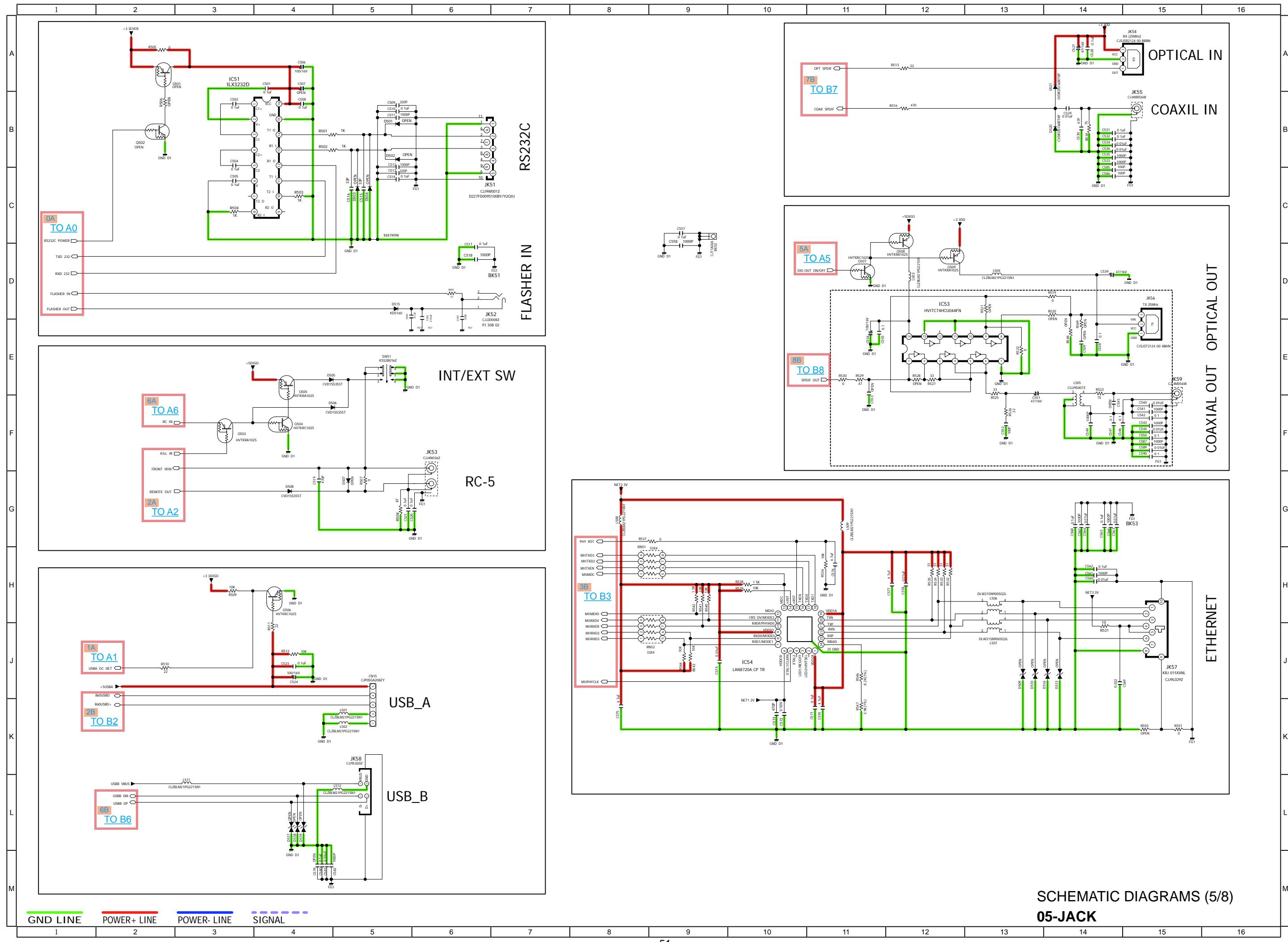


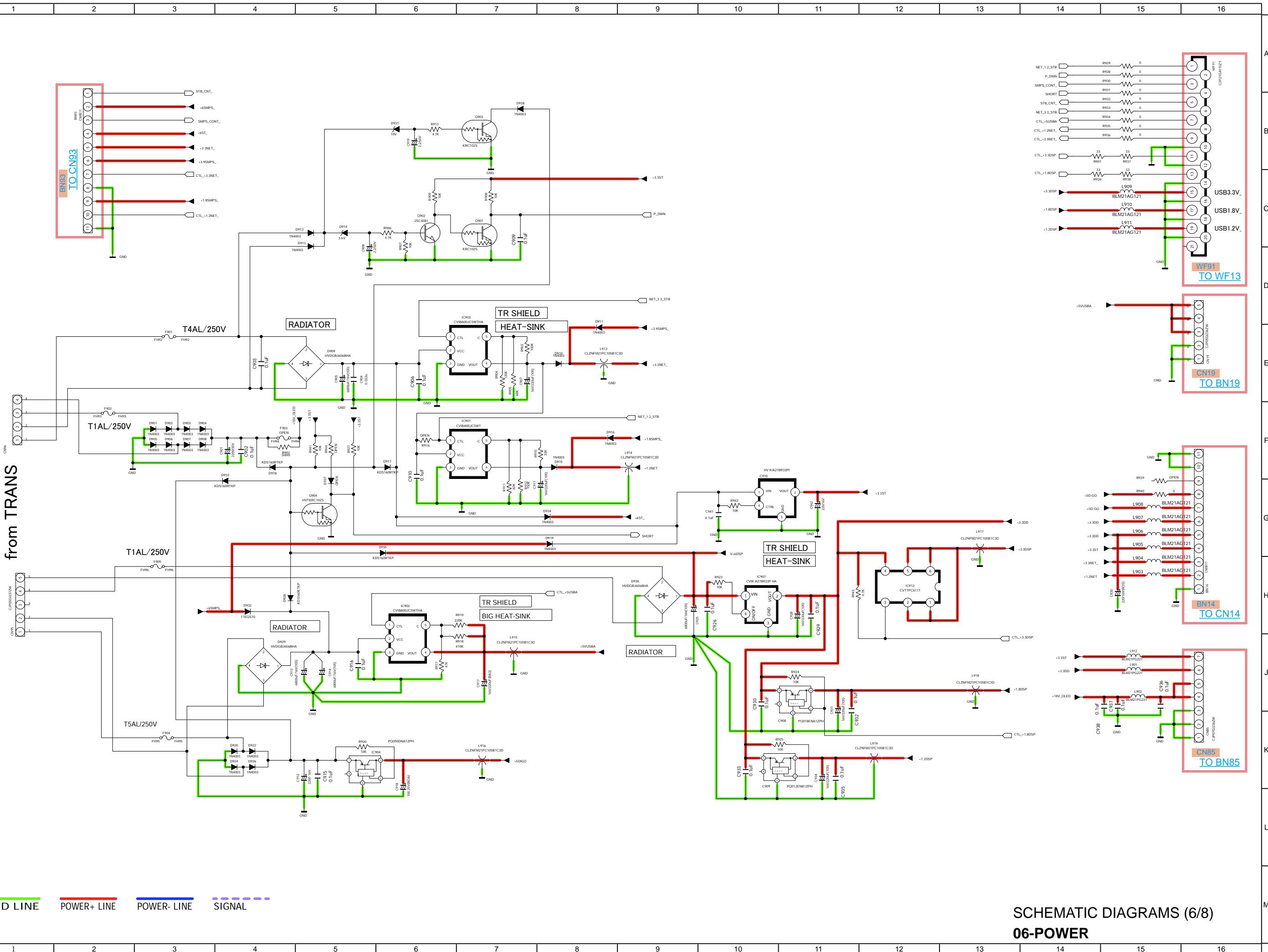
**IC21**  
**CVIDM860A**

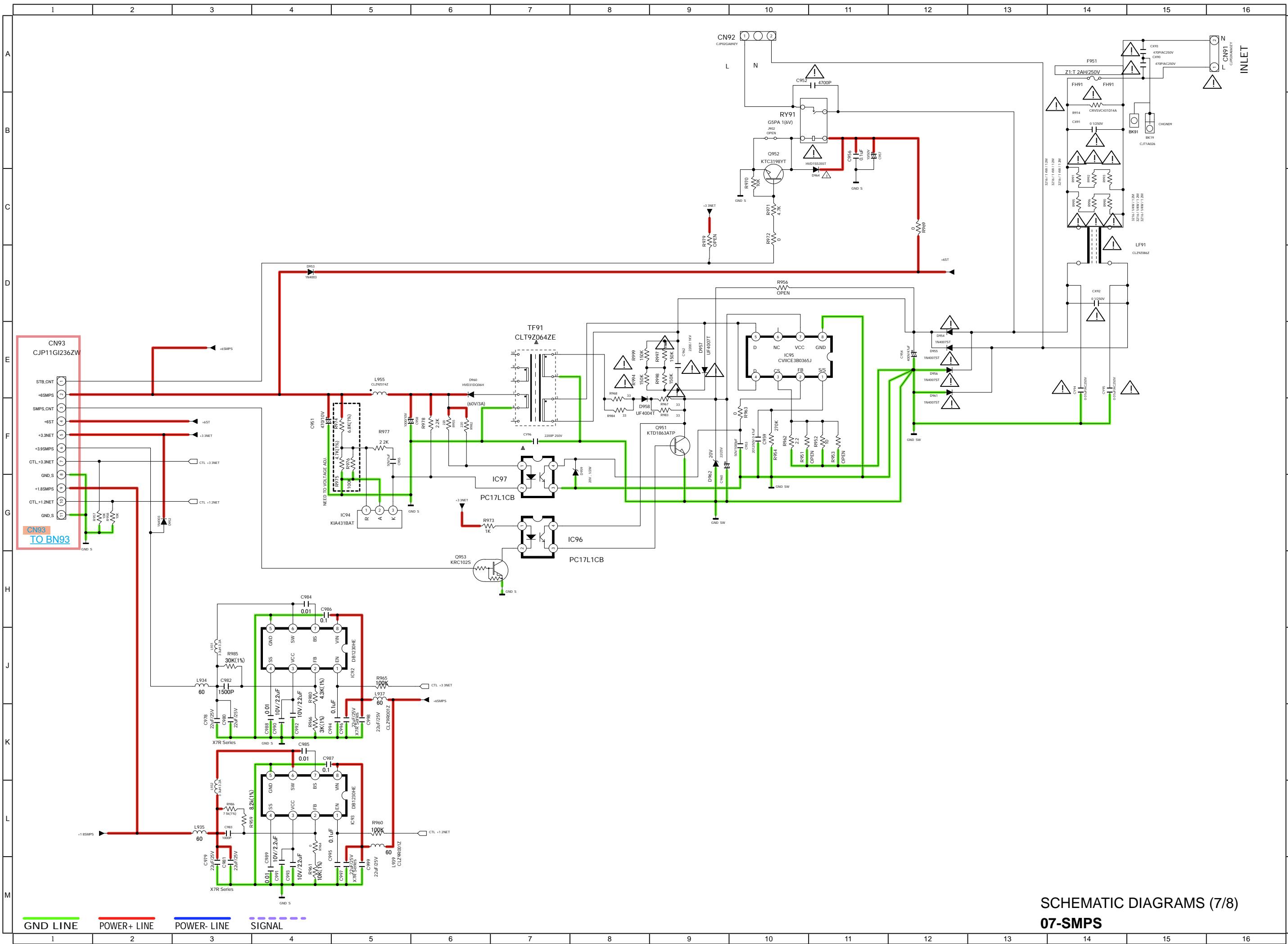


**SCHEMATIC DIAGRAMS (3/8)**  
**03-NETWORK**

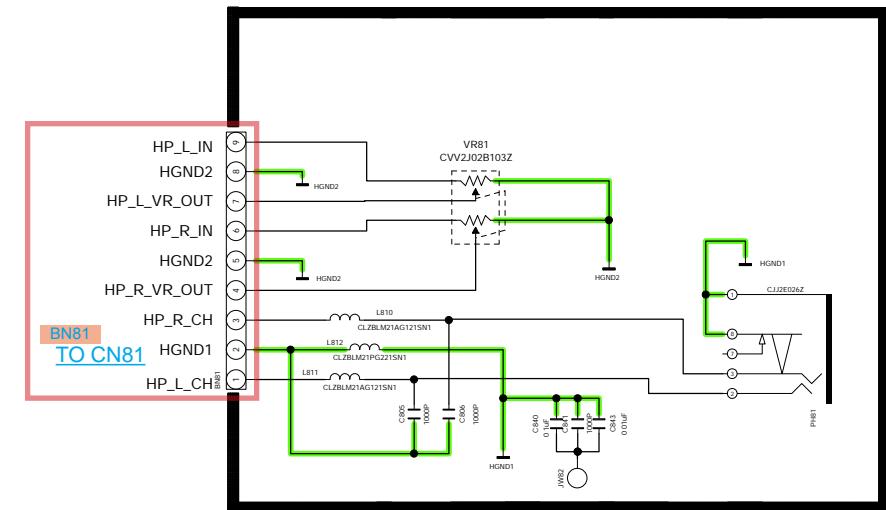
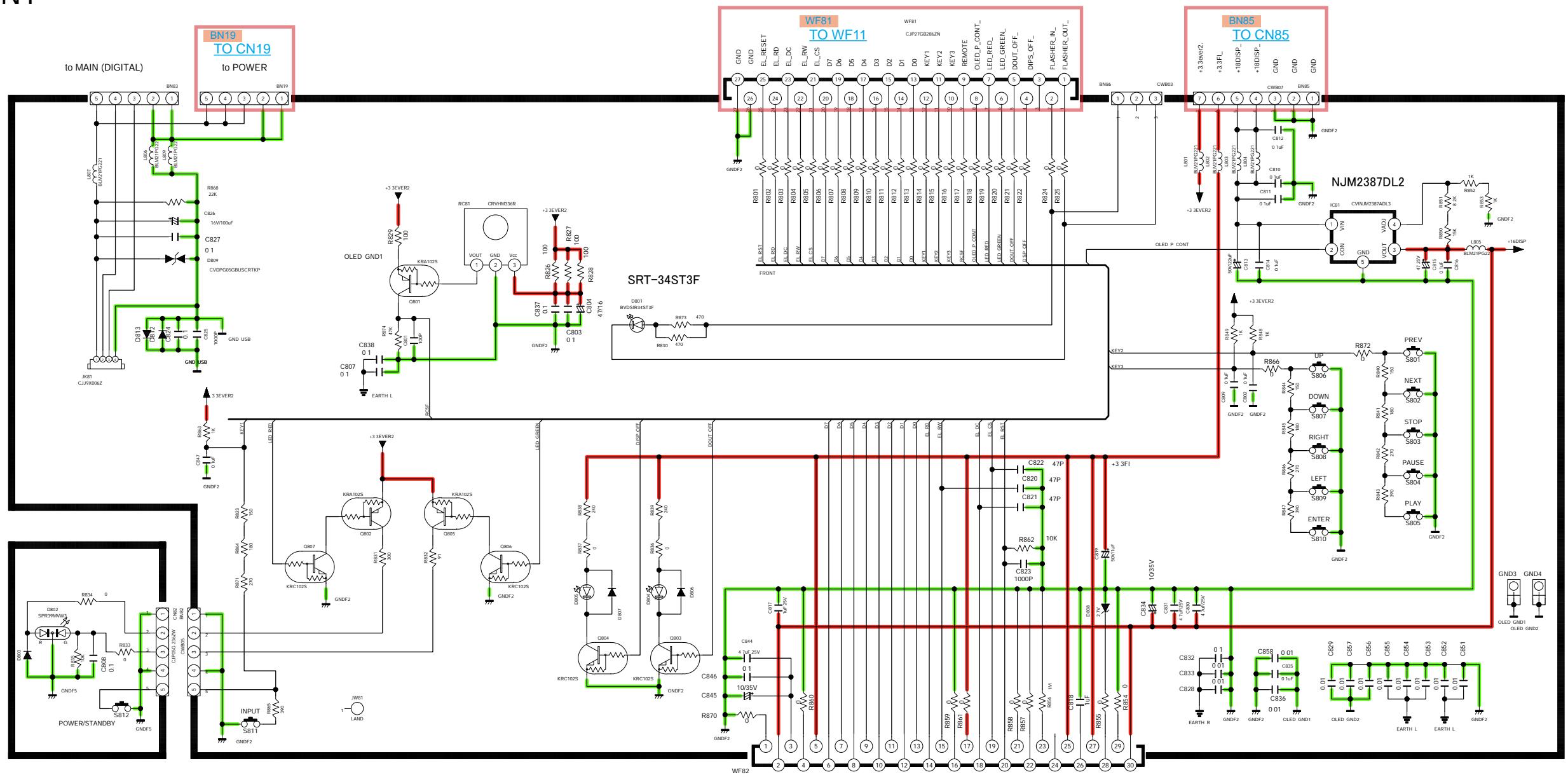








# NA8005 FRONT

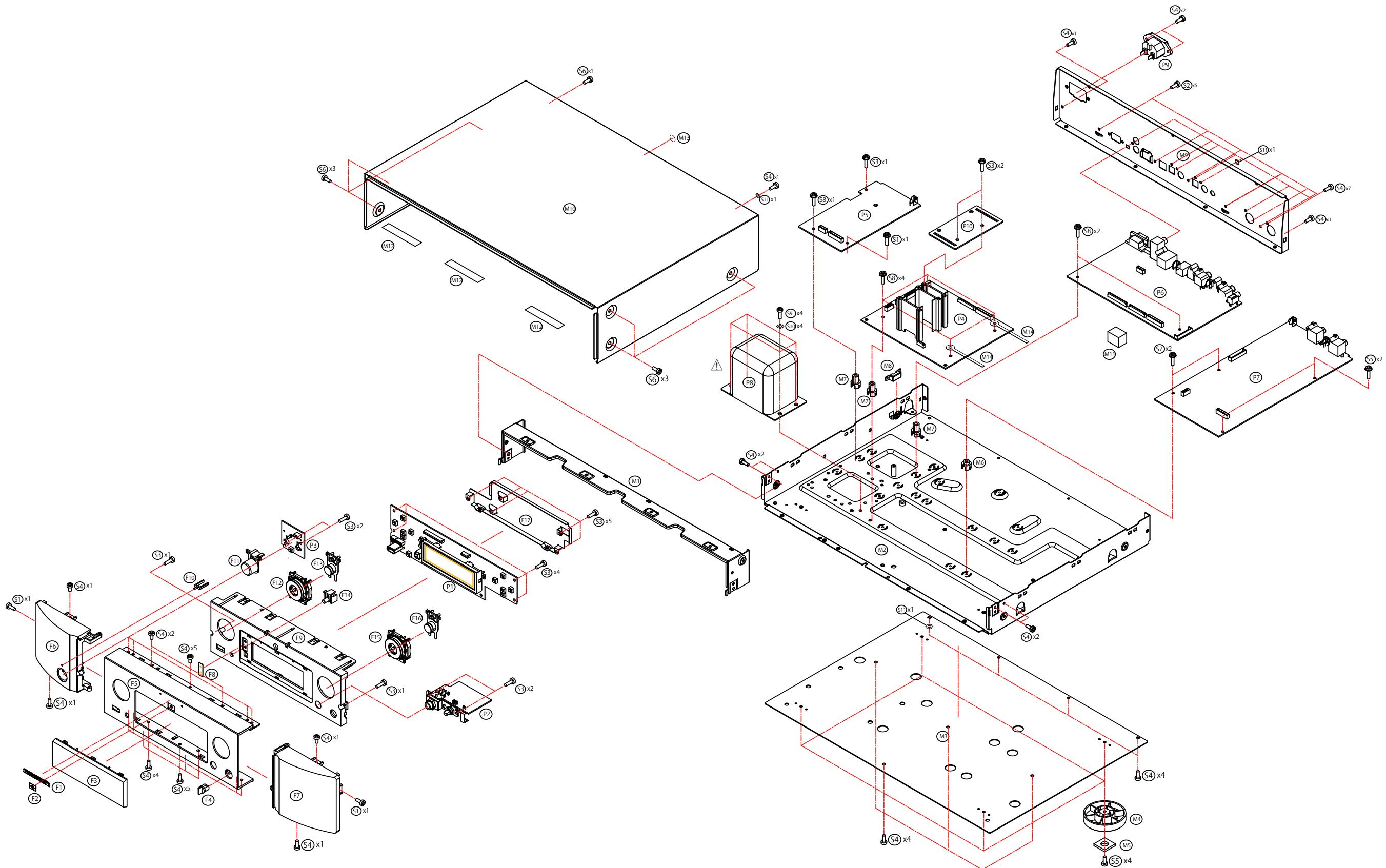


SCHEMATIC DIAGRAMS (8/8)  
08-FRONT

GND LINE POWER+ LINE POWER- LINE SIGNAL

## EXPLODED VIEW

Please see the last chapter for the part list.



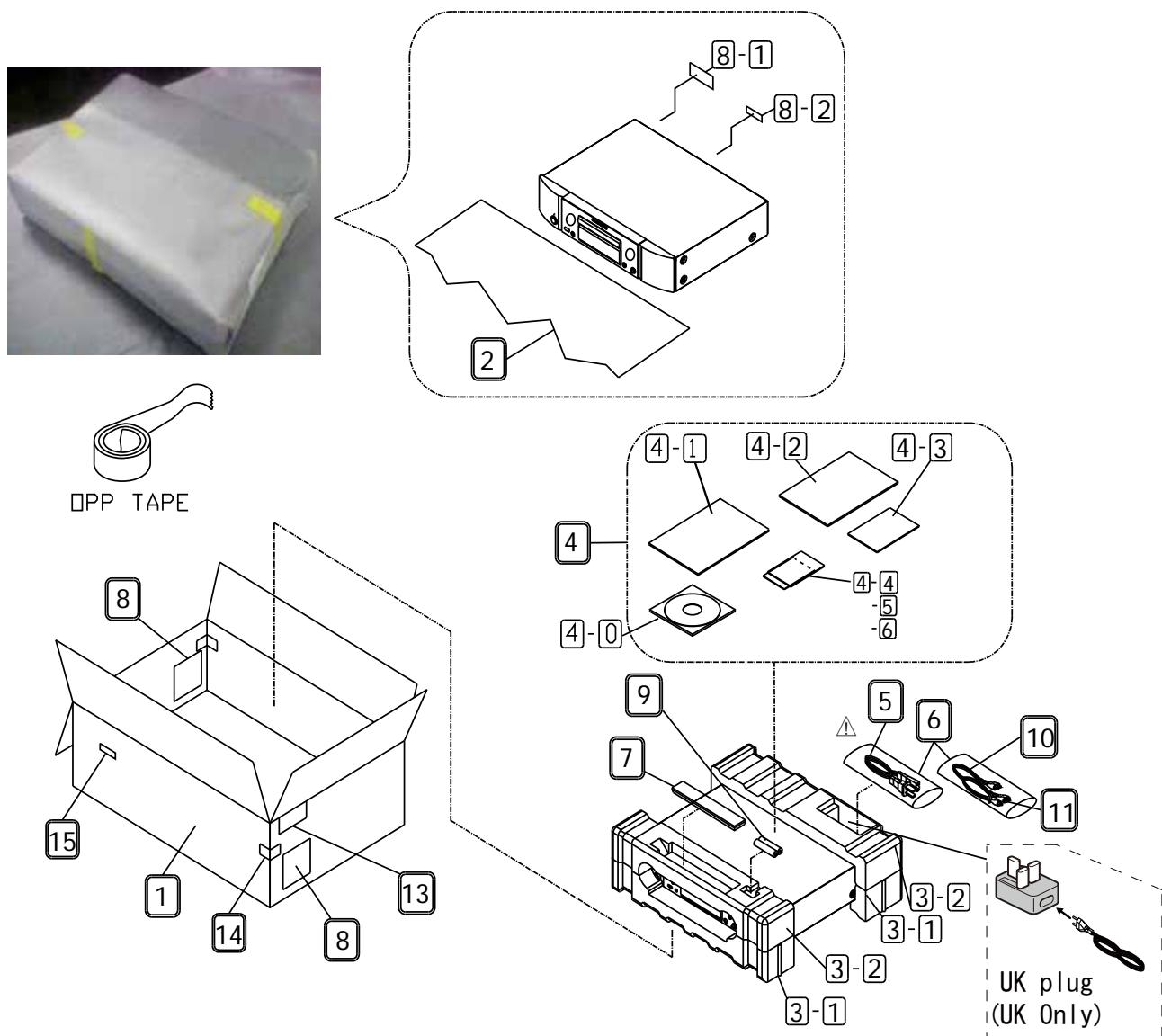
**WARNING:**  
Parts marked with this symbol have critical characteristics.  
Use ONLY replacement parts recommended by the manufacturer.

## **Personal notes:**

## **Personal notes:**

## PACKING VIEW

Please see the last chapter for the part list.



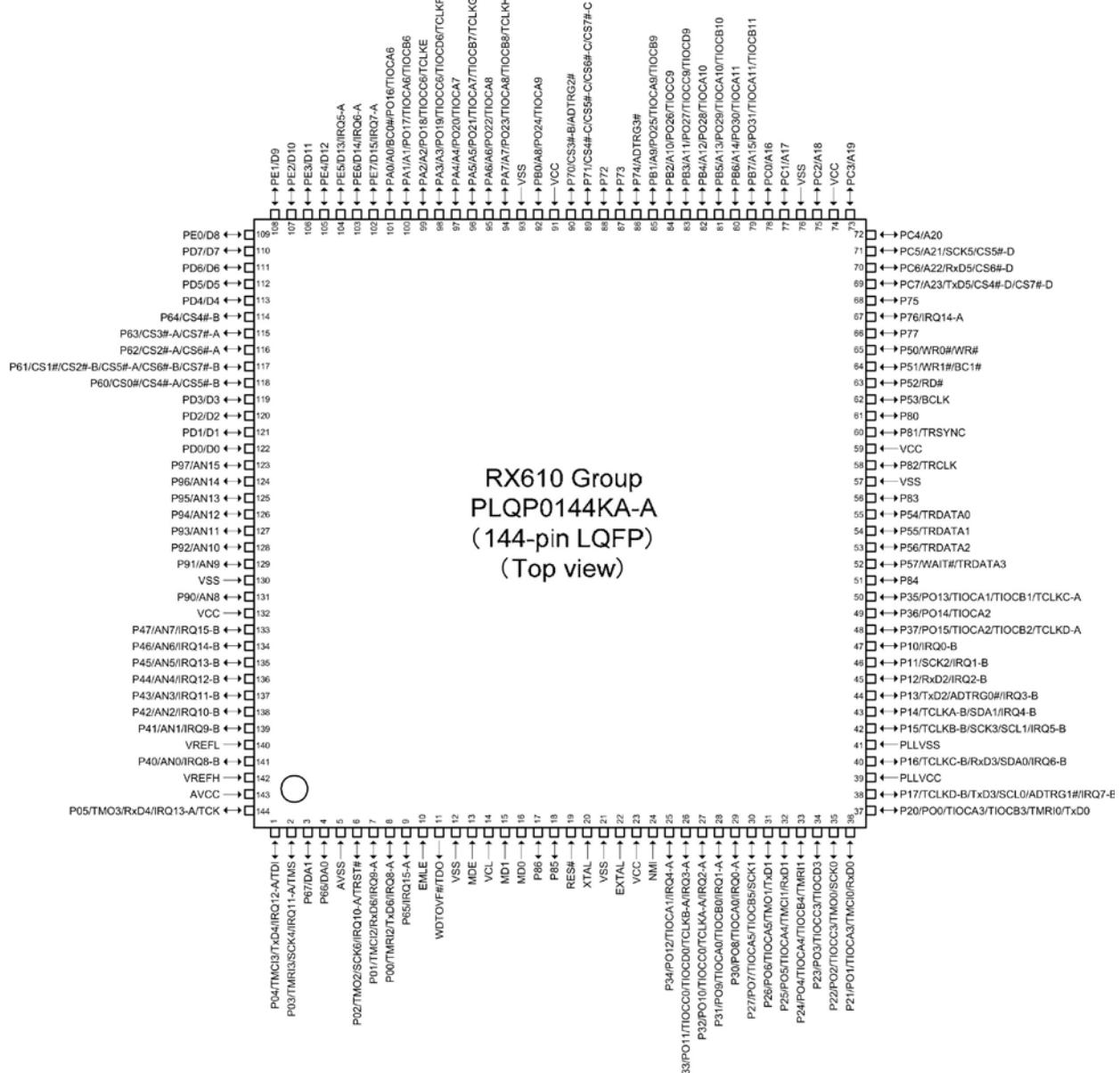
# SEMICONDUCTORS

Only major semiconductors are shown, general semiconductors etc. are omitted to list.

The semiconductor which described a detailed drawing in a schematic diagram are omitted to list.

## 1. IC's

R5F56108VNFP (MAIN : IC 11)



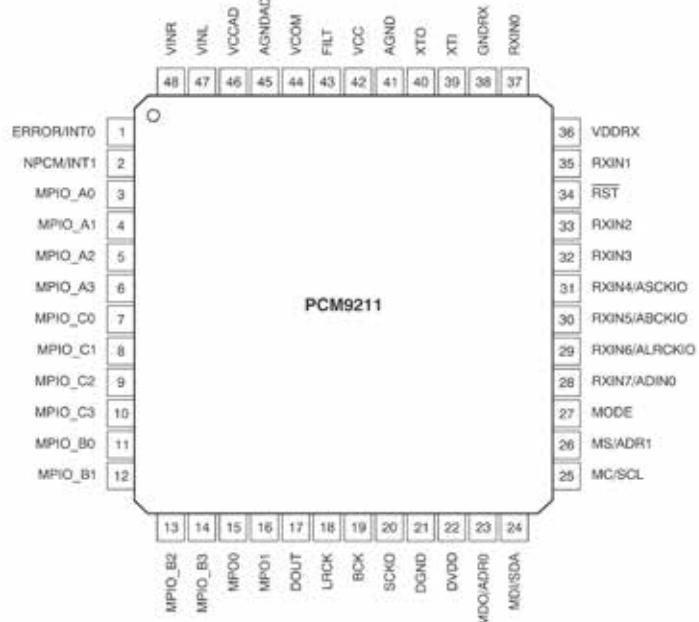
### R5F56108VNFP Terminal Functions

Pin	Port Name	I/O	Function	Network STBY	STBY MODE	Network STBY	PD/PUI
1	TDI	I	CONNECTION for EMULATOR	H	I	I	PU
2	TMS	I	CONNECTION for EMULATOR	H	I	I	PU
3	TEST1	I	Port for Setting up "PWB CHECK MODE"	L	I	I	PD
4	232C POWER	O	POWER Control for 232C	L	O/L	O/L	PD
5	AVSS	-	GND	L	-	-	-
6	TRST#	I	Connection port to EMULATOR	L	I	I	PD
7	RXD MI232O	I	UPDATE	H	I	I	PU
8	TXD MO232I	O	UPDATE	L	O/L	O/L	-
9	P.Down	I	P.Down Detection (INT)	P-0	I	I	PU
10	EMLE	I	Connection port to EMULATOR	L	I	I	PD
11	TDO	O	Connection port to EMULATOR	H	O/L	O/L	-
12	VSS	-	GND	L	-	-	-
13	MDE	I	Setting operation mode	L	I	I	PD
14	VCL	I	Connect to capacitor, 0.1μF		I	I	-
15	MD1	I	Connection port to EMULATOR	H	I	I	PU
16	MD0	I	Connection port to EMULATOR	H	I	I	PU
17	OPEN	O	OPEN	L	O/L	O/L	-
18	OPEN	O	OPEN	L	O/L	O/L	PD
19	RESET	I	RESET	H	I	I	PU
20	X-OUT	-	X OUT 12MHz	P-1	-	-	-
21	VSS	-	GND	L	-	-	-
22	X-IN	-	X IN 12MHz	P-2	-	-	-
23	VCC	-	+3.3V_CPU	H	-	-	-
24	NMI	I	Request Interruption PORT	H	I	I	PU
25	EL RESET	O	OLED RESET "L" = RESET	L	O/L	O/L	PD
26	EL E, RD	O	EL READ OUT	L	O/L	O/L	-
27	EL D/C	O	Switch Data/Commando "H"data, "L"command	L	O/L	O/L	-
28	EL R/W, WR	O	EL WRITE	L	O/L	O/L	-
29	EL CS	O	EL CS "L" = Communication enable with OLED	L	O/L	O/L	-
30	D7	O	Data Bus for OLED	L	O/L	O/L	-
31	D6	O	Data Bus for OLED	L	O/L	O/L	-
32	D5	O	Data Bus for OLED	L	O/L	O/L	-
33	D4	O	Data Bus for OLED	L	O/L	O/L	-
34	D3	O	Data Bus for OLED	L	O/L	O/L	-
35	D2	O	Data Bus for OLED	L	O/L	O/L	-
36	D1	O	Data Bus for OLED	L	O/L	O/L	-
37	D0	O	Data Bus for OLED	L	O/L	O/L	-
38	SCK_USBB	O	USBB/E.VOL(I2C) CLOCK OUTPUT	L	O/L	O/L	PU
39	PLLVCC	-	+3.3V_CPU	H	-	-	-
40	SDA_USBB	I/O	USBB/E.VOL(I2C) DATA IN/OUT	L	O/L	O/L	PU
41	PLLVSS	-	GND	L	-	-	-
42	USBB_POWER_DET	I	USBB_BUS Power Detection	L	I	I	PU
43	E_REQ	I	D M 870 / 860 Interrupt for Communicatio request	L	I	I	PD
44	E_RXDMOEI	SO	NETWORK serial DATA INPUT DM870/860 RXD)	H	O/L	O/L	PU(CX870)
45	E_TXDMIEO	SI	NETWORK serial DATA OUTPUT DM870/860 TXD)	L	O/L	I	PU(CX870)
46	E_SPICLK	O	ETHERNET Control Communication PORT (CLK)	H	O/L	O/L	PU(CX870)
47	DC_DET	I	DC POWER Abnormal Detection	L	I	I	PU
48	EEPROM SDA	I/O	EEPROM R1EX24256A Control Port	H(PU)	I	I	PU
49	EEPROM SCL	O	EEPROM R1EX24256A Control Port	H(PU)	I	I	PU
50	OLED Power Cont	O	OLED +18V power control	L	O/L	O/L	PD
51	OPEN	I	OPEN	L	I	I	-
52	/E_SPICS	O	SCI CS Signal OUTPUT to DM870/860	H	O/L	O/L	PU(CX870)
53	E_SPIMIEO	I	ETHERNETCommunication control Port	H	I	I	PU(CX870)
54	E_SPIMOEI	O	ETHERNETCommunication control Port	L	O/L	O/L	PU(CX870)

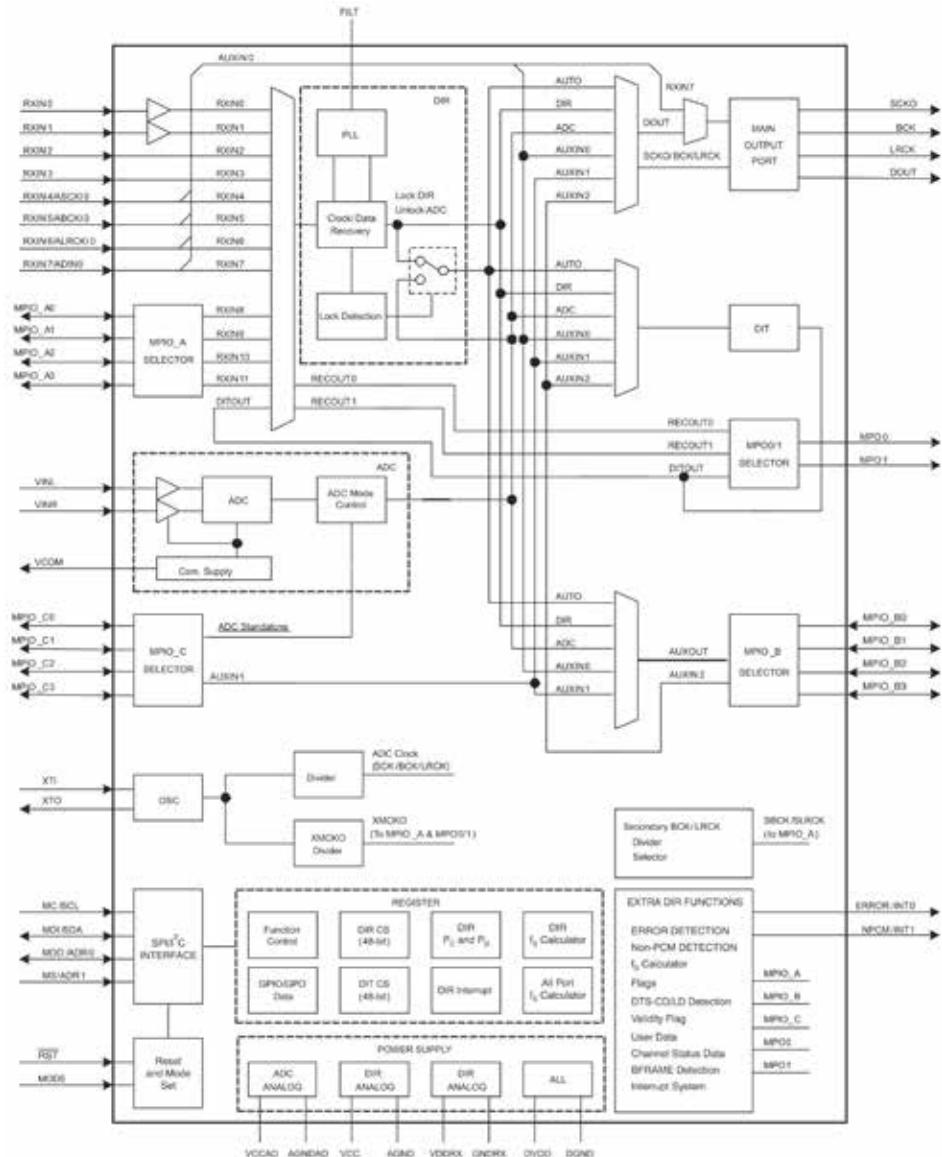
Pin	Port Name	I/O	Function	Network STBY	STBY MODE	Network STBY	PD/PU
55	E_RESET	O	Reset to DM870/860 (P.UP at DM870/860 side)	H	O/L	O/L	PU(CX870)
56	OPEN	I	OPEN	L	I	I	-
57	VSS	-	GND	L	-	-	-
58	OPEN	O	OPEN	L	O/L	O/L	-
59	VCC	-	+3.3V_CPU	H	-	-	-
60	44/48	O	Select DAC MCK	L	O/L	O/L	-
61	Ether_Power_Cont2	O	1.2V power Control for DM870	L	O/L	O/H	-
62	OPEN	O	OPEN	H	O/L	O/L	-
63	Ether Power Cont1	O	3.3V power Control for DM870	H	O/L	O/H	-
64	DOUT_ON_OFF	O	Power control for DIGITAL OUTPUT	L	O/L	O/L	PD
65	TEST2	I	Port for Setting up "PWB CHECK MODE"	L	I	I	PD
66	OPEN	O	OPEN	L	O/L	O/L	-
67	DIR_INT	I	DIR Control	L	I	I	PD (DIR_RERR) Common
68	DIR_RST	O	DIR Control	L	O/L	O/L	-
69		O	SPI Communication with CY920	L			
70		I	SPI Communication with CY920	L			
71		O	SPI Communication with CY920	L			
72	DAC_MS	O	DAC Control	L	O/L	O/L	-
73	DAC_RST	O	DAC Control	L	O/L	O/L	-
74	VCC	-	+3.3V_CPU	H	-	-	-
75	AMUTE	O	Mute Control	L	O/L	O/L	PU
76	VSS	-	GND	L	-	-	-
77	NET_3.3_STB	O	BCO+3.3V Power Control L at Network STBY/Normal STBY	L	O/L	O/L	-
78	NET_1.2_STB	O	BCO+1.2V Power control L at Network STBY/Normal STBY	L	O/L	O/L	-
79	DAC_MDI	O	DAC Control	L	O/L	O/L	-
80	DAC_MDO	O	OPEN	L	O/L	O/L	-
81	DAC MC	O	DAC Control	L	O/L	O/L	-
82	REMOTE OUT	O	Remote Output signal	L	O/H	O/H	PU
83	DSD/PCM_OUT	O	DSD or PCM Switching signal	L	O/L	O/L	-
84	OPEN	O	OPEN	L	O/L	O/L	-
85	DIR/USB	O	DIR/USBB Switching	L	O/L	O/L	-
86	DIR_DO	I	DIR Control	L	I	I	-
87	DIR DI	O	DIR Control	L	O/L	O/L	-
88	DIR_CL	O	DIR Control	L	O/L	O/L	-
89	DIR_CE	O	DIR Control	L	O/L	O/L	-
90	DIR_RERR	I	DIR Control	L	I	I	PD
91	VCC	-	+3.3V_CPU	H	-	-	-
92	TEST3	I	Port for Setting up "PWB CHECK MODE"	L	I	I	PD
93	VSS	-	GND	L	-	-	-
94	CS2000_SCL	O	CS2000(I2C)_Clock Output	L	O/L	O/L	PU
95	CS2000_SDA	I/O	CS2000(I2C)_DATA IN/OUT	L	O/L	O/L	PU
96	OPEN	O	OPEN	L	O/L	O/L	-
97	SMPS ON/OFF	O	ON/STANDBY Control L=Network/ Normal STBY, H=ON	L	O/H	O/H	PD
98	A.Power ON/OFF	O	ON/STANDBY Control L=Network/ Normal STBY, H=ON	L	O/L	O/L	PD
99	VBUS Power Cont	I/O	VBUS Power Control	L	O/L	O/L	PD
100	OPEN	O	OPEN	L	O/L	O/L	-
101	USBB_RST	O	RESET for USBB DSP ACTIVE:L	L	O/L	O/L	PD
102	USBB_INT	I	USBB Interruption ACTIVE:L	L	O/L	O/L	PU
103	USBB_MUTE	I	USBB MUTE	PU	O/L	O/L	PU
104	DSD/PCM	I	USBB DSD/PCM Detection	PU	O/L	O/L	PU
105	OPEN	O	OPEN	L	O/L	O/L	-
106	OPEN	O	OPEN	L	O/L	O/L	-
107	OPEN	O	OPEN	L	O/L	O/L	-
108	OPEN	O	OPEN	L	O/L	O/L	-
109	OPEN	O	OPEN	L	O/L	O/L	-
110	OPEN	O	OPEN	L	O/L	O/L	-
111	OPEN	O	OPEN	L	O/L	O/L	-

Pin	Port Name	I/O	Function	Network STBY	STBY MODE	Network STBY	PD/PU
112	OPEN	O	OPEN	L	O/L	O/L	-
113	OPEN	O	OPEN	L	O/L	O/L	-
114	GD25Q32_CLK	O	GD25Q32-6P, CLK	L	O/L	O/L	-
115	GD25Q32_HOLD	I/O	GD25Q32-7P,HOLD# ( IO3 )	L	I	I	PU
116	GD25Q32_WP	O	GD25Q32-3P,WP# ( IO2 )	L	I	I	PU
117	GD25Q32_SO	O	GD25Q32-2P,SO ( IO1 )	L	I	I	PU
118	GD25Q32_SI	O	GD25Q32-5P,SI ( IO0 )	L	I	I	PU
119	CLT_+3.3DSP	O	DSP +3.3V Power Control	L	O/L	O/L	-
120	CLT_+1.8DSP	O	DSP+1.2V Power Control	L	O/L	O/L	-
121	GD25Q32_CS	O	GD25Q32-1P, CS	H(PU)	O/L	O/L	-
122	OPEN	O	OPEN	H(PU)	O/L	O/L	-
123	USBA_OVER_CURRENT_MONI	I	USBA Over Current Detection	L	O/L	O/L	PD
124	KILL IR	O	Unable IR Sensor input	L	O/L	O/L	-
125	LED_DISP_OFF	O	LED Control, DISPLAY OFF	L	O/L	O/L	PD
126	LED_DIGOUT_OFF	O	LED Control, DIGITAL OUT OFF	L	O/L	O/L	PD
127	TEST4	I	Port for Setting up "PWB CHECK MODE"	L	I	I	PD
128	LED_NOR_STB	O	LED Control, NORMAL STANDBY	L	O/L	O/L	PD
129	LED_NET_LED	O	LED Control, NETWORK STANDBY	H	O/L	O/H	PD
130	VSS	-	GND	L	-	-	-
131	OPEN	O	OPEN	L	O/L	O/L	PD
132	VCC	-	+3.3V CPU	H	-	-	-
133	REGION	I	Region ID PORT 0V:E2 0.43V:JP 0.82V:E3 1.24V:E1C	0.4V	I	I	-
134	MODEL	I	Model ID PORT 0V:NA8005 3.3V:Other	0V	I	I	-
135	STB_KEY_DET	I	Key detection for POWER ON	H(PU)	I	I	PU
136	OPEN	I	OPEN	0.755V	I	I	PD
137	REMOTE IN	I	REMOTE IN	P-23	I	I	PU
138	KEY3	I	Key Input 3(A/D port)	P-24	I	I	PU
139	KEY2	I	Key Input 2(A/D port)	P-24	I	I	PU
140	VREFL	-	GND	L	-	-	-
141	KEY1	I	Key Input 1(A/D port)	P-24	I	I	-
142	VREFH	-	+3.3V_CPU	H	-	-	-
143	AVCC	-	+3.3V_CPU	H	-	-	-
144	TCK	I	Connection port to EMULATOR	H	I	I	PU

## PCM9211 (DIGITAL : IC42)



## PCM9211 Block Diagram



## PCM9211 Pin Descriptions

NO.	NAME	PIN		DESCRIPTION
		I/O	5-V TOLERANT	
1	ERROR/INT0	O	No	DIR Error detection output / Interrupt0 output
2	NPCM/INT1	O	No	DIR Non-PCM detection output / Interrupt1 output
3	MPIO_A0	I/O	Yes	Multipurpose I/O, Group A <sup>(1)</sup>
4	MPIO_A1	I/O	Yes	Multipurpose I/O, Group A <sup>(1)</sup>
5	MPIO_A2	I/O	Yes	Multipurpose I/O, Group A <sup>(1)</sup>
6	MPIO_A3	I/O	Yes	Multipurpose I/O, Group A <sup>(1)</sup>
7	MPIO_C0	I/O	Yes	Multipurpose I/O, Group C <sup>(1)</sup>
8	MPIO_C1	I/O	Yes	Multipurpose I/O, Group C <sup>(1)</sup>
9	MPIO_C2	I/O	Yes	Multipurpose I/O, Group C <sup>(1)</sup>
10	MPIO_C3	I/O	Yes	Multipurpose I/O, Group C <sup>(1)</sup>
11	MPIO_B0	I/O	Yes	Multipurpose I/O, Group B <sup>(1)</sup>
12	MPIO_B1	I/O	Yes	Multipurpose I/O, Group B <sup>(1)</sup>
13	MPIO_B2	I/O	Yes	Multipurpose I/O, Group B <sup>(1)</sup>
14	MPIO_B3	I/O	Yes	Multipurpose I/O, Group B <sup>(1)</sup>
15	MPO0	O	No	Multipurpose output 0
16	MPO1	O	No	Multipurpose output 1
17	DOUT	O	No	Main output port, serial digital audio data output
18	LRCK	O	No	Main output port, LR clock output
19	BCK	O	No	Main output port, Bit clock output
20	SCKO	O	No	Main output port, System clock output
21	DGND	-	-	Ground, for digital
22	DVDD	-	-	Power supply, 3.3 V (typ.), for digital
23	MDO/ADR0	I/O	Yes	Software control I/F, SPI data output / I <sup>2</sup> C slave address setting0 <sup>(2)</sup>
24	MDI/SDA	I/O	Yes	Software control I/F, SPI data input / I <sup>2</sup> C data input/output <sup>(2)(3)</sup>
25	MC/SCL	I	Yes	Software control I/F, SPI clock input / I <sup>2</sup> C clock input <sup>(2)</sup>
26	MS/ADR1	I	Yes	Software control I/F, SPI chip select / I <sup>2</sup> C slave address setting1 <sup>(2)</sup>
27	MODE	I	No	Control mode setting, (see the Serial Control Mode section, Control Mode Pin Setting)
28	RXIN7/ADIN0	I	Yes	Biphase signal, input 7 / AUXIN0, serial audio data input <sup>(2)</sup>
29	RXIN6/ALRCKI0	I	Yes	Biphase signal, input 6 / AUXIN0, LR clock input <sup>(2)</sup>
30	RXIN5/ABCKI0	I	Yes	Biphase signal, input 5 / AUXIN0, bit clock input <sup>(2)</sup>
31	RXIN4/ASCKI0	I	Yes	Biphase signal, input 4 / AUXIN0, system clock input <sup>(2)</sup>
32	RXIN3	I	Yes	Biphase signal, input 3 <sup>(2)</sup>
33	RXIN2	I	Yes	Biphase signal, input 2 <sup>(2)</sup>
34	<u>RST</u>	I	Yes	Reset Input, active low <sup>(2)(4)</sup>
35	RXIN1	I	Yes	Biphase signal, input 1, built-in coaxial amplifier
36	VDDRX	-	-	Power supply, 3.3 V (typ.), for RXIN0 and RXIN1.
37	RXIN0	I	Yes	Biphase signal, input 0, built-in coaxial amplifier
38	GNDRX	-	-	Ground, for RXIN
39	XTI	I	No	Oscillation circuit input for crystal resonator or external XTI clock source input <sup>(5)</sup>
40	XTO	O	No	Oscillation circuit output for crystal resonator
41	AGND	-	-	Ground, for PLL analog
42	VCC	-	-	Power supply, 3.3 V (typ.), for PLL analog
43	FILT	O	No	External PLL loop filter connection terminal; must connect recommended filter
44	VCOM	O	No	ADC common voltage output; must connect external decoupling capacitor
45	AGNDAD	-	-	Ground, for ADC analog
46	VCCAD	-	-	Power supply, 5.0 V (typ.), for ADC analog
47	VINL	I	No	ADC analog voltage input, left channel
48	VINR	I	No	ADC analog voltage input, right channel

(1) Schmitt trigger input

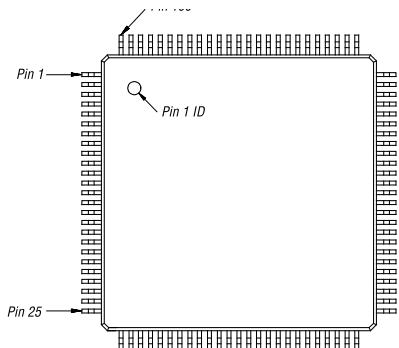
(2) Schmitt trigger input

(3) Open-drain configuration in I<sup>2</sup>C mode

(4) Onboard pull-down resistor (50 kΩ, typical)

(5) CMOS Schmitt trigger input

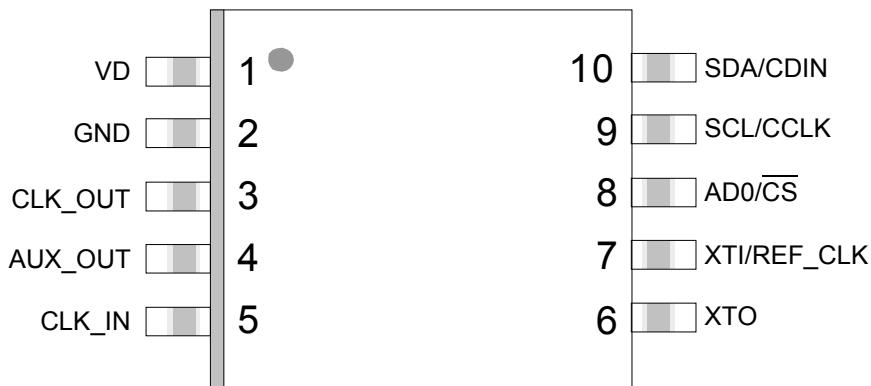
## EPM240T100C5N (DIGITAL : IC41)



### EPM240T100C5N Terminal Function

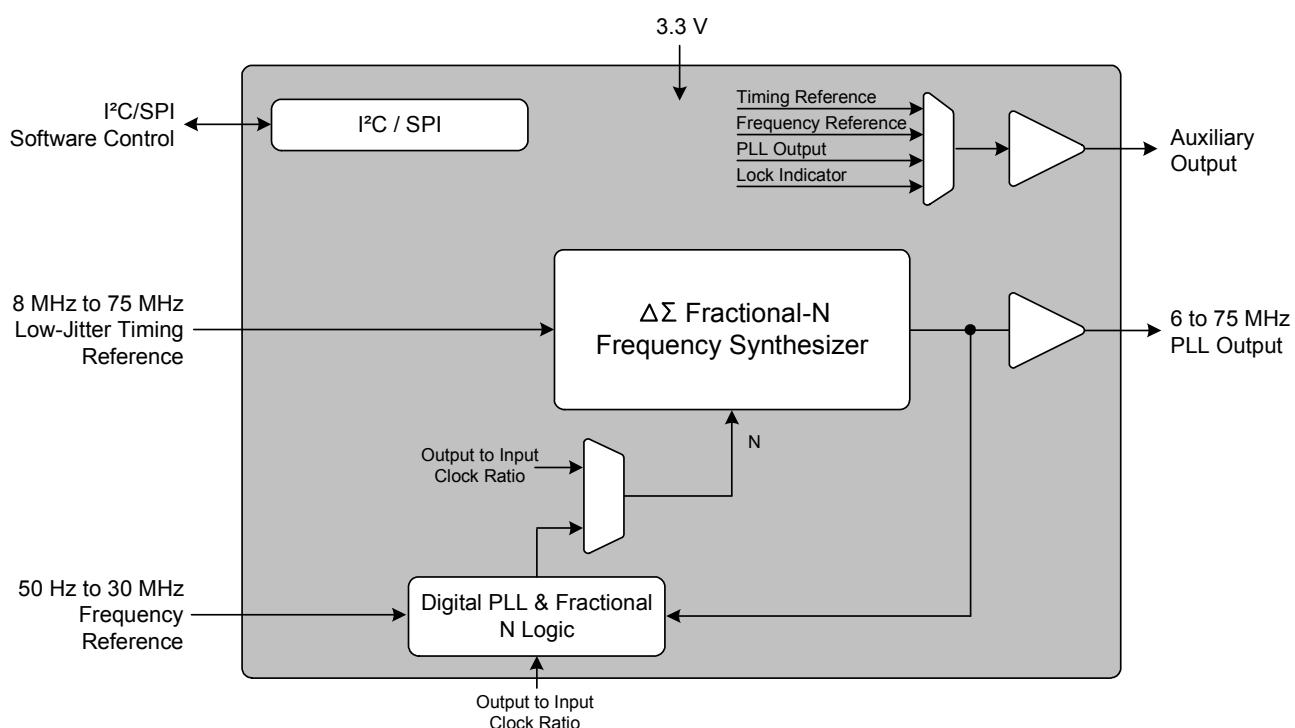
Pin No.	New	Pin/Pad Name	Connect Device	Direction	Detail
1	IO	NC	-	-	NC
2	IO	NC	-	-	NC
3	IO	NC	-	-	NC
4	IO	.MODE3.	CD MECHA	IN	PLD_RST(MODE3.)
5	IO	.SRDATAO	CD MECHA	IN	SRDATA BE→PLD Control
6	IO	.SRCLK	CD MECHA	IN	SRCLK BE→PLD Control
7	IO	.DFRST_IN	CD MECHA	IN	DFRST_IN BE→PLD Control
8	IO	.MODE2.	CD MECHA	IN	MP3 fs (44.1k,48k:L / 32k:H) Detect
9	VCCIO1	33_VCCIO	DA3.3V	-	Power Supply
10	GNDIO	GND	DGND	-	Ground
11	GNDINT	GND	DGND	-	Ground
12	IO/GCLK0	.FE_DSD/PCM	CD MECHA	IN	MODE(FE_DSD/PCM)
13	VCCINT	33_VCCIO	DA3.3V	-	Power Supply
14	IO/GCLK1	.BE_DAC_CS	CD MECHA	IN	BE_DAC_CS
15	IO	.PDATA0	CD MECHA	IN	PDATA0
16	IO	.PLRCK	CD MECHA	IN	PLRCK
17	IO	.PBCK.	CD MECHA	IN	PBCK.
18	IO	.DMIX_L	CD MECHA	IN	DMIX_L
19	IO	.DMIX_R	CD MECHA	IN	DMIX_R
20	IO	.DBCK	CD MECHA	IN	DBCK
21	IO	.PMCK	CD MECHA	IN	PMCK
22	TMS	.TMS	PLD UPDATE	IN	Test Mode State for JTAG
23	TDI	.TDI	PLD UPDATE	IN	Test Data Input for JTAG
24	TCK	.TCK	PLD UPDATE	IN	Test Clock for JTAG
25	TDO	.TDO	PLD UPDATE	OUT	Test Data Out for JTAG
26	IO	.SLAVE_MCK	CD MECHA	OUT	CD MECHA MCK(33.8688MHz)
27	IO	.DAC_DSD_DATAL	DSD1792A	OUT	DAC_DSD_DATAL
28	IO	.DAC_DSD_DATAR	DSD1792A	OUT	DAC_DSD_DATAR
29	IO	.DAC_DSD_BCK	DSD1792A	OUT	DAC_DSD_BCK
30	IO	.DAC_LRCK	DSD1792A	OUT	DAC_LRCK
31	VCCIO1	33_VCCIO	DA3.3V	-	Power Supply
32	GNDIO	GND	DGND	-	Ground
33	IO	.DAC_DATA	DSD1792A	OUT	DAC_DATA
34	IO	.DAC_BCK	DSD1792A	OUT	DAC_BCK
35	IO	.DAC_MCK_22.5792M	DAC_MCK 22M	IN	DAC_MCK 22.5792MHz
36	IO	.22.5792M_DIR	DIR_MCK 22M	IN	DIR_MCK 22.5792MHz
37	IO	.22.5792M_USBA	USBA_MCK 22M	IN	USBA_MCK 22.5792MHz
38	IO	.DAC_MCK_24.576M	DAC_MCK 24M	IN	DAC_MCK 24.576MHz
39	IO	PLD_AMUTE	AUDIO MUTE	OUT	ANALOG Mute
40	IO	.44/48	AUDIO UNIT SELECTOR	OUT	Switch MCK (22M/24M)
41	IO	.DAC_CS_OUT	DSD1792A	OUT	DAC_CS_OUT
42	IO	.DAC_DATA_OUT	DSD1792A	OUT	DAC_DATA_OUT
43	IO/DEV_OE	.DAC_CK_OUT	DSD1792A	OUT	DAC_CK_OUT
44	IO/DEV_CLRn	3.3DD_MONI	+3.3DD	IN	+3.3DD Moni
45	VCCIO1	33_VCCIO	DA3.3V	-	Power Supply
46	GNDIO	GND	DGND	-	Ground

Pin No.	New	Pin/Pad Name	Connect Device	Direction	Detail
47	IO	.DAC_RST_OUT	DSD1792A	OUT	DAC RST
48	IO	.CS2000MCK_OUT	CS2000	OUT	CS2000MCK_OUT
49	IO	.CS2000MCK_IN	CS2000	IN	CS2000MCK_IN
50	IO	.PLD_RSV3	M3062LFGPGP	OUT	PLD_RSV3
51	IO	.PLD_DAC_SEL	AUDIO UNIT SELECTOR	OUT	PLD_DAC_SEL
52	IO	.DAC_CK_IN	M3062LFGPGP	IN	DAC_CK_IN : for L/Rch DAC :to DSD1792A MC
53	IO	.DAC_CS_IN	M3062LFGPGP	IN	DAC_CS_IN : for L/Rch DAC:to DSD1792A MS
54	IO	.DAC_DATA_IN	M3062LFGPGP	IN	DAC_DATA_IN : for L/Rch DAC:to DSD1792A MDI
55	IO	.USBB_MODE	M3062LFGPGP	IN	USB_B_MODE
56	IO	.DIR1_MCK	PCM9211	IN	DIR_MCK
57	IO	.DIR1_BCK	PCM9211	IN	DIR_BCK
58	IO	.DIR1_LRCK	PCM9211	IN	DIR_LRCK
59	VCCIO2	33_VCCIO	DA3.3V	-	Power Supply
60	GNDIO	GND	DGND	-	Ground
61	IO	.DIR1_DATA	PCM9211	IN	DIR_DATA
62	IO/GCLK2	.ERROR	PCM9211	IN	DIR_ERROR
63	VCCINT	33_VCCIO	DA3.3V	-	Power Supply
64	IO/GCLK3	.USBB_DSD_PCM	"TMS320C6748BZWT3 /M3062LFGPGP"	IN	USBB_DSD_PCM
65	GNDINT	GND	DGND	-	Ground
66	IO	.USBB_44/48	"TMS320C6748BZWT3 /(M3062LFGPGP)"	IN	USBB Moni (44.1kHz:L/48kHz:H)
67	IO	.USBB_DSD_BCK	TMS320C6748BZWT3	IN	USBB_DSD_BCK
68	IO	.USBB_DSD_DATA_L	TMS320C6748BZWT3	IN	USBB_DSD_DATA_L
69	IO	.USBB_DSD_DATA_R	TMS320C6748BZWT3	IN	USBB_DSD_DATA_R
70	IO	.CD_USBB_DIR	M3062LFGPGP	IN	MODE (CD,USBB/DIR) Detect
71	IO	NC	CS2000	-	NC
72	IO	USBB_MUTE	TMS320C6748BZWT3	IN	USBB Mute Control
73	IO	NC	-	-	NC
74	IO	NC	-	-	NC
75	IO	NC	-	-	NC
76	IO	./INT_EXT	M3062LFGPGP	IN	INT/EXT selection port
77	IO	.D_MUTE	M3062LFGPGP	IN	Moni USBB_PCM→DSD MUTE timing
78	IO	.DAC_CONT_SEL	M3062LFGPGP	IN	DAC_CONT_SEL (SYSCON/DV3.2)
79	GNDIO	GND	DGND	-	Ground
80	VCCIO2	33_VCCIO	DA3.3V	-	Power Supply
81	IO	.DAC_RST_IN	M3062LFGPGP	IN	DAC_RST_IN : for L/Rch DAC:to DSD1792A RST
82	IO	.DSD_MUTE_F	M3062LFGPGP	IN	EN_VER(DSD_MUTE_F) : USBB_DSD→PCM MUTE timing
83	IO	.USBB_MCK_OUT	"TMS320C6748BZWT3 /PCM9211"	OUT	MCK for USB-DAC,DIR
84	IO	.M/SEL	N3305 7P FFC Connector	OUT	M/SEL for SYSCON Update
85	IO	.AUDIO_DSD/PCM	M3062LFGPGP	IN	Audio Gain (SACD : Hi/Others : Lo) Detect
86	IO	.CONT5.	M3062LFGPGP	IN	MCK (USB-A/Others) Detect
87	IO	CLK22.5792Mhz	AK8142	OUT	AK8142 22.5792MHz
88	IO	.SLAVE_MCK_33.8688M	AK8142	IN	AK8142 MCK 33.8688MHz
89	IO	.OCXO_IN_22.5792M	AK8142	OUT	AK8142 22.5792MHz
90	IO	AMUTE	AUDIO UNIT	IN	DV3.2 AMUTE (L : MUTE/H : MUTE Off)
91	IO	NC	-	-	NC
92	IO	NC	-	-	NC
93	GNDIO	GND	DGND	-	Ground
94	VCCIO2	33_VCCIO	DA3.3V	-	Power Supply
95	IO	NC	-	-	NC
96	IO	NC	-	-	NC
97	IO	NC	-	-	NC
98	IO	NC	-	-	NC
99	IO	NC	-	-	NC
100	IO	NC	-	-	NC



Pin Name	#	Pin Description
VD	1	Digital Power ( <i>Input</i> ) - Positive power supply for the digital and analog sections.
GND	2	Ground ( <i>Input</i> ) - Ground reference.
CLK_OUT	3	PLL Clock Output ( <i>Output</i> ) - PLL clock output
AUX_OUT	4	Auxiliary Output ( <i>Output</i> ) - This pin outputs a buffered version of one of the input or output clocks, or a status signal, depending on register configuration
CLK_IN	5	Frequency Reference Clock Input ( <i>Input</i> ) - Clock input for the Digital PLL frequency reference
XTO	6	Crystal Connections (XTI/XTO) / Timing Reference Clock Input (REF_CLK) ( <i>Input/Output</i> ) -
XTI/REF_CLK	7	XTI/XTO are I/O pins for an external crystal which may be used to generate the low-jitter PLL input clock. REF_CLK is an input for an externally generated low-jitter reference clock.
AD0/CS	8	Address Bit 0 ( $\text{I}^2\text{C}$ ) / Control Port Chip Select (SPI) ( <i>Input</i> ) - AD0 is a chip address pin in $\text{I}^2\text{C}$ Mode. CS is the chip select signal in SPI Mode.
SCL/CCLK	9	Control Port Clock ( <i>Input</i> ) - SCL/CCLK is the serial clock for the serial control port in $\text{I}^2\text{C}$ and SPI mode.
SDA/CDIN	10	Serial Control Data ( <i>Input/Output</i> ) - SDA is the data I/O line in $\text{I}^2\text{C}$ Mode. CDIN is the input data line for the control port interface in SPI Mode.

### CS2000-CP Block Diagram



## CS4398 (AUDIO : IC61)

DSD_B	1		28	DSD_A
DSD_SCLK	2		27	VLS
SDIN	3		26	VQ
SCLK	4		25	AMUTEC
LRCK	5		24	AOUTA-
MCLK	6		23	AOUTA+
VD	7		22	VA
DGND	8		21	AGND
M3 (AD1/CDIN)	9		20	AOUTB+
M2 (SCL/CCLK)	10		19	AOUTB-
M1 (SDA/CDOUT)	11		18	BMUTEC
M0 (AD0/CS)	12		17	VREF
RST	13		16	REF_GND
VLC	14		15	FILT+

### CS4398 Terminal Functions

Pin Name	Pin #	Pin Description
DSD_A	28	Direct Stream Digital Input ( <i>Input</i> ) - Input for Direct Stream Digital serial audio data.
DSD_B	1	Direct Stream Digital Input ( <i>Input</i> ) - Input for Direct Stream Digital serial audio data.
DSD_SCLK	2	DSD Serial Clock ( <i>Input</i> ) - Serial clock for the Direct Stream Digital audio interface.
SDIN	3	Serial Audio Data Input ( <i>Input</i> ) - Input for two's complement serial audio data.
SCLK	4	Serial Clock ( <i>Input</i> ) - Serial clock for the serial audio interface.
LRCK	5	Left Right Clock ( <i>Input</i> ) - Determines which channel, Left or Right, is currently active on the serial audio data line.
MCLK	6	Master Clock ( <i>Input</i> ) - Clock source for the delta-sigma modulator and digital filters.
VD	7	Digital Power ( <i>Input</i> ) - Positive power for the digital section.
DGND	8	Digital Ground ( <i>Input</i> ) - Ground reference for the digital section.
RST	13	Reset ( <i>Input</i> ) - The device enters system reset when enabled.
VLC	14	Control Port Power ( <i>Input</i> ) - Positive power for Control Port I/O.
FILT+	15	Positive Voltage Reference ( <i>Output</i> ) - Positive reference voltage for the internal sampling circuits.
REF_GND	16	Reference Ground ( <i>Input</i> ) - Ground reference for the internal sampling circuits.
VREF	17	Voltage Reference ( <i>Input</i> ) - Positive voltage reference for the internal sampling circuits.
BMUTEC	18	Mute Control ( <i>Output</i> ) - The Mute Control pin is active during power-up initialization, muting, power-down or if the master clock to left/right clock frequency ratio is incorrect. During reset, these outputs are set to a high impedance.
AOUTB+	20	Differential Right Channel Analog Output ( <i>Output</i> ) - The full-scale differential analog output level is specified in the Analog Characteristics specification table.
AOUTB-	19	Differential Right Channel Analog Output ( <i>Output</i> ) - The full-scale differential analog output level is specified in the Analog Characteristics specification table.
AGND	21	Analog Ground ( <i>Input</i> ) - Ground reference for the analog section.
VA	22	Analog Power ( <i>Input</i> ) - Positive power for the analog section.
AOUTA+	23	Differential Left Channel Analog Output ( <i>Output</i> ) - The full-scale differential analog output level is specified in the Analog Characteristics specification table.
AOUTA-	24	Differential Left Channel Analog Output ( <i>Output</i> ) - The full-scale differential analog output level is specified in the Analog Characteristics specification table.
VQ	26	Quiescent Voltage ( <i>Output</i> ) - Filter connection for internal quiescent voltage.
VLS	27	Serial Audio Interface Power ( <i>Input</i> ) - Positive power for serial audio interface I/O.
Stand-Alone Mode Definitions		
M3	9	
M2	10	Mode Selection ( <i>Input</i> ) - Determines the operational mode of the device.
M1	11	
M0	12	
Control Port Mode Definitions		
AD1/CDIN	9	Address Bit 1 (I <sup>2</sup> C) / Control Data Input (SPI) ( <i>Input</i> ) - AD1 is a chip address pin in I <sup>2</sup> C mode; CDIN is the input data line for the Control Port interface in SPI mode.
SCL/CCLK	10	Serial Control Port Clock ( <i>Input</i> ) - Serial clock for the serial Control Port.
SDA/CDOUT	11	Serial Control Data (I <sup>2</sup> C) / Control Data Output (SPI) ( <i>Input/Output</i> ) - SDA is a data I/O line in I <sup>2</sup> C mode. CDOUT is the output data line for the Control Port interface in SPI mode.
AD0/CS	12	Address Bit 0 (I <sup>2</sup> C) / Control Port Chip Select (SPI) ( <i>Input</i> ) - AD0 is a chip address pin in I <sup>2</sup> C mode; CS is the chip select signal for SPI format.

## ADuM1286(AUDIO : IC65)

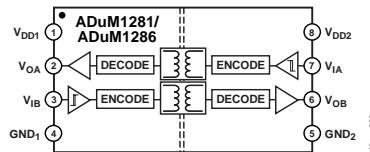


Figure 5. ADuM1281/ADuM1286 Pin Configuration

Table 21. ADuM1281/ADuM1286 Pin Function Descriptions

Pin No.	Mnemonic	Description
1	V <sub>DD1</sub>	2.7 V to 5.5 V Supply Voltage for Isolator Side 1.
2	V <sub>OA</sub>	Logic Output A.
3	V <sub>IB</sub>	Logic Input B.
4	GND <sub>1</sub>	Ground 1. Ground reference for Isolator Side 1.
5	GND <sub>2</sub>	Ground 2. Ground reference for Isolator Side 2.
6	V <sub>OB</sub>	Logic Output B.
7	V <sub>IA</sub>	Logic Input A.
8	V <sub>DD2</sub>	2.7 V to 5.5 V Supply Voltage for Isolator Side 2.

## ADuM1280 (AUDIO : IC68, IC69, IC70) / ADuM1285 (AUDIO : IC66, IC67)

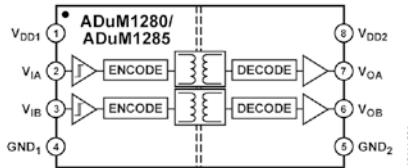


Figure 4. ADuM1280/ADuM1285 Pin Configuration

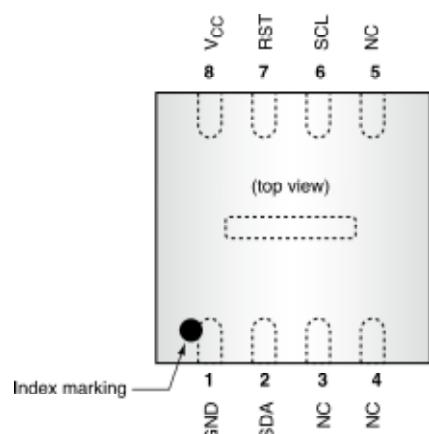
Table 20. ADuM1280/ADuM1285 Pin Function Descriptions

Pin No.	Mnemonic	Description
1	V <sub>DD1</sub>	2.7 V to 5.5 V Supply Voltage for Isolator Side 1.
2	V <sub>IA</sub>	Logic Input A.
3	V <sub>IB</sub>	Logic Input B.
4	GND <sub>1</sub>	Ground 1. Ground reference for Isolator Side 1.
5	GND <sub>2</sub>	Ground 2. Ground reference for Isolator Side 2.
6	V <sub>OB</sub>	Logic Output B.
7	V <sub>OA</sub>	Logic Output A.
8	V <sub>DD2</sub>	2.7 V to 5.5 V Supply Voltage for Isolator Side 2.

## MFI337S3959 (DIGITAL : IC25)

Signal name	Pin	I/O	Description
GND	1		Supply voltage, negative terminal
SDA	2	I/O	I <sup>2</sup> C data
NC	3-5		Must not be connected
SCL	6	I	I <sup>2</sup> C clock
RST	7	I	At reset: selects I <sup>2</sup> C slave address. During operation: CP warm reset.
V <sub>CC</sub>	8		Supply voltage, positive terminal

Figure 1-1 CP chip pinouts, top view



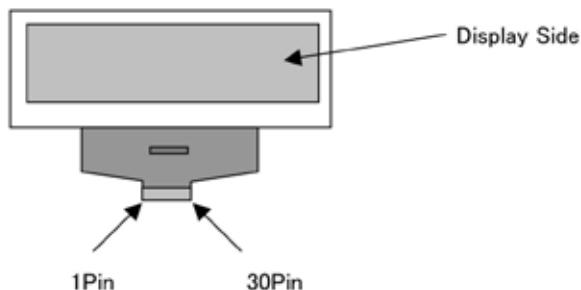
PG-USON-8-1 package

The thermal pad on the bottom of the CP may be left unconnected or optionally connected to GND.

## 2. DISPLAY

S020-MXS4035A-3

端子番号 Pin No.	端子名 Pin Name	入出力 IO	機能 Functions
1	VSS	P	グランド GND
2	VCC	P	ドライバー系陽極電源 Power supply for Anode Driver
3	VCOMH	O	ドライバー系陰極電源 Power supply for Cathode Driver
4	VLSS	P	アナロググランド Analog system ground
5	CLS	I	VDDIO に接続 Connected to VDDIO
6	D7	I	データバス Data Bus
7	D6	I	データバス Data Bus
8	D5	I	データバス Data Bus
9	D4	I	データバス Data Bus
10	D3	I	データバス Data Bus
11	D2	I	データバス Data Bus
12	D1 (SDIN)	I	データバス、またはシリアルデータ入力 Data Bus or Serial Date Input
13	D0 (SCLK)	I	データバス、またはシリアルクロック入力 Data Bus or Serial Clock Input
14	E, RD#	I	読み出し (シリアルインターフェース時、内部で "L" 固定になる) Read (This pin stays "L"(low) in Serial Interface Mode)
15	R/W#, WR#	I	書き込み (シリアルインターフェース時、内部で "L" 固定になる) Write (This pin stays "L"(low) in Serial Interface Mode)
16	BS0	I	インターフェース選択子 Select MCU bus interface setting •BS0=0, BS1=0 : 4 line SPI      •BS0=0, BS1=1 : 8bit 8080 Parallel •BS0=1, BS1=0 : 3 line SPI      •BS0=1, BS1=1 : 8bit 6800 Parallel
17	BS1	I	データ/コマンド切替制御 "H":データ, "L":コマンド Data/Command Control. "H":Data, "L":Command
18	D/C#	I	チップセレクト "L" でI/F通信可能 Chip Select, Active "L"
19	CS#	I	リセット "L" でリセット Reset, Active "L"
21	VSS	P	グランド GND
22	CL	I	VSSに接続してください。 Connected to VSS
23	IREF	O	陽極出力基準電流設定端子 Reference current setting
24	NC	-	
25	VDDIO	P	インターフェイス系電源 Power supply for Interface logic level
26	VDD	O	内部ロジック系電源 Power supply for Core logic operation
27	VCI	P	外部ロジック系電源 Low voltage power supply
28	VSL	P	陽極基準電位 Segment Reference voltage
29	VLSS	P	アナロググランド Analog system ground
30	VCC	P	ドライバー系陽極電源 Power supply for Anode Driver







REF No.	Part No.	Part Name	Remarks	Q'ty	New	Ver
★	nsp	HEAT SINK	CMY4A222-V2	1		
★	nsp	SCREW	CTB3+8JR	1		
★	nsp	HEAT SINK	CMY4A222-V2	1		
★	nsp	SCREW	CTB3+8JR	1		



REF No.	Part No.	Part Name	Remarks	Q'ty	New	Ver
JW82	nsp	W RE ASS'Y	CWE8102100RV	1		
L801-807	nsp	FERRITE CH P BEAD(2012/220R) BLM21PG221SN1/MURATAMURATA	CLZBLM21PG221SN1	7		
L809	nsp	FERRITE CH P BEAD(2012/220R) BLM21PG221SN1/MURATAMURATA	CLZBLM21PG221SN1	1		
L810,811	nsp	FERRITE CH P BEAD(2012/220R) BLM21AG121SN1/MURATAMURATA	CLZBLM21AG121SN1	2		
L812	nsp	FERRITE CH P BEAD(2012/220R) BLM21PG221SN1/MURATAMURATA	CLZBLM21PG221SN1	1		
PH81	90M-YT004500R	JACK PHONES(635mm SILVER) PJ-612A-51/YUQIU	CJ2E026Z	1		
S801-812	00D9430004402	SW TACT SKHV10910G	CST1A012ZT	12		
VR81	00D9430196908	IRES , VARIABLE	CVV2J02B103Z	1		
WF81	nsp	WAFER_FFC 125mm,ANGLE_125-2S-NPW	CJP27GB286ZN	1		
WF82	nsp	WAFER_FPC/FPC(30P, 1mm PITCH, ANGLE) FPC 10S-12X-NPW	CJP30GB305ZN	1		
★	963179100040S	OLED MODULE (MXS4035-A) S020-MXS4035-A-3	CFLMKS4035-A	1		
★	nsp	CUSHION_R	CHG1A577	1		
★	nsp	PLATE_EARTH	CMC1A348-V1	1		
★	nsp	COVER_SH ELD OLED	CMC1A448	1		
★	nsp	HOLDER_OLED	CMH1A346	1		





REF No.	Part No.	Part Name	Remarks	Q'ty	New	Ver
C761 715	00MOA227025R0	CAP ELECT(220uF/25V 10X20 ELNA/ROA)	CCEA1EROA221T	2		
C721 722	943134502670M	CAP ELECT(47uF/25V ROS ELNA ) ROS-25V470MG3#PE-T2	CCEA1EROS470T	2		
C723	nsp	CAP CH P(1608 50V/470pF C0G) SAMSUNG CL10C471JB8NNNC	CCUS1H471JAS	1		
C751 752	13405026100AS	CAP ELECT(3300uF/25V LKG NICHICON) LKG1E332MESBZT NICHICON	CCEA1ELKG332E	2		
C753 754	00MOA106025R1	CAP ELECT (10uF/25V ROA ELNA) ROA-25V100ME3#-T2	CCEA1EROA100T	2		
C755 756	00MOA477025R6	CAP ELECT (470uF/25V 12.5X25 ROA) ROA-25V471M 6#-S13	CCEA1EROA471E	2		
C757 758	00MOA227025R0	CAP ELECT(220uF/25V 10X20 ELNA/ROA)	CCEA1EROA221T	2		
C759 760	943134502650M	CAP ELECT (47uF/25V ROA ELNA) ROA-25V 470MG3#-T2	CCEA1EROA470T	2		
C761	00MOA22803520	CAP ELECT(35V/220uF)	CCEA1VH222E	1		
C762	nsp	CAP, ELECT(50V/0.1uF)	CCEA1HH0R1T	1		
C763	nsp	CAP, ELECT(50V/10uF)	CCEA1HH100T	1		
C769	nsp	CAP, CH P(1608, 50V/0.1uF, X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C771	nsp	CAP,ELECT(2200uF/25V , RA3 , ELNA)	CCEA1ERA3222E	1		
C772	00MOA107025R1	CAP, ELECT(ROA, 25V/100UF, 10X16) ROA-25V101MH4#-T2	CCEA1EROA101T	1		
C773	nsp	CAP, CH P(1608, 50V/0.1uF, X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C774	943134502660M	CAP, ELECT (22uF/25V, ROS, ELNA) 22 UF M 25V ARS-TYPE ELNA	CCEA1EROS220T	1		
C775	00MOA107025Z1	CAP, ELECT (ELNA/ROS, 25V/100uF)	HCEA1ER101T	1		
C776	nsp	CAP, CH P(1608, 50V/0.1uF, X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C777	943134502660M	CAP, ELECT (22uF/25V, ROS, ELNA) 22 UF M 25V ARS-TYPE ELNA	CCEA1EROS220T	1		
C781,782	nsp	CAP, MYLAR(50V/1000pF/J)	HCO1H102JZT	2		
C785	nsp	CAP, CH P(1608, 50V/0.01uF, X7R) SAMSUNG CL10B103KB8NNNC	CCUS1H103KCS	1		
C786	nsp	CAP, CH P(1608, 50V/0.1uF, X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C787	nsp	CAP, CH P(1608, 50V/1000pF, X7R) SAMSUNG CL10B102KB8NNNC	CCUS1H102KCS	1		
C788	nsp	CAP, CH P(1608, 50V/0.01uF, X7R) SAMSUNG CL10B103KB8NNNC	CCUS1H103KCS	1		
C789	nsp	CAP, CAP CH P(1608 50V/0.1uF X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C790	nsp	CAP, CH P(1608 50V/1000pF X7R) SAMSUNG CL10B102KB8NNNC	CCUS1H102KCS	1		
C791	nsp	CAP, CH P(1608 50V/0.1uF X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C792	nsp	CAP, CH P(1608 50V/0.01uF X7R) SAMSUNG CL10B103KB8NNNC	CCUS1H103KCS	1		
C793	nsp	CAP, CH P(1608 50V/1000pF X7R) SAMSUNG CL10B102KB8NNNC	CCUS1H102KCS	1		
C794	nsp	CAP, CH P(1608 50V/0.1uF X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C795	nsp	CAP, CAP CH P(1608 50V/0.01uF X7R) SAMSUNG CL10B103KB8NNNC	CCUS1H103KCS	1		
C796	nsp	CAP, CH P(1608 50V/1000pF X7R) SAMSUNG CL10B102KB8NNNC	CCUS1H102KCS	1		
C797	nsp	CAP, CH P(1608 50V/0.1uF X7R) SAMSUNG CL10B104KB8NNNC	CCUS1H104KCS	1		
C798	nsp	CAP, CH P(1608 50V/0.01uF X7R) SAMSUNG CL10B103KB8NNNC	CCUS1H103KCS	1		
C799	nsp	CAP, CH P(1608, 50V/1000pF, X7R) SAMSUNG CL10B102KB8NNNC	CCUS1H102KCS	1		
<b>OTHER PARTS GROUP</b>						
CN81	nsp	LOCK NG TYPE , STRAIGHT WAFER , 2MM	CJP09G1236ZW	1		
CN98	nsp	LOCK NG TYPE , STRAIGHT WAFER , 2mm	CJP05G1236ZW	1		
B603,604	nsp	PLATE , EARTH(TRONIC ELECTRONICS)	CJT1A026	2		
BK61,62	nsp	BRACKET , PCB	CMD1A569-V1	2		
F601-603	90M-FS001530R	FUSE(0 8A, 372 SER ES/TR5) 0.8A, 372 SERIES/TR5	KBA2D0800A3EYT	3		
JK61	943643101920S	JACK , RCA (1P, BK, GOLD PLATE) RJ-011R-29T	CJJ4M073ZY	1		
JK71	943643101920S	JACK , RCA (1P, BK, GOLD PLATE) RJ-011R-29T	CJJ4M073ZY	1		
L602,603	nsp	FERRITE CHIP BEAD(2012/220R) BLM21PG221SN1/MURATAMURATA	CLZBLM21PG221SN1	2		
L604	nsp	FERRITE CHIP BEAD(2012/220R) BLM21AG121SN1/MURATAMURATA	CLZBLM21AG121SN1	1		
L607,608	nsp	FERRITE CHIP BEAD(2012/220R) BLM21PG221SN1/MURATAMURATA	CLZBLM21PG221SN1	2		
WF61	nsp	WAFER, FFC(19P-1 25mm, STRAIGHT) 12511HS-19/YEONHO	CJP19GA115ZY	1		
X601	14181008150AS	OSC, SMD(24.576MHz, NZ2520S) NZ2520S NDK	COX245761150SN	1		
X602	14181008250AS	OSC, SMD(22.5792MHz, NZ2520SD) NZ2520S NDK	COX225791150SN	1		
	nsp	HEAT S NK	CMY2A223-V2	1		
	943219500190M	T.R 2SD2081 NPN TO220F SANKEN 2SD2081	CVT2SD2081	1		
	nsp	HEAT S NK	CMY2A223-V2	1		
	943219500170M	T.R 2SB1259 PNP TO220F SANKEN 2SB1259	CVT2SB1259	1		









**EXPLODED**

※Parts indicated by "nsp" on this table cannot be supplied.

※The parts listed b NOTE: The symbols in the column Remarks indicate the following destinations.

U : North America model N : Europe model K : China model F : Japan model

B : Black model SG : Silver gold model

REF No.	Part No.	Part Name	Remarks	Q'ty	New	Ver
★	nsp	FRONT PCB				
LP1	-----	FRONT PCB ASS'Y	COP12593C	1		
LP2	-----	PHONE PCB ASS'Y	COP12593C	1		
LP3	-----	STANDBY PCB ASS'Y	COP12593C	1		
LP10	-----	CARD CABLE PCB	COP12593C	1		
★	nsp	POWER PCB				
LP4	-----	POWER PCB ASS'Y	COP12587C	1		
LP5	-----	SMPS PCB ASS'Y	COP12587C	1		
P6	9U6391013200M	DIGITAL PCB ASS'Y N	COP12585J	1 *		
P6	9U6391015400M	DIGITAL PCB ASS'Y U	COP12585L	2 *		
P6	9U6391015600M	DIGITAL PCB ASS'Y K	COP12585M	3 *		
P6	9U6391015600M	DIGITAL PCB ASS'Y F	COP12585K	4 *		
P7	nsp	AUDIO PCB ASS'Y	COP12586C	1		
F1	421410006004M	BADGE , MARANTZ	CGB1A206	1		
F2	42141003200AM	BADGE , STAR MARK	SG	CGB1A270D11	1	
F2	42141003201AM	BADGE , STAR MARK	B	CGB1A270K117	1	
F3	943416101110M	WINDOW	CGU1A481A32Z	1 *		
F4	00M24AW154120	KNOB , LEVEL	SG	CBN1A170RMD10	1	
F4	943411007050M	KNOB , LEVEL	B	CBN1A170B37	1	
F5	943402104060M	PANEL , AL FRONT	SG	CKM1A264YC62	1 *	
F5	943402104070M	PANEL , AL FRONT	B	CKM1A264ZC23	1 *	
F6	943402105000M	PANEL , SIDE(L)	SG	CGW2A463ROSD10	1 *	
F6	943402105010M	PANEL , SIDE(L)	N1B,U1B	CGW2A463RNTB37	1 *	
F6	943402105020M	PANEL , SIDE(L)	K1B	CGW2A463RNRB37	1 *	
F7	943402105030M	PANEL , SIDE(R)	SG	CGW2A464ROD10	1 *	
F7	943402105040M	PANEL , SIDE(R)	B	CGW2A464RN837	1 *	
F8	nsp	SHEET , LED		CGX1A496Z	1	
F9	nsp	PANEL , SUB	SG	CGW1A549RMZD10	1	
F9	nsp	PANEL , SUB	B	CGW1A549B37	1	
F10	481510003006M	INDICATOR , POWER	CGL1A274	1		
F11	411510021036M	KNOB , POWER	SG	CBT1A1072RMD10	1	
F11	411510015017M	KNOB , POWER	B	CBT1A1072	1	
F12	943412101300M	CURSOR KNOB ASS'Y	SG	CGK2A175RA	1 *	
F12	943412101310M	CURSOR KNOB ASS'Y	B	CGK2A175UA	1 *	
F13	943412101320M	ENTER KNOB ASS'Y	SG	CGK1A176UA	1 *	
F13	943412101330M	ENTER KNOB ASS'Y	B	CGK1A176TA	1 *	
F14	943411103250M	BUTTON , INPUT	SG	CBT1A1190RMD10	1 *	
F14	943411103250M	BUTTON , INPUT	B	CBT1A1190	1 *	
F15	943411103270M	CURSOR BUTTON ASS'Y	SG	CGK2A175TA	1 *	
F15	943411103280M	CURSOR BUTTON ASS'Y	B	CGK2A175SA	1 *	
F16	943411103290M	PLAY BUTTON ASS'Y	SG	CGK1A176SA	1 *	
F16	943411103300M	PLAY BUTTON ASS'Y	B	CGK1A176VA	1 *	
F17	nsp	CHASSIS , FRONT		CUF1A024	1	
M1	nsp	FRAME , FRONT		CUF2A004	1	
M2	nsp	CHASSIS , BOTTOM		CUA5A289	1	
M3	nsp	PLATE , BOTTOM		CUA1A350K1	1	
M4	00M243W057210	FOOT , FRONT		CKL2A042H46	4	
M5	00M32CW107010	CUSHION , FOOT		CHG1A360	4	
M6	nsp	HOLDER , PCB		CHE170	2	
M7	nsp	HOLDER , PCB		CHE2A030	7	
M8	nsp	COVER , SCREW		CMD1A495	1	
M9	nsp	PANEL , REAR		CKF1A477Z(N),X(F),W(K)	1	
M9	nsp	PANEL , REAR		CKF1A477Z(N),Y(U),W(K)	1	
M10	401310003033M	CABINET , TOP	SG	CKC2A187D11	1	
M10	401310003002M	CABINET , TOP	B	CKC2A187K117	1	
M11	nsp	CUSHION		CHG1A160Z	1	
M12	nsp	TAPE , HEMELON		CHS1A032	3	
M13	nsp	SCREW , COVER		CGX1A439	1	
M14	nsp	CLAMPER , WIRE		CHE36-3	2	
P8	943101102280M	TRANS , POWER ( 230V/50Hz, NA8005/N1 )	N	CLT5P054ZE	1 *	
P8	943101102290M	TRANS , POWER ( 120V/60Hz, NA8005/U1B )	U	CLT5P054ZU	1 *	
P8	943101102480M	TRANS , POWER ( 220V/50Hz, NA8005/K1B )	K	CLT5P054ZH	1 *	
P8	943101102300M	TRANS , POWER ( 100V/50-60Hz, NA8005/FN )	F	CLT5P054ZJ	1 *	
P9	00MYJ04002640	RECEPTACLE , AC		CJJ8A006ZW	1	
S1	nsp	SCREW	SG	CTB3+6FR	3	
S2	nsp	SCREW	SG	CTB3+6FFB	5	
S3	nsp	SCREW	SG	CTB3+8JR	18	
S4	nsp	SCREW	SG	CTB3+8JFB	42	
S5	nsp	SCREW	SG	CTW3+8JR	6	
S6	nsp	SCREW	SG	CTW3+8JFB	7	
S7	nsp	SCREW	SG	CTW3+12JR	2	
S8	nsp	SCREW	SG	CTW3+18JR	7	
S9	nsp	SCREW	SG	CTBR4+8FFB	4	
S10	nsp	WASHER , GND	SG	CNW1A051	4	
S11	nsp	WASHER , GND	B	CNW1A035	3	
★	943606502510S	FFC(27P, 80MM, 125MM, B, 10MM)		CWC4C4A27B300B10	1 *	
★	943606502520S	FFC(21P, 80MM, 1.25MM, B, 10MM)		CWC4C4A21B100B10	1 *	
★	943606502530S	FFC(19P, 100MM, 1.25MM, B, 10MM, SHIELD)		CWC6F4A19B100B10	1 *	
★	nsp	BRACKET , PHONE		CMD1A677-V1	1	
★	nsp	2P WIRE ASS'Y(100MM)		CWZPM5003TW91A	1	
★	nsp	NUT , PHONE		CNE1A013	1	

## PACKING

※Parts indicated by "nsp" on this table cannot be supplied.

※The parts listed t NOTE:The symbols in the column Remarks indicate the following destinations.

U : North America model N : Europe model K : China model F : Japan model

B : Black model SG : Silver gold model

REF No.	Part No.	Part Name	Remarks	Q'ty	New	Ver
1	943531103890M	BOX OUTCARTON	CPG1A994X	1	*	
2	nsp	BAG POLY	CPB1A213	1		
3-1	943533101940M	PAD SNOWBOTTOM(F/R)	CPS2A964	1	*	
3-2	943533101930M	PAD SNOWTOP(F/R)	CPS2A962	1	*	
4	-----	INSTRUCTIONMANUALASS'Y	-----	1		
4-0	35201029301AM	CDMANUAL N	N CFT1A118ZA	1	*	
4-0	35201029300AM	CDMANUAL U	U CFT1A119ZA	1	*	
4-0	35201029303AM	CDMANUAL K	K CFT1A120ZA	1	*	
4-1	54111112100AM	MANUAL_INSTRUCTION	F CQX1A1773Z	1	*	
4-2	nsp	SAFETYINSTRUCTION	F CQE1A712Z	1		
4-2	nsp	SAFETYINSTRUCTION	K CQE1A708Z	1		
4-2	nsp	SAFETYINSTRUCTION	U CQE1A706Z	1		
4-2	nsp	SAFETYINSTRUCTION	N CQE1A689Z			
4-3	54111112202AM	MANUAL GETT NGSTART	F CQX1A1776Z	1	*	
4-3	54111112200AM	MANUAL GETT NGSTART	U CQX1A1775Z	1	*	
4-3	54111112201AM	MANUAL GETT NGSTART	N CQX1A1774Z	1	*	
4-3	54111112203AM	MANUAL_GETT NGSTART	K CQX1A1777Z	1	*	
4-4	nsp	CARD_USER(JAPAN)	F CQE1A139S	1		
4-5	nsp	SHEET,ADDRESS(JAPAN)	F CQE1A195N	1		
4-5	nsp	CARDFORCHINAIDENTIFICATION	K CQE1A450Z	1		
4-6	nsp	CARD,WARRANTY	U CQE1A131V	1		
4-6	nsp	WARRANTYCANADA	U CQE1A132V	1		
5	611050028007S	CORD,POWER F	F CJA2J115ZV	1		
5	90M-ZC000650R	CORD,POWER K	K CJA2N075Z	1		
5	90M-ZC000310R	CORD,POWER U	U CJA2A070Z	1		
5	90M-ZC000320R	CORD POWER N	N CJA2B054Z	1		
6	nsp	BAG POLY		CPB1A213	1	
7	30701016100AM	REMOCON RC003NA		CARTNA8005A1	1	
8	nsp	LABEL CONTROL		CQB1A993Z	1	
8-1	nsp	LABEL_SERIAL		CQB1A993Z	1	
8-2	nsp	LABEL_DATE	F, K CQB1A994Z	1		
9	nsp	BATTERY(SIZE'AAA')		CABR03PPB	1	
10	nsp	2PCORD.PIN		CJS4M009X	1	
11	nsp	1PCORD.PIN		CJS4N014Z	1	
13	nsp	CARD,WARRANTY(JAPAN)	F CQE1A123W	1		
13	nsp	CARD,WARRANTYCHINA	K CQE1A449W	1		
14	nsp	LABEL,WHITEM1SG	SG CQB1A908Z	1		
15	nsp	SpotifyLabel	N CQB1A1273Z	1		
15	nsp	LABEL QPLAY	K CQB1A1310Z	1		