

# Service Service Service



# Service Manual



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**CLASS 1  
LASER PRODUCT**

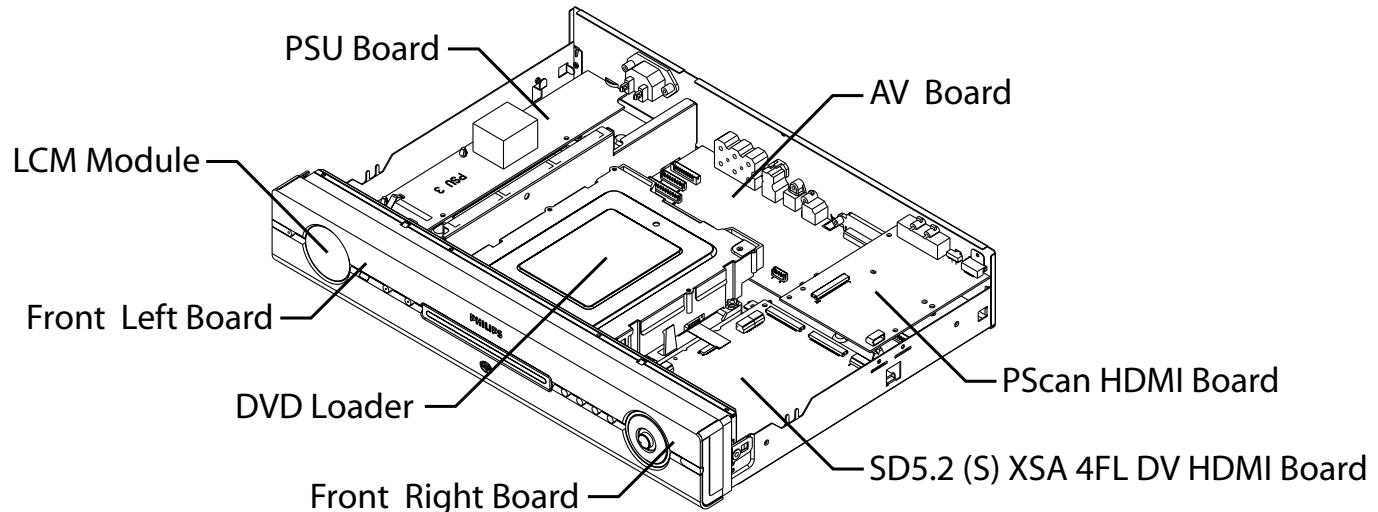
3139 785 30880

**Version 1.0**



**PHILIPS**

## **LOCATION OF PC BOARDS**



#### **VERSION VARIATIONS:**

**SPECIFICATIONS****GENERAL:**

Mains voltage : 220-240V for /00  
                   110-127V/220-240V for /69  
 Mains frequency : 50/60Hz  
 Power consumption : 25W  
                   < 1W at Standby  
 Dimension centre unit : 435 x 79 x 315mm

**AUDIO PERFORMANCE:**

Output Voltage : 2.0Vrms  
 Channel Unbalance (1kHz) : < 0.5dB  
 Crosstalk (1kHz) : 115dB  
 Crosstalk (20Hz-20kHz) : 100dB  
 Freq. Response (20Hz-20kHz) : < 0.2dB  
 THD (1kHz) : 95dB  
 THD (20Hz-20kHz) : 90dB

**AUDIO FORMAT:**

MPEG : Compressed Digital  
 Dolby Digital : 16, 20, 24 bits  
 PCM : fs, 44.1, 48, 96 kHz  
 MP3 (ISO 9660) : 96, 112, 128, 256 kbps & variable  
                   bit rate fs, 32, 44.1, 48 kHz

**VIDEO PERFORMANCE:**

Video Output <sup>1)</sup> : 1.0V<sub>p-p</sub>

S-Video Output <sup>1)</sup>  
                   Y : 1.0V<sub>p-p</sub>  
                   C : 0.3V<sub>p-p</sub>

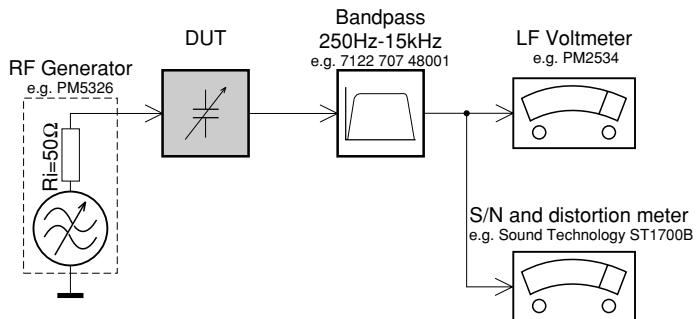
Component Video Output <sup>1)</sup>

Y : 1.0V<sub>p-p</sub>  
 PbPr : 0.7V<sub>p-p</sub>

<sup>1)</sup> Output terminals to be terminated with 75Ω

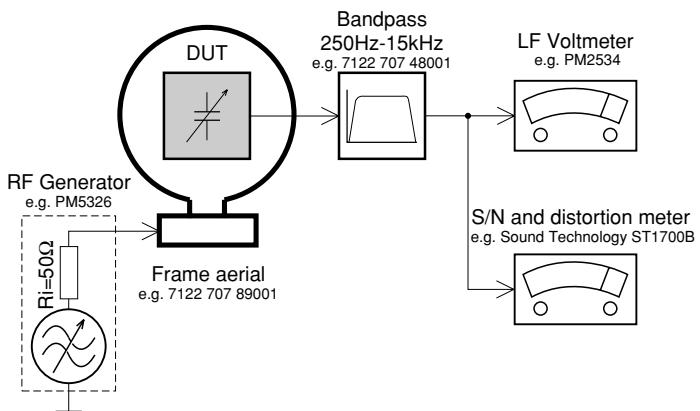
## MEASUREMENT SETUP

### Tuner FM



Use a bandpass filter to eliminate hum (50Hz, 100Hz) and disturbance from the pilottone (19kHz, 38kHz).

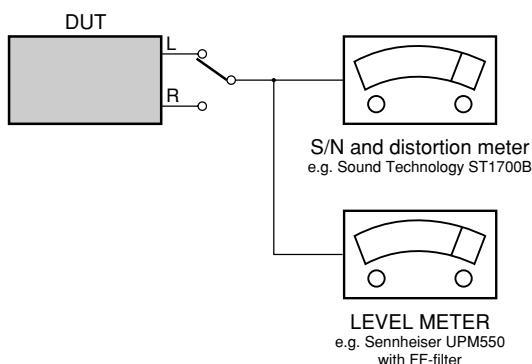
### Tuner AM (MW,LW)



To avoid atmospheric interference all AM-measurements have to be carried out in a Faraday's cage.  
Use a bandpass filter (or at least a high pass filter with 250Hz) to eliminate hum (50Hz, 100Hz).

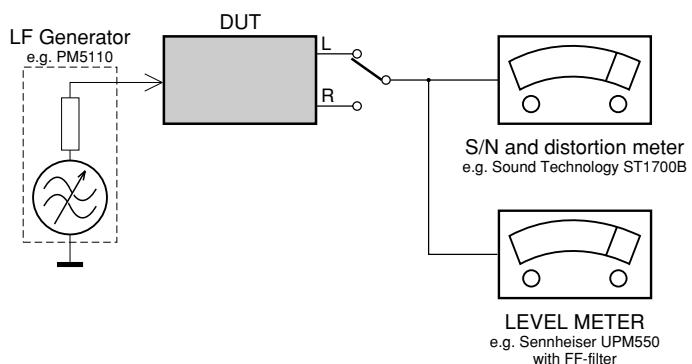
### CD

Use Audio Signal Disc SBC429 4822 397 30184  
(replaces test disc 3)



### Recorder

Use Universal Test Cassette **CrO<sub>2</sub>** SBC419 4822 397 30069  
or Universal Test Cassette **Fe** SBC420 4822 397 30071



## SERVICE AIDS

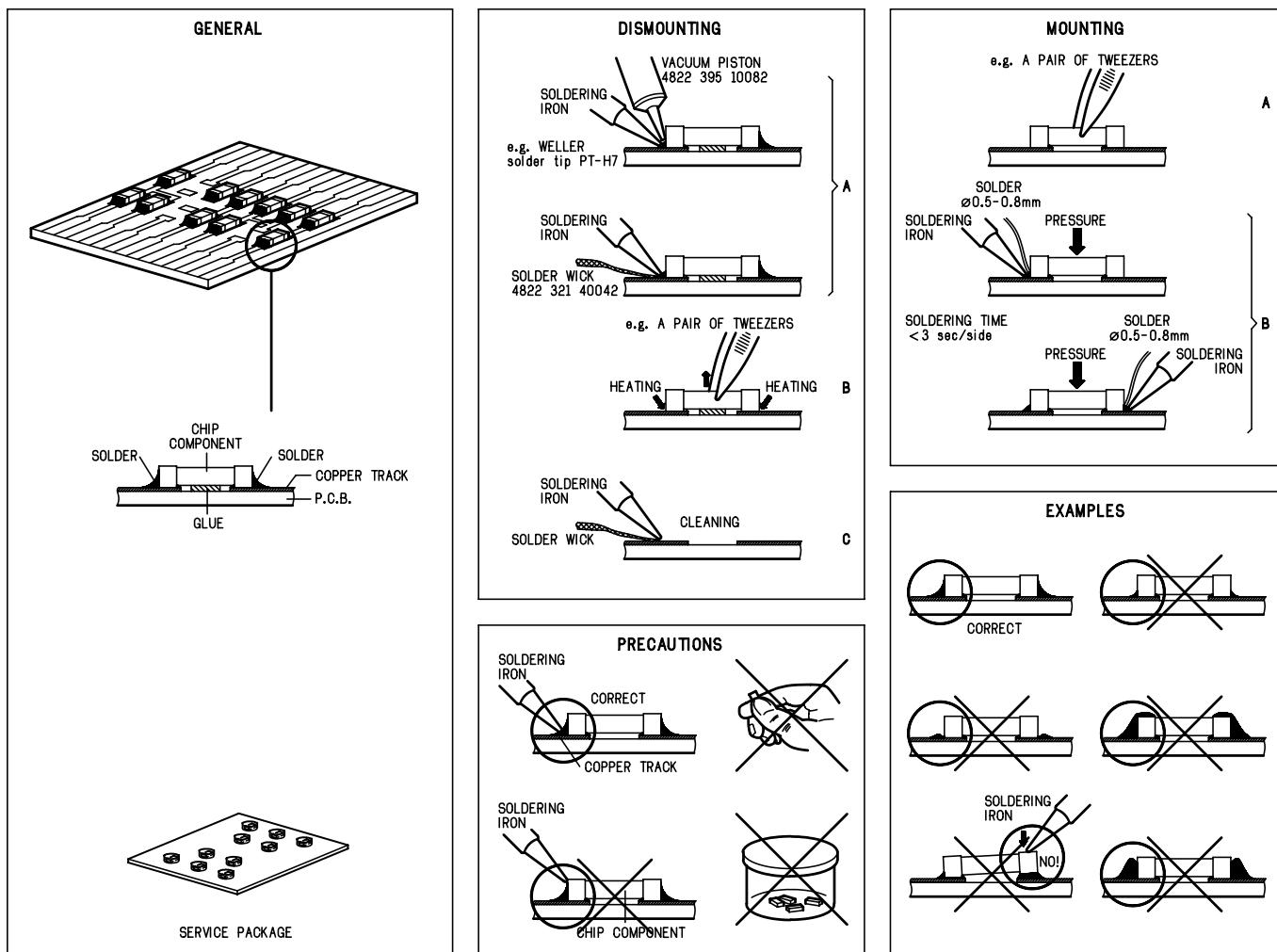
### Service Tools:

Universal Torx driver holder .....	4822 395 91019
Torx bit T10 150mm .....	4822 395 50456
Torx driver set T6 - T20 .....	4822 395 50145
Torx driver T10 extended .....	4822 395 50423

### Compact Disc:

SBC426/426A Test disc 5 + 5A .....	4822 397 30096
SBC442 Audio Burn-in Test disc 1kHz .....	4822 397 30155
SBC429 Audio Signals disc .....	4822 397 30184
Dolby Pro-logic Test Disc .....	4822 395 10216

## HANDLING CHIP COMPONENTS



**WARNING**

All ICs and many other semi-conductors are susceptible to electrostatic discharges (ESD). Careless handling during repair can reduce life drastically.

When repairing, make sure that you are connected with the same potential as the mass of the set via a wrist wrap with resistance. Keep components and tools also at this potential.

**ESD****F ATTENTION**

Tous les IC et beaucoup d'autres semi-conducteurs sont sensibles aux décharges statiques (ESD). Leur longévité pourrait être considérablement écourtée par le fait qu'aucune précaution n'est prise à leur manipulation.

Lors de réparations, s'assurer de bien être relié au même potentiel que la masse de l'appareil et enfiler le bracelet serti d'une résistance de sécurité.

Veiller à ce que les composants ainsi que les outils que l'on utilise soient également à ce potentiel.

**D****WARNUNG**

Alle ICs und viele andere Halbleiter sind empfindlich gegenüber elektrostatischen Entladungen (ESD). Unsorgfältige Behandlung im Reparaturfall kann die Lebensdauer drastisch reduzieren. Veranlassen Sie, dass Sie im Reparaturfall über ein Pulssarmband mit Widerstand verbunden sind mit dem gleichen Potential wie die Masse des Gerätes. Bauteile und Hilfsmittel auch auf dieses gleiche Potential halten.

**WAARSCHUWING**

Alle IC's en vele andere halfgeleiders zijn gevoelig voor electrostatische ontladingen (ESD). Onzorgvuldig behandelen tijdens reparatie kan de levensduur drastisch doen verminderen. Zorg ervoor dat u tijdens reparatie via een polsband met weerstand verbonden bent met hetzelfde potentiaal als de massa van het apparaat.

Houd componenten en hulpmiddelen ook op ditzelfde potentiaal.

**AVVERTIMENTO**

Tutti IC e parecchi semi-conduttori sono sensibili alle scariche statiche (ESD). La loro longevità potrebbe essere fortemente ridotta in caso di non osservazione della più grande cauzione alla loro manipolazione. Durante le riparazioni occorre quindi essere collegato allo stesso potenziale che quello della massa dell'apparecchio tramite un braccialetto a resistenza. Assicurarsi che i componenti e anche gli utensili con quali si lavora siano anche a questo potenziale.

**ESD PROTECTION EQUIPMENT:**

Complete Kit ESD3 (small tablemat, wristband, connection box, extention cable and earth cable) ..... 4822 310 10671  
Wristband tester ..... 4822 344 13999



Safety regulations require that the set be restored to its original condition and that parts which are identical with those specified, be used

Safety components are marked by the symbol  $\Delta$ .



Veiligheidsbepalingen vereisen, dat het apparaat bij reparatie in zijn oorspronkelijke toestand wordt teruggebracht en dat onderdelen, identiek aan de gespecificeerde, worden toegepast.

De Veiligheidsonderdelen zijn aangeduid met het symbool  $\Delta$



Les normes de sécurité exigent que l'appareil soit remis à l'état d'origine et que soient utilisées les pièces de rechange identiques à celles spécifiées.

Less composants de sécurité sont marqués  $\Delta$



Bei jeder Reparatur sind die geltenden Sicherheitsvorschriften zu beachten. Der Original zustand des Geräts darf nicht verändert werden; für Reparaturen sind Original-Ersatzteile zu verwenden.

Sicherheitsbauteile sind durch das Symbol  $\Delta$  markiert.



Le norme di sicurezza esigono che l'apparecchio venga rimesso nelle condizioni originali e che siano utilizzati i pezzi di ricambio identici a quelli specificati.

Componenti di sicurezza sono marcati con  $\Delta$



After servicing and before returning set to customer perform a leakage current measurement test from all exposed metal parts to earth ground to assure no shock hazard exist. The leakage current must not exceed 0.5mA.

**Warning !**

Invisible laser radiation when open.  
Avoid direct exposure to beam.

**Varning !**

Osynlig laserstrålning när apparaten är öppnad och spärren är urkopplad. Betrakta ej strålen.

**Varoitus !**

Avatussa laitteessa ja suojalukituksen ohittaaessa olet alittiina näkymättömälle laserisäteilylle. Älä katso sääteeseen!

**Advarse !**

Osnylig laserstråling ved åbning når sikkerhedsafbrydere er ude af funktion. Undgå udsættelse for stråling.



"Pour votre sécurité, ces documents doivent être utilisés par des spécialistes agréés, seuls habilités à réparer votre appareil en panne".

## DISMANTLING INSTRUCTIONS

### Dismantling of the DVD Loader

- 1) The tray can be manually open by inserting a screw driver and push the lever in the direction as shown in Figure 1 to unlock the tray before sliding it out.
- 2) Slide out the tray and remove the Cover Tray assembly (pos 113 + pos 115 + pos 117) as shown in Figure 2.
- 3) Loosen 7 screws to remove the Cover Top (pos 197).
  - 2 screw each on the left & right side
  - 3 screws on the rear
- 4) Loosen 4 screws D (see Figure 5) to remove the DVD Loader (pos 1003-0001).
- 5) Loosen 3 screws E (see Figure 5) to remove the Plate Shield (pos 191) from the DVD Loader.

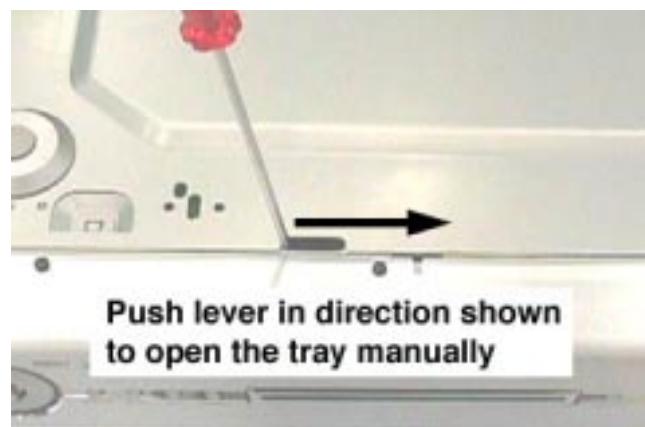


Figure 1

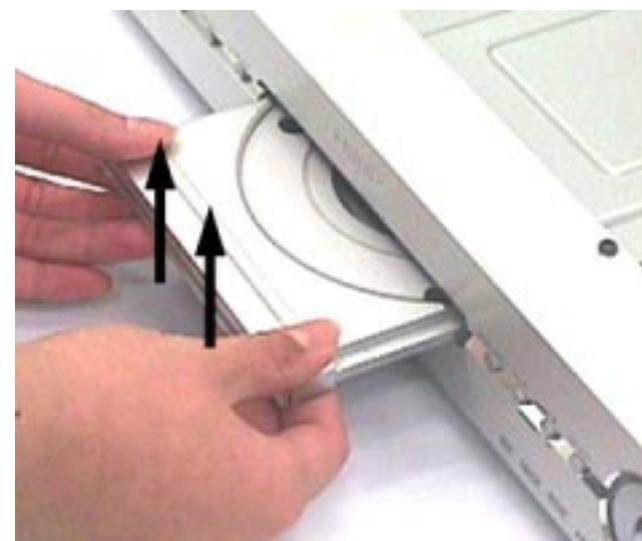


Figure 2

### Dismantling of the PSCAN HDMI Board, AV Board, PSU Board and SD5.2 (S) XSA 4FL DV HDMI Board

- 1) Loosen 3 screws A (see Figure 3 & 4) to remove the PSCAN HDMI Board (pos 1006).
- 2) Loosen 7 screws B (see Figure 3) and uncatch C1 (see Figure 5) to remove the AV Board (pos 1002).
- 3) Loosen 2 screws G and uncatch 2 catches C2 (see Figure 5) to remove the PSU Board (pos 1004).
- 4) Loosen 4 screws F (see Figure 5) to remove the SD5.2 (S) XSA 4FL DV HDMI Board (pos 1003-1001).

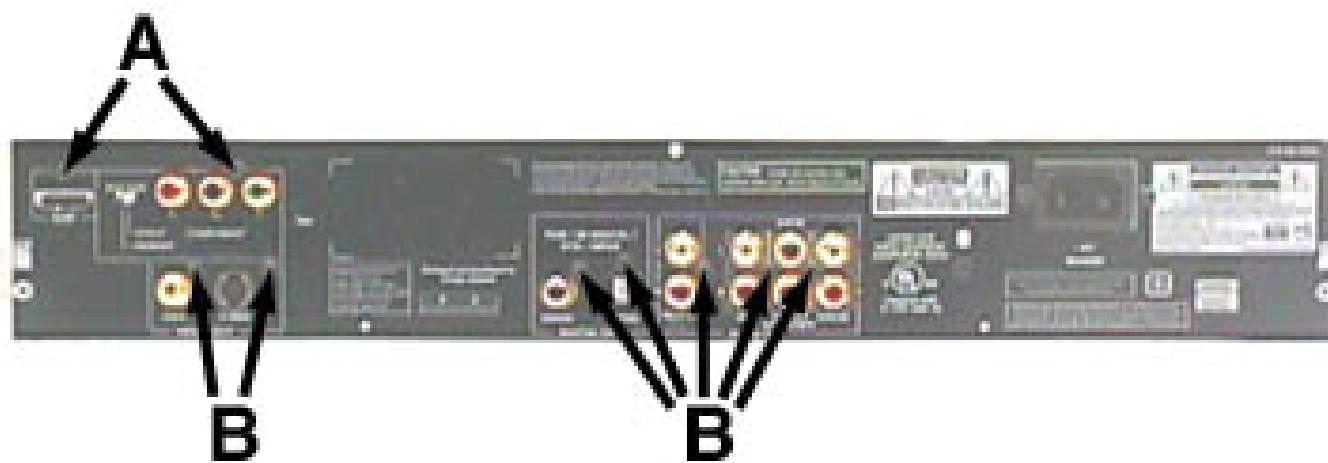


Figure 3

Figure 5

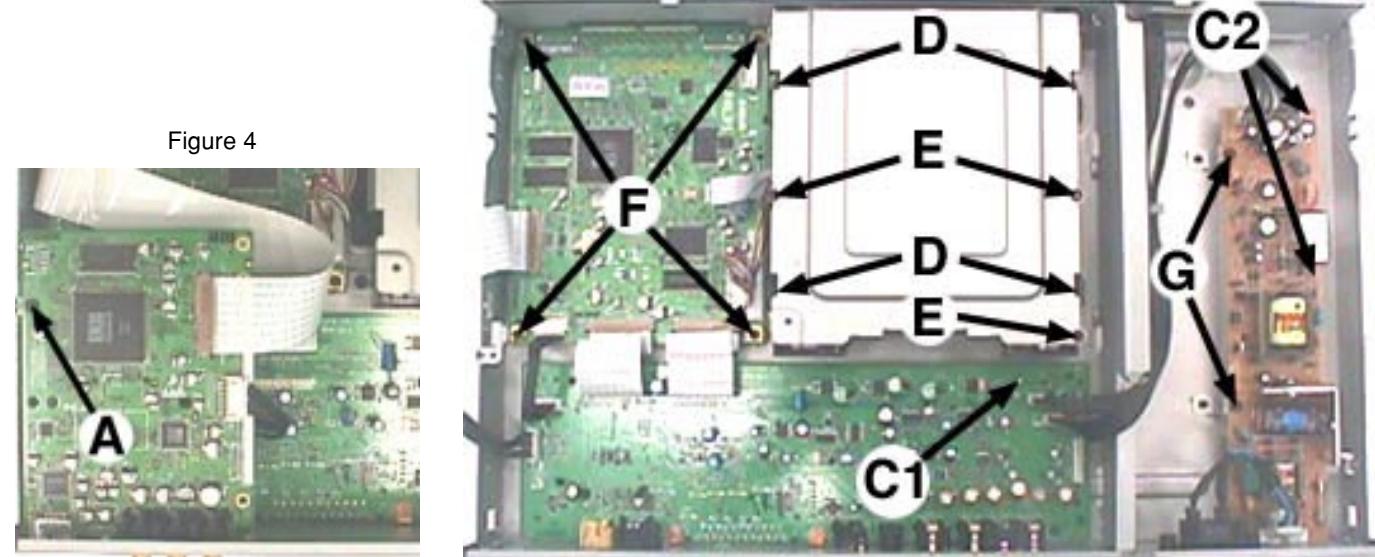
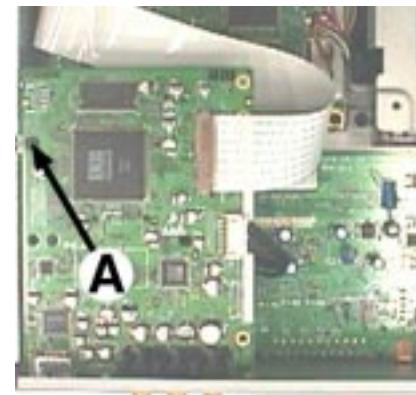


Figure 4



### Detaching the Cabinet Front assembly

- 1) Loosen 7 screws H (see Figure 6 & 7) and uncatch 2 catches C3 (see Figure 7) to detach the Cabinet Front assembly from the Frame (pos 161) by sliding it out towards the front.
  - see Service position C

Figure 6

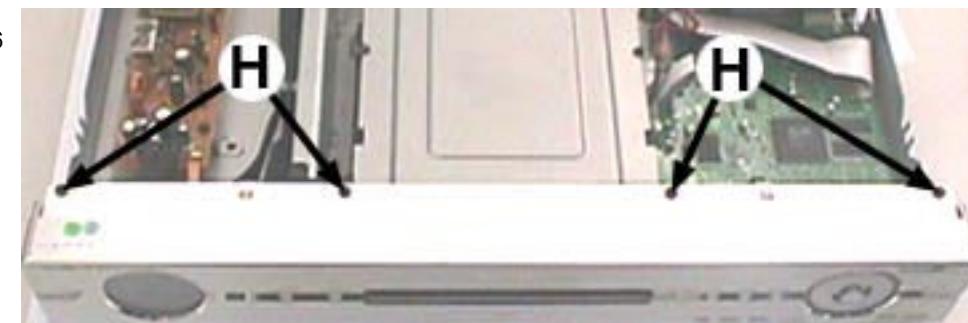
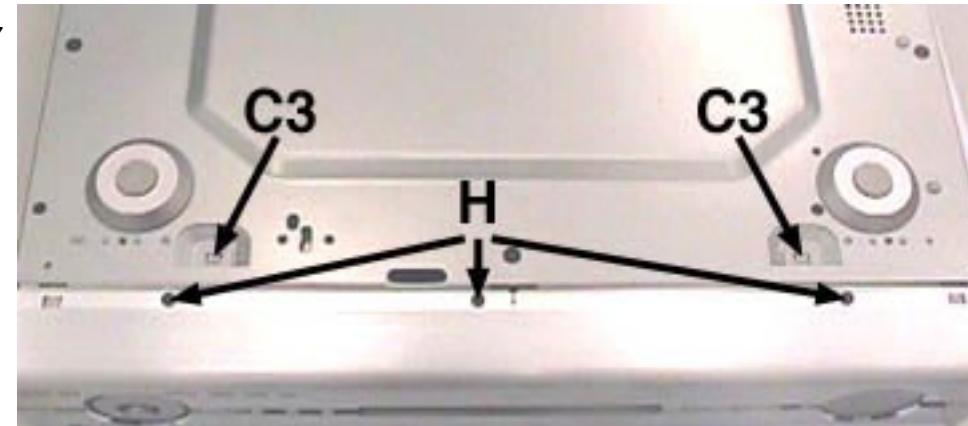
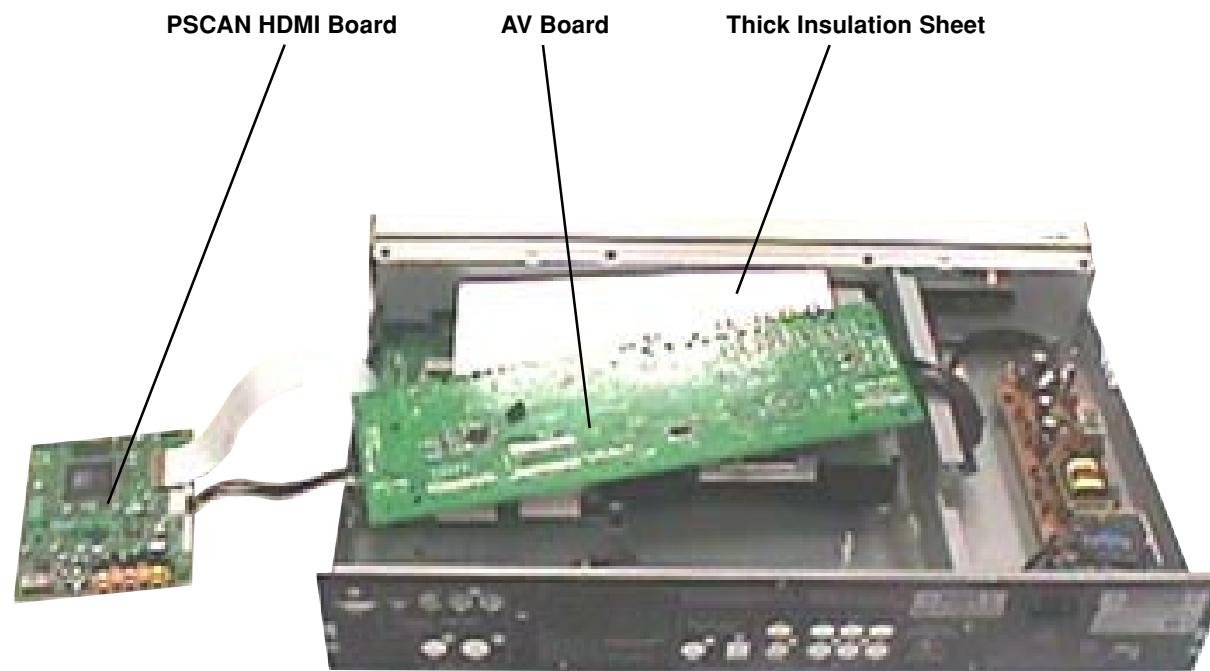


Figure 7

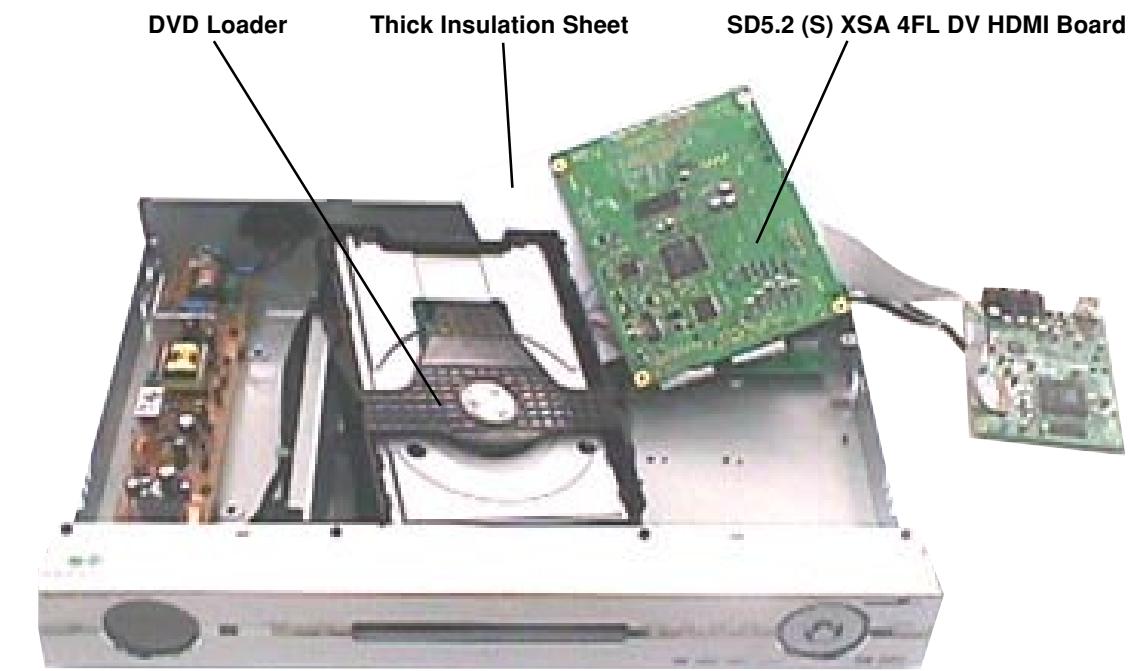


## SERVICE POSITIONS

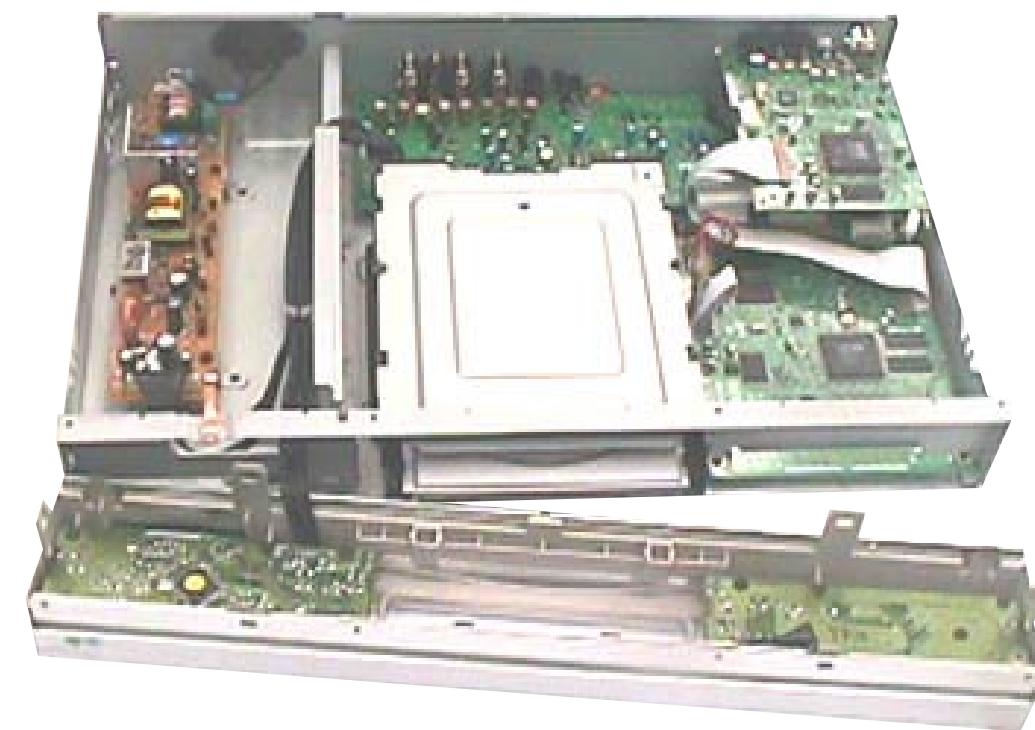
Service position A



Service position B



Service position C



## Diagnostic Software

### 1. Definitions and Abbreviations

#### **Definitions**

Control PC	Automatic test equipment, part of the production control system in the factory, to control the execution of Diagnostic Nuclei in the DVD player.
Diagnostic Nucleus	Part of the Diagnostic Software. Each nucleus contains an atomic and software independent diagnostic test, testing a functional part of the DVD player hardware on component level.
Script	Part of the Diagnostic Software. Each script contains a sequence of Diagnostic Nuclei to be executed.
Service PC	PC used by a service- or repairman to communicate with the Diagnostic Software in the DVD player.

#### **Abbreviations**

BHDS	Basic Hardware Diagnostic Software
BDS	Basic Diagnostic Software
FDS	Full Diagnostic Software
HSI	Hardware Software Interface
OSD	On Screen Display
PWB	Printed Wire Board
RC	Remote Control
ZiVa5	the single-chip DVD backend processor from LSI Logic
SACD	Super Audio CD
SD5.2	Standard Design DVD-SACD Module – 5 <sup>th</sup> generation

### 2. Product Overview

The purpose of the DVD Diagnostic Software is to support the user (factory and service) in diagnosing the DVD player. The context of the diagnostic software consists of:

- Service PC, a personal computer with a terminal emulation program that emulates a simple ASCII terminal. The Service PC is connected to the RS232 port of the DVD player and is used for Level 2 / Second Line diagnostic mode. The Service PC is operated by a service personnel or repairman.
- Local Keyboard, input device of the DVD player hardware, used during power-on and during the execution of the Player Test Script.
- Test Pin, input device of the DVD player hardware, used during power-on.
- Local Display, output devices of the DVD player hardware, used by the Player Test Script and Dealer Test Script to output messages and test results.
- Control PC, a computer system in the factory connected to the RS232 port of the DVD player, used during Level 1 diagnostic mode to communicate with the Diagnostic Software (bi-directional).
- DVD Player Hardware, all the DVD player hardware which is subject to the Diagnostic Software.

The DVD has only one single RS232 port, implying that all interfaces using this port are mutually exclusive. The Full Diagnostic Software (FDS) is aimed to support service and repair and can present menus and receive commands via a Service PC.

The Diagnostic Software contains a large number of tiny Diagnostic Nuclei. Each nucleus contains an atomic and software independent diagnostic test, testing a functional part of the DVD player hardware on component level. Each Diagnostic Nucleus works directly on the DVD player hardware, no drivers will be used. Each Diagnostic Nucleus returns a Result indicating if and which error was detected (e.g. faulty SDRAM chip).

It is possible to combine these diagnostic nuclei in a Script to create a high level test like a player test, a script can be as simple as a sequence of executed Diagnostic Nuclei. The Diagnostic Software contains standard scripts for dealer and player test. Each script returns a Result indicating if and which error was detected (e.g. faulty module).

During power-on the Selector (part of the DVD boot sequence) will check the presence of certain triggers (signal on Test Pin, key combinations on local keyboard). Via the Selector the appropriate diagnostic mode will be activated:

<b>Power-on condition</b>	<b>Activate diagnostic mode: <i>Name used in Factory</i></b>	<b>Name used in Service</b>	<b>Short Description</b>
OPEN & PLAY key pressed	(Dealer Script)	Dealer test	Fully automatic stand-alone test without any user intervention resulting in one overall result: passed or not passed. No SDRAM tests are performed prior to DSW startup.
OPEN & STOP key pressed	Level 3 or Player test (Player Script)	First Line	Automatic test sequence with user intervention and use of screen/audio test device (i.e. a television set). The result will be an indication of which board is faulty. No SDRAM tests are performed prior to DSW startup.
Test pin active & 'C' character received from RS232 Port	Level 1 or Module test in production line	-	Externally (Control PC) controlled test sequence via a simple command line interface. Each command activates one Diagnostic Nucleus. SDRAM tests are performed prior to DSW startup.
Test pin active & 'M' character received from RS232 Port	Level 2 or Module test for repair	Second Line	Externally (Service PC) controlled test sequence via a simple menu driven interface. Each menu selection activates a Diagnostic Nucleus or a Standard Script. SDRAM tests are performed prior to DSW startup.

Note: Some players do not have a power-on key, in such cases, the player must be powered-on by connecting the power-cable.

The Selector, Command Handler and Menu Handler are the heart of the Diagnostic Software and will be referenced as the Diagnostic Engine in the context of this document. They form a framework in which Diagnostic Nuclei and standard Scripts can be added via a well-defined software interface. The actual contents of the Menu Handler are part of the FDS.

### **3. Product Functions**

#### ***Dealer diagnostics (For I2C Master Modules only)***

The dealer diagnostic mode can be activated by pushing the script activation keys during power-on. The script activation keys vary depending on the model used.

This test consists of running a predefined script (Dealer Test Script). The script controls the execution of a number of diagnostic tests. No external input is required during the execution of the script. During the execution a progress indicator will be displayed on the local display. At the end of the script one message is returned indicating if a failure in the DVD player is detected or not. The message will be displayed on the local display. No external test equipment is required.

<u>DVD Project/Model</u>	<u>Dealer Script mode activation key</u>
DVP900 – All series	Press and hold OPEN and PLAY key

#### ***First Line & Level 3 diagnostics (For I2C Master Modules only)***

The First Line & Level 3 diagnostic mode can be activated by pushing the script activation keys during power-on. The script activation keys vary depending on the model used.

This test consists of running a predefined script (Player Test Script). The script controls the execution of a number of diagnostic nuclei. The first group of Diagnostic Nuclei in this script will check the local display and the local keyboard and will output the result on the local display before it continues with the remaining Diagnostic Nuclei of the script. The Diagnostic Nuclei for the readout of the error log and the Basic Engine will also be part of the predefined script. During execution of the script input from the local keyboard is required for confirmation of

certain steps. At the end of the script a message is returned indicating if a failure in one of the modules (Basic Engine, Digital PWB or Display PWB) is detected or not. The message will be displayed on the local display. The test will be done with a closed player and requires equipment for testing audio and video (e.g. a television set).

<u>DVD Project/Model</u>	<u>Player Script mode activation key</u>
DVP900– All series	Press and hold OPEN and STOP key

### ***Level 1 diagnostics***

The Level 1 diagnostic mode can be activated by making the test pin active (pull to ‘L’) during power-on and subsequently, sending a ‘C’ character to the RS232 port. The execution of Diagnostic Nuclei is under external control via a simple command line interface. There is no standard Level 1 Script embedded in the Diagnostic software. Each command activates one Diagnostic Nucleus. In case a Diagnostic Nucleus detects a failure it will try to give an indication via Result which component(s) or connection(s) is (are) failing. Result consists of an error number and an error string. These will be sent to the Control PC. The error string contains the textual equivalent of the error number extended with additional detailed error information. The required external test equipment will be part of the Control PC.

### ***Second Line & Level 2 diagnostics***

The Second Line & Level 2 diagnostic mode will be activated by making the test pin active during power-on and sending a ‘M’ character to the RS232 port. The execution of Diagnostic Nuclei is under external control via the terminal program on the Service PC with a simple menu driven interface. In case a Diagnostic Nucleus detects a failure it will try to give an indication via Result of which component(s) or connection(s) is(are) failing. Result consists of an error number and an error string. These will be sent to the service PC. The error string contains the textual equivalent of the error number extended with additional detailed error information. The required external test equipment will be part of the standard repair suite.

## **4. Functional Requirements**

### ***Diagnostic Engine***

The diagnostic Engine consists of the following objects:

- Selector (partly implemented in the DVD boot sequence)
- Command Handler
- Menu Handler

The actual contents of the Menu Handler are part of the FDS.

### ***Scripts***

Each script controls the execution of Diagnostic Nuclei. There is no direct interaction from the script with the hardware. A script can be a sequence of Diagnostic Nuclei and can also contain branches and loops. Results of the execution of a Script are given back to the user. The Scripts will be embedded in the ROM of the DVD player and will be written in C-code as part of sources of the Diagnostic Software (there is no script interpreter).

The following script objects are defined for the Basic Diagnostic Software:

- Dealer Test Script
- Player Test Script

### ***Diagnostic Nuclei***

Each nucleus contains an atomic and independent diagnostic test, testing a functional part of the DVD player hardware on component level. Each Nucleus returns a result message to its caller. Some tests (e.g. generating a color bar) can only return an “OK” result. Internal communication will be done via a uniform interface between the diagnostic Engine, Scripts and the Diagnostic Nuclei.

The diagnostic Engine can only operate if a certain (minimal) set of hardware is functioning properly. To test this set of hardware, a set of basic diagnostic nuclei is embedded in the DVD player. Each basic diagnostic nucleus will only test that part of the hardware which is required for execution of the diagnostic Engine, e.g. a RAM test will only test that part of RAM that is used by the diagnostic engine. After the Diagnostic Engine is operational it is possible to do a full RAM diagnostic. All basic diagnostic nuclei start with prefix ‘Basic’.

In the overview each Diagnostic Nucleus consists of a reference number, a reference name and remarks. Reference number and name are coupled and one of them is enough for unique identification.

Since this document caters to more than one SD module, and not all modules support the same set of test nuclei, a note is placed in the remarks of each test to indicate whether this applies to a specific module or not. Otherwise it is assumed that the test is supported by all platforms.

### **Basic Diagnostic Nuclei**

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
1	BasicSpAcc	Serial port Access test/initialization
2	CompProclnfo	Display processor specific info (LOR register, Chip Revision)

### **Processor and Peripherals**

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
6	PapChksFl	Calculate and Verify Checksum FLASH
7a	PapUclkAclkCdda	External uClock A_CLK in CD-DA mode (SD5.2 only)
7b	PapUclkAclkDvd	External uClock A_CLK in DVD mode (SD5.2 only)
7c	PapUclkAclkDvd96	External uClock A_CLK in DVD (96kHz) mode (SD5.2 only)
8a	PapSgsAclkCdda	Internal PLL CLK in CD-DA mode
8b	PapSgsAclkDvd	Internal PLL CLK in DVD mode
8c	PapSgsAclkDvd96	Internal PLL CLK in DVD (96kHz) mode
10	PapFlashWrAcc	Tests Backend flash write access function
11	Papi2cNvram	I2C NVRAM access
12	Papi2cDisp	I2C / Sio Bus - Display PWB communications check
13	PapUdeEcho / PapAtapiEcho	UDE Echo (SD5.2) / ATAPI bus echo (SD5.12/SD5.31)
14	PapUdePass	Enter UDE Pass-through mode (SD5.2 only)
15	PapNvramWrR	NVRAM Write Read
62	PapChksSum	Show checksums stored in flash

### **Components**

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
16	CompSdramWrR	SDRAM Write Read

### **Audio**

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
18a	AudioDeemp0On	Audio Deemphasization 0 On
18b	AudioDeemp0Off	Audio Deemphasization 0 Off
18c	AudioDeemp1On	Audio Deemphasization 1 On
18d	AudioDeemp1Off	Audio Deemphasization 1 Off
18e	AudioDeemp0TristateOn	Audio Deemphasization 0 Tristate On
18f	AudioDeemp0TristateOff	Audio Deemphasization 0 Tristate Off
18g	AudioDeemp1TristateOn	Audio Deemphasization 1 Tristate On
18h	AudioDeemp1TristateOff	Audio Deemphasization 1 Tristate Off
19a	AudioMuteOn	Audio Mute On
19b	AudioMuteOff	Audio Mute Off
20a	AudioPinkNoiseOn	Audio Pinknoise On
20b	AudioPinkNoiseOff	Audio Pinknoise (or beep tone) Off
21a	AudioSineOn	Audio Sine signal On/Off
21b	AudioSineBurst	Audio Sine signal Burst

### **Video**

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
22a	InitProgressiveBoard	Initialize Progressive Scan Board
23a	VideoColDencOnPAL	Colourbar (PAL) DENC On
23b	VideoColDencOff	Colourbar DENC/Digital Video Off
23c	VideoColDencOnNTSC	Colourbar (NTSC) DENC On
23d	AudioVideoHDMIon	Audio Video HDMI On
23e	AudioVideoHDMIoff	Audio Video HDMI Off
24a	VideoProgMPEGon	Progressive - Digital Video Colour bar ON (SD5.2 only)

24b	VideoYuvMPEGon	Enhanced YUV - Digital Video Colour bar ON
25a	VideoScartLo	Scart Low (SD5.2 only)
25b	VideoScartMi	Scart Medium (SD5.2 only)
25c	VideoScartHi	Scart High (SD5.2 only)
54	VideoScartSwComm	Scart Switch communication (SD5.2 only)
55a	VideoScartSwDvd	Scart Switch Dvd (SD5.2 only)
55b	VideoScartSwPass	Scart Switch Pass-through (SD5.2 only)
57a	VideoScartPinLo	PIO-pins used for Scart-switching
57b	VideoScartPinMi	PIO-pins used for Scart-switching
57c	VideoScartPinHi	PIO-pins used for Scart-switching
61a	VideoColOutRGB	Enable Video Output to RGB
61b	VideoColOutYUV	Enable Video Output to YUV

***DisplayPWB (slave processor)***

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
26	DispVer	Version number (SD5.2 only)
27	DispKeyb	Keyboard (SD5.2 only)
28	DispRc	Remote Control (SD5.2 only)
29	DispLed	LEDs (SD5.2 only)
30a	DispDisplay	VFT Display test (SD5.2 only)
30b	DispLCDDisplay	LCD display test (SD5.2 only)
30c	DispLCDBkLight	LCD Backlight test (SD5.2 only)
60	DispP50	P50 loopback test (SD5.2 only)

***Log (error logging in NVRAM)***

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
31	LogReadErr	Read last Errors
32	LogReadBits	Read errors Bits
33	LogReset	Reset

***Miscellaneous***

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
34	MiscReadConfig	Read Configuration area from NVRAM
35	MiscNvramReset	NVRAM Reset
36	MiscNvramMod	Modify NVRAM contents
46	MiscApplVer	Read version of application software
47a	MiscTrayOpenNr	Read the number of times the tray opened
47b	MiscPowerOnTime	Read the total time the player's power has been on
47c	MiscPlayTimeCddaVcd	Read the Playtime of CDDA and VCD discs
47d	MiscPlayTimeDvd	Read the Playtime of DVD discs

***Basic Engine***

<u>Ref. #</u>	<u>Reference Name</u>	<u>Remark</u>
37	BeVer	Version number
38a	BeFocusOn	Focus On
38b	BeFocusOff	Focus Off
39a	BeDiscmotorOn	Discmotor On
39b	BeDiscmotorOff	Discmotor Off
40a	BeRadialOn	Radial control On
40b	BeRadialOff	Radial control Off
41a	BeSledgeIn	Sledge Inwards
41b	BeSledgeOut	Sledge Outwards
42a	BeGroovesIn	jump Grooves to Inside
42b	BeGroovesMid	jump Grooves to Middle
42c	BeGroovesOut	jump Grooves to Outside
43a	BeTrayIn	Tray In
43b	BeTrayOut	Tray Out
44	BeReset	Reset Basic Engine
58a	LaserCdOn	CD Laser on

58b	LaserCdOff	CD Laser off
58c	LaserDvdOn	DVD Laser on
58d	LaserDvdOff	DVD Laser off

**Furore IC**

Ref. #	Reference Name	Remark
62	Furore_SdramWrR	Furore SDRAM Write Read test (SD5.2 only)
63	Furore_SdramWrRFast	Furore SDRAM interconnection test (SD5.2 only)
64	Furore_Id	Furore version ID check (SD5.2 only)
83	Furore_Reset	Furore reset (SD5.2 only)
84a	Furore_High	Sets Furore output pins DSD_PCM0-9 to high (SD5.2 only)
84b	Furore_Low	Sets Furore output pins DSD_PCM0-9 to low (SD5.2 only)

**Audio DAC related**

Ref. #	Reference Name	Remark
65	DAC_I2C	Resets DAC and check I2C communication with DAC (SD5.2 only)
66a	DAC_I2CEnable	Enable I2C communication to AV board (SD5.2 only)
66b	DAC_I2CDisable	Disable I2C communication to AV board (SD5.2 only)
67a	DAC_ClockInternal	Uses internal clock from monoboard for DAC (256fs) (SD5.2 only)
67b	DAC_ClockExternal	Uses external clock for DAC (384fs) (SD5.2 only)
68a	DAC_AudioPreMuteOn	Enable Audio Pre-mute pin (SD5.2 only)
68b	DAC_AudioPreMuteOff	Disable Audio Pre-mute pin (SD5.2 only)
69a	DAC_CenterOn	Enable Center on pin (SD5.2 only)
69b	DAC_CenterOff	Disable Center on pin (SD5.2 only)
79	DAC_Reset	Resets DAC (SD5.2 only)
80a	DAC_ModeCDDA	Sets DAC to CDDA mode (SD5.2 only)
80b	DAC_ModeDVD48	Sets DAC to DVD mode (48kHz) (SD5.2 only)
80c	DAC_ModeDVD96	Sets DAC to DVD mode (96kHz) (SD5.2 only)
80d	DAC_ModeDSD	Sets DAC to DSD mode (SD5.2 only)
81a	DAC_LowPowerStandbyOn	Enable Low Power Standby (SD5.2 only)
81b	DAC_LowPowerStandbyOff	Disable Low Power Standby (SD5.2 only)
82a	DAC_UpsamplingFreq192k	Sets Upsampling frequency to 192kHz (SD5.2 only)
82b	DAC_UpsamplingFreq96k	Sets Upsampling frequency to 96kHz (SD5.2 only)
82c	DAC_UpsamplingOn	Enable upsampling (SD5.2 only)
82d	DAC_UpsamplingOff	Disable upsampling (SD5.2 only)

**Basic Engine - Special Diagnostics**

Ref. #	Reference Name	Remark
70	BedReadFlashID	Read flash memory manufacturer and device ID
71	BedCalcRomChecksum	Calculate ROM checksum

**RS232**

Ref. #	Reference Name	Remark
93a	Rs232cl2cCheck	RS232 I2C communication check
93b	Rs232TxRxCheck	RS232 transmit receive check
93c	Rs232cCtsRtsCheck	RS232 Cts Rts check
93d	Rs232clrPortCheck	RS232 port check
93e	Rs232cStbyCtrlCheck	RS232 standby control check

**5. Description of Diagnostic Engine****Selector**

During initialization of the Diagnostic Software the Selector will first execute the required Basic Diagnostic Nuclei (nuclei of which the names start with the word "Basic") to ensure correct operation. In case one of the basic diagnostics fails an error code and message will be communicated to the outside world via the RS232. In case all basic diagnostics are passed successfully, the selector will activate the appropriate diagnostic mode.

### **Command Handler**

The Command Handler handles all commands from the Control PC. Commands will be received as ASCII strings via the RS232 port. All commands received are interpreted by the Command Handler and only correct commands result in the execution of the matching Diagnostic Nucleus. Illegal commands result in an error message to the Control PC.

### **Menu Handler**

The Menu Handler (part of the Full Diagnostic Software) handles all menus and selections to and from the Service PC. The interface consists of simple text menus with sub-menus. A menu selection results in the execution of the matching Diagnostic Nucleus. Only valid menu selections will be accepted.

## **6. Description of Script Objects**

### ***Dealer test (for I2C Master Modules Only)***

The Dealer Test Script object performs the Dealer test by executing a few Diagnostic Nucleus which do not need any user intervention and are meaningful on a stand alone DVD player. During the execution of the Dealer test a progress indicator will be displayed on the local display.

In general the Diagnostic Nuclei with a large test scope (i.e. testing many components at once) will be executed first.

When a Diagnostic Nucleus detects a fault a message indicating that the test is not passed will be displayed on the local display [ ERROR ] and the Dealer test will terminate. If all Diagnostic Nuclei are passed successfully a message indicating that the test is passed [ PASS ] will be displayed on the local display and the Dealer test will terminate. The only way to continue after termination is via a power down of the DVD player.

### ***Player test (for I2C Master Modules Only)***

The Player Test Script object performs the Player test by executing all available Diagnostic nuclei which are meaningful on a DVD player which is connected to audio/video test equipment (e.g. a television set). Some Diagnostic Nuclei require user intervention.

The first phase will check the following modules:

- Display PWB (communication, display, backlight, LEDs, keyboard, RC, P50)
- Basic Engine (communication, mechanical)
- Digital PWB (all components)

For each module all the available Diagnostic Nuclei which are meaningful for that module will be executed. Some Diagnostic Nuclei require user intervention. After all tests have been executed an indication will be given on the local display which module is most likely to be faulty. Also, the error code of the last detected error will be displayed. To continue, push the PLAY key on the local keyboard, the second phase will be entered automatically. The test of the module Display PWB consists of the Diagnostic Nuclei PapI2cDisp, DispDisplay, DispLCDDisplay, DispLCDBkLight, DispLed, DispKeyb and DispRc and can be aborted at any moment by switching the power off.

The second phase is the read out of the error log and error bits from the NVRAM. Only a part of the information will be displayed on the local display at the time. With the keys: STOP key and OPEN/CLOSE (EJECT) key, it will be possible to walk through the whole error log and bits in a controlled way. The loop phase can be entered by pushing the PLAY key on the local keyboard.

The loop phase will check the following modules:

- Display PWB (communication)
- Basic Engine (communication)
- Digital PWB (all components)

For each module all the available Diagnostic Nuclei which are meaningful for that module will be executed. None of the Diagnostic Nuclei require any user intervention. After all tests have been executed an indication will be given on the local display which module is most likely to be faulty. Also, the error code of the last detected error will be displayed. A loop counter on the local display will be incremented and the loop phase will start all over again.

## **7. Description of Diagnostic Nucleus Objects**

### ***BasicSpAcc***

This is a Basic Diagnostic Nucleus required for correct operation of the Diagnostic Software.

Checks and initializes RS232 port and outputs a message that the Diagnostic software has been started.

### ***CompProInfo***

Reads and display processor specific information. This platform specific function allows the user to read certain registers in the chip at runtime that would be helpful in getting info on the current chip used. For ZiVA5, the Latched-On-Reset register contents and the Chip Revision register contents are read and displayed.

### ***PapChksFlash***

Calculates and verifies the checksum of the backend FLASH or ROM. This includes the entire binary ROM image checksum.

### ***PapUclkAclkCdda, PapUclkAclkDvd, PapUclkAclkDvd96 (for SD5.2 only)***

Diagnostic Nucleus PapUclkAclkCdda switches clock A\_CLK from the microclock to 16.93 MHz(CD\_DA).

Diagnostic Nucleus PapUclkAclkDvd switches clock A\_CLK from the microclock to 18.43 MHz(DVD).

Diagnostic Nucleus PapUclkAclkDvd96 switches clock A\_CLK from the microclock to 36.86 MHz(DVD).

Correct operation cannot be detected by the Diagnostic Software but must be checked with external equipment (e.g. a frequency counter).

*Note:* This nucleus will only work if the external clock is available on board.

### ***PapSgsAclkCdda, PapSgsAclkDvd, PapSgsAclkDvd96***

Diagnostic Nucleus PapSgsAclkCdda switches the PCM clock to 16.93MHz(CD\_DA).

Diagnostic Nucleus PapSgsAclkDvd switches the PCM clock to 18.43 MHz(DVD).

Diagnostic Nucleus PapSgsAclkDvd96 switches the PCM clock to 36.86 MHz(DVD).

Correct operation cannot be detected by the Diagnostic Software but must be checked with external equipment (e.g. a frequency counter).

### ***PapFlashWrAcc***

Reads out the Flash ROM's Chip Identification Number. This nucleus tests the write access by writing a defined sequence of data to the FLASH chip, to be able to read the FLASH ID number.

### ***PapI2cNvram***

Checks the I2C interface with the NVRAM by reading from an arbitrary address.

### ***PapI2cDisp (for SD5.2 only)***

Checks the I2C or SIO bus interface with the slave processor on the display PWB with the echo command.

### ***PapUDEEcho (SD5.2) / PapAtapiEcho (SD5.12/SD5.31)***

Checks the UDE / ATAPI interface to the basic engine with an 'echo' command. This tests the hardware communications interface between the loader and the backend processor.

### ***PapUDEPass (for SD5.2 only)***

Switches the RS232 port and the UDE port in pass-through mode. The only way to exit this nucleus is via a power off of the DVD player.

### ***PapNvramWrR***

Non destructive pattern test (checker board) of **all** locations in the NVRAM. This Nucleus can detect stuck at faults and permanent coupling errors in the NVRAM chip.

### ***VidPortOutAA, VidPortOut55 (for SD5.2 only)***

Writes the value 0xAA to the Digital Video Interface port of the ZiVa5 ITU-656 ports.

Writes the value 0x55 to the Digital Video Interface port of the ZiVa5 ITU-656 ports.

***AudioMuteOn, AudioMuteOff***

Diagnostic Nucleus AudioMuteOn switches the audio mute on.

Diagnostic Nucleus AudioMuteOff switches the audio mute off.

Correct operation cannot be detected by the Diagnostic Software but must be checked externally.

Note: The audio will be muted by the DAC.

***AudioPinkNoiseOn, AudioPinkNoiseOff***

Diagnostic Nucleus AudioPinkNoiseOn will generate a pink noise internally on the ZiVa5 chip, which will be passed to the DAC where it is converted to an analogue audio signal.

Diagnostic AudioPinkNoiseOff will switch off the pink noise generated with AudioPinkNoiseOn.

The signal will also be passed to the digital outputs. For a 6 channel player the signal must be available on all 6 analogue audio channels.

Correct operation cannot be detected by the Diagnostic Software but must be checked externally.

***AudioSineOn, AudioSineBurst***

Nucleus AudioSineOn will generate a sine signal of 1 kHz on the analogue output. The sine signal digital data are stored in the audio buffer in the SDRAM, passed through the audio decoder and without any further processing to the audio DAC where it is converted to an analogue audio signal.

The signal will also be passed to the digital outputs.

Correct operation cannot be detected by the Diagnostic Software but must be checked externally. The sine signal can be stopped by pressing the STOP key on the DVD player or by sending a '#' character via hyperterminal.

Nucleus AudioSineBurst will generate a sine signal of 1 kHz on the analogue output that lasts for about four seconds. The generation of the signal is the same as in AudioSineOn.

Correct operation cannot be detected by the Diagnostic Software but must be checked externally.

Note: In the player script the test AudioSineOn will be used.

***VideoColDencOnPAL, VideoColDencOff, VideoColDencOnNTSC***

Diagnostic Nucleus VideoColDencOnPAL enables colour bar (PAL format) in the DENC in the ZiVa5 chip.

Diagnostic Nucleus VideoColDencOff disables colour bar in the DENC and the digital video outputs in the ZiVa5 chip.

Diagnostic Nucleus VideoColDencOnNTSC enables colour bar (NTSC format) in the DENC in the ZiVa5 chip.

The signal must be simultaneously available on the RGB, Y/C and CVBS outputs. The RGB and CVBS signals will be present on the SCART, while the Y/C signal will only be available on the Y/C connector.

Correct operation cannot be detected by the Diagnostic Software but must be checked externally.

***VideoProgMPEGon, VideoYuvMPEGon (for SD5.2 only)***

VideoProgMPEGon outputs a colourbar pattern on the TV using the external progressive board connected to the DVD module.

VideoYuvMPEGon outputs a colourbar pattern on the TV using the external "enhanced-YUV" board connected to the DVD module.

***VideoScartLo, VideoScartMi, VideoScartHi (for SD5.2 only)***

VideoScartLo steers pin16 on the SCART connector with the value Low (0 to 2V)

VideoScartMi steers pin16 on the SCART connector with the value Medium (4.5 to 7V)

VideoScartHi steers pin16 on the SCART connector with the value High (9.5 to 12V).

Correct operation cannot be detected by the Diagnostic Software but must be checked externally.

***DispVer (for SD5.2 only)***

Gets the version number of the slave processor on the Display PWB.

***DispKeyb (for SD5.2 only)***

Checks that all keys will be pressed once (arbitrary sequence). Feed back will be given via the local display for each key that has been pressed.

***DispRc (for SD5.2 only)***

Displays Header, System and Command code for each received RC code on the local display.. The remote control test can be left by pressing the PLAY key on the local keyboard.

***DispLed (for SD5.2 only)***

Lights all local LEDs until the PLAY key on the local keyboard is pressed.

***DispDisplay, DispLCDDisplay, DispLCDBacklight (for SD5.2 only)***

- For VFT-display only: Displays test patterns on the local display until the PLAY key on the local keyboard is pressed. Test patterns can be walked through by pressing the OPEN/CLOSE (EJECT) key (next pattern) .
- For LCD-display only: Displays test patterns on the local display until the PLAY key on the local keyboard is pressed. Test patterns can be walked through by pressing the OPEN/CLOSE (EJECT) key (next pattern) .
- For LCD-display only: Sets the backlight intensity to different levels (LOW – DIM – BRIGHT) until the PLAY key on the local keyboard is pressed. Test patterns can be walked through by pressing the OPEN/CLOSE (EJECT) key (next pattern) .

***LogReadErr***

Read error log from NVRAM and output the data as error numbers only.

***LogReadBits***

Read error bits from NVRAM and output the numbers of the error bits which are set. Each error bit will get a number, starting from 1 (bit 0 in first byte of error bit area) to  $N^*8$  (bit 7 in last byte of error bit area) where N is the number of bytes in the error bit area.

***LogReset***

Reset all error log and bits in the NVRAM.

***MiscReadConfig***

Read configuration area from NVRAM and output as raw data.

***MiscNvramReset***

Reset the whole NVRAM to zero.

***MiscNvramMod***

Read/Modify any location in NVRAM.

***BeVer***

Get version number of the Basic Engine / Loader used.

***BeFocusOn, BeFocusOff***

Diagnostic Nucleus BeFocusOn puts the laser of the BE into focus (focus loop).

Diagnostic Nucleus BeFocusOff switches the focus loop off.

***BeDiscmotorOn, BeDiscmotorOff***

Diagnostic Nuclei BeDiscmotorOn switches the disc motor (=spindle motor) on.

Diagnostic Nuclei BeDiscmotorOff switches the disc motor (=spindle motor) off.

***BeRadialOn, BeRadialOff***

Diagnostic Nuclei BeRadialOn closes the radial loop.

Diagnostic Nuclei BeRadialoff opens the radial loop.

***BeSledgeIn, BeSledgeOut***

Diagnostic Nuclei BeSledgeIn moves the sledge fully inwards.

Diagnostic Nuclei BeSledgeOut moves the sledge fully outwards.

***BeGroovesIn, BeGroovesMid, BeGroovesOut***

Diagnostic Nuclei BeGroovesIn lets the laser spot jump to the inside limit of the disc.

Diagnostic Nuclei BeGroovesMid lets the laser spot jump to the middle of the disc.

Diagnostic Nuclei BeGroovesOut lets the laser spot jump to the outside limit of the disc.

***BeTrayIn, BeTrayOut***

Diagnostic Nucleus BeTrayIn closes the disc tray.

Diagnostic Nucleus BeTrayOut opens the disc tray.

***BeReset***

Resets the Basic Engine.

***MiscAppVer***

Read the version of the application software from a fixed location in the NVRAM.

***MiscTrayOpenNr, MiscPowerOnTime, MiscPlayTimeCddaVcd, MiscPlayTimeDvd***

MiscTrayOpenNr reads the number of times the tray has opened

MiscPowerOnTime reads the total time the player has been powered on

MiscPlayTimeCddaVcd reads the total playtime of CDDA and VCD discs

MiscPlayTimeDvd reads the total playtime of DVD discs

***VideoScartSwComm (for SD5.2 only)***

VideoScartSwComm checks the I2C interface with the scart switch on the Audio/Video board.

***VideoScartSwDvd, VideoScartSwPass (for SD5.2 only)***

VideoScartSwDvd and VideoScartSwPass sets the scart switch IC on the Audio/Video board to give out DVD signal or pass-through the auxiliary Scart input.

***VideoScartPinLo, VideoScartPinMi, VideoScartPinHi***

VideoScartPinLo, VideoScartPinMi and VideoScartPinHi are used to toggle the PIO-pins that were used in DVDrv2A to set the level of the Scart-pin 8. These PIO-pins may have a different meaning.

***LaserCdOn, LaserCdOff, LaserDvdOn, LaserDvdOff***

Diagnostic Nucleus LaserCdOn/LaserDvdOn puts the selected laser on.

Diagnostic Nucleus LaserCdOff/LaserDvdOff switches the laser off.

***DispP50 (for SD5.2 only)***

DispP50 performs a loop-back test on the P50 communication present on the front panel.

***VideoColOutRGB, VideoColOutYUV***

VideoColOutRGB and VideoColOutYUV nuclei configures the DENC on the ZiVa5 chip to output either RGB format or YUV format respectively.

***PapChksSum***

The PapChksSum nucleus displays the value of the binary image checksum stored in the backend flash.

***FURORE\_SdramWrR (for SD5.2 only)***

Checks the SDRAM memory of the FURORE SDRAM. Checking is done with a data bus test, an address bus test and finally a device test.

***FURORE\_SdramWrRFast (for SD5.2 only)***

Checks the lower and upper memory portion of the FURORE SDRAM. Checking is done faster compared to FURORE\_SdramWrRLow, FURORE\_SdramWrRHigh because the device test is skipped. Checking is done with a data bus test, and finally an address bus test.

***FURORE\_Id (for SD5.2 only)***

This nucleus gets the revision ID of the FURORE chip.

**DAC\_I2C, DAC\_I2Cenable, DAC\_I2Cdisable (for SD5.2 only)**

This nucleus checks the interface between the I2C controller on the ZiVa5 and the external DAC board.

**DAC\_ClockInternal, DAC\_ClockExternal (for SD5.2 only)**

These nuclei select the final clock input to the audio DAC.

- Internal clock means the PCM clock from the monoboard is used.
- External clock means the DSD clock on the AV board is used.

**DAC\_AudioPreMuteOn, DAC\_AudioPreMuteOff (for SD5.2 only)**

This nucleus selects Audio Pre-mute on/off.

**DAC\_CentreOn, DAC\_CentreOff (for SD5.2 only)**

This nucleus selects the centre on/off.

**BedReadFlashID**

Loads a special diagnostic firmware on the front engine which reads the Flash device and manufacturer's ID .

**BedCalcRomChkSum**

Loads a special diagnostic firmware on the front engine that calculates the checksum of the front engine ROM.

**BedScratchTest**

Loads a special diagnostic firmware on the front engine that checks the scratch detector circuit of the front engine.

**DAC\_Reset**

This nucleus resets the DAC board.

**DAC\_ModeCDDA, DAC\_ModeDVD48, DAC\_ModeDVD96, DAC\_ModeDSD (for SD5.2 only)**

- DAC\_ModeCDDA switches the DACs into CDDA mode.
- DAC\_ModeDVD48 switches the DACs into DVD 48 kHz mode.
- DAC\_ModeDVD96 switches the DACs into DVD 96 kHz mode.
- DAC\_ModeDSD switches the DACs into DVD DSD mode.

**DAC\_LowPowerStandbyOn, DAC\_LowPowerStandbyOff (for SD5.2 only)**

DAC\_LowPowerStandbyOn switches the Low power standby mode on, removing power supply to SCART switch IC.

DAC\_LowPowerStandbyOff switches the Low power standby mode off, enabling power supply to SCART switch IC.

**DAC\_UpSamplingFreq192k, DAC\_UpSamplingFreq96k, DAC\_UpSamplingOn, DAC\_UpSamplingOff (for SD5.2 only)**

These nuclei select whether upsampling should be activated and at what frequency.

**Furore\_Reset (for SD5.2 only)**

This nucleus resets the Furore IC.

**Furore\_High, Furore\_Low (for SD5.2 only)**

These nucleus sets the output lines DSD\_PCM0-9 to either high or low.

### **Procedure to upgrade software**

---

- 1) Power up the set.
- 2) Open tray by press "OPEN/CLOSE" button on the set or press and hold "STOP" button on the RC.
- 3) Place upgrade disc onto tray and close.
- 4) The set will response and display shows [READING] follow by [UPGRADING].  
*Caution: Do not press any buttons or interrupt the mains supply during the upgrading process, otherwise the set may become defective.*
- 5) The whole process should not take more than 3 minutes.
- 6) Tray will open indicating that the upgrading process is completed. The set display will continue to show [UPGRADING].
- 7) Take out the upgrade disc from the tray and switch off the mains supply.
- 8) Power up the set again. Tray will close and go to standby automatically.  
*Note : If the set does not go to standby automatically, give it sufficient time (about 1 minute) to initialize NVM etc. then press "STANDBY" button to go to standby.*

### **Procedure for check Software version**

---

- 1) Power up the set.
- 2) Open tray by press "OPEN/CLOSE" button on the set or press and hold "STOP" button on the RC.
- 3) Press "DISC MENU" button on the RC.
- 4) The TV screen will shows:

*Copyright(c)  
PHILIPS 2002-03  
SD5.2-9000S-XX*

*04Oct2004  
17:34  
YYYYYYYY  
A1160-129  
C3091-178  
UDE28.0001.07  
FE Ver:08*

**XX = software version number**  
**YYYYYYYY = slash version**

### **Reprogramming of DVD Version Matrix**

---

After repair, the customer setting and region code may be lost. Reprogramming will put the set back in the state in which it has left the factory, ie. with the default setting and the allowed region code.

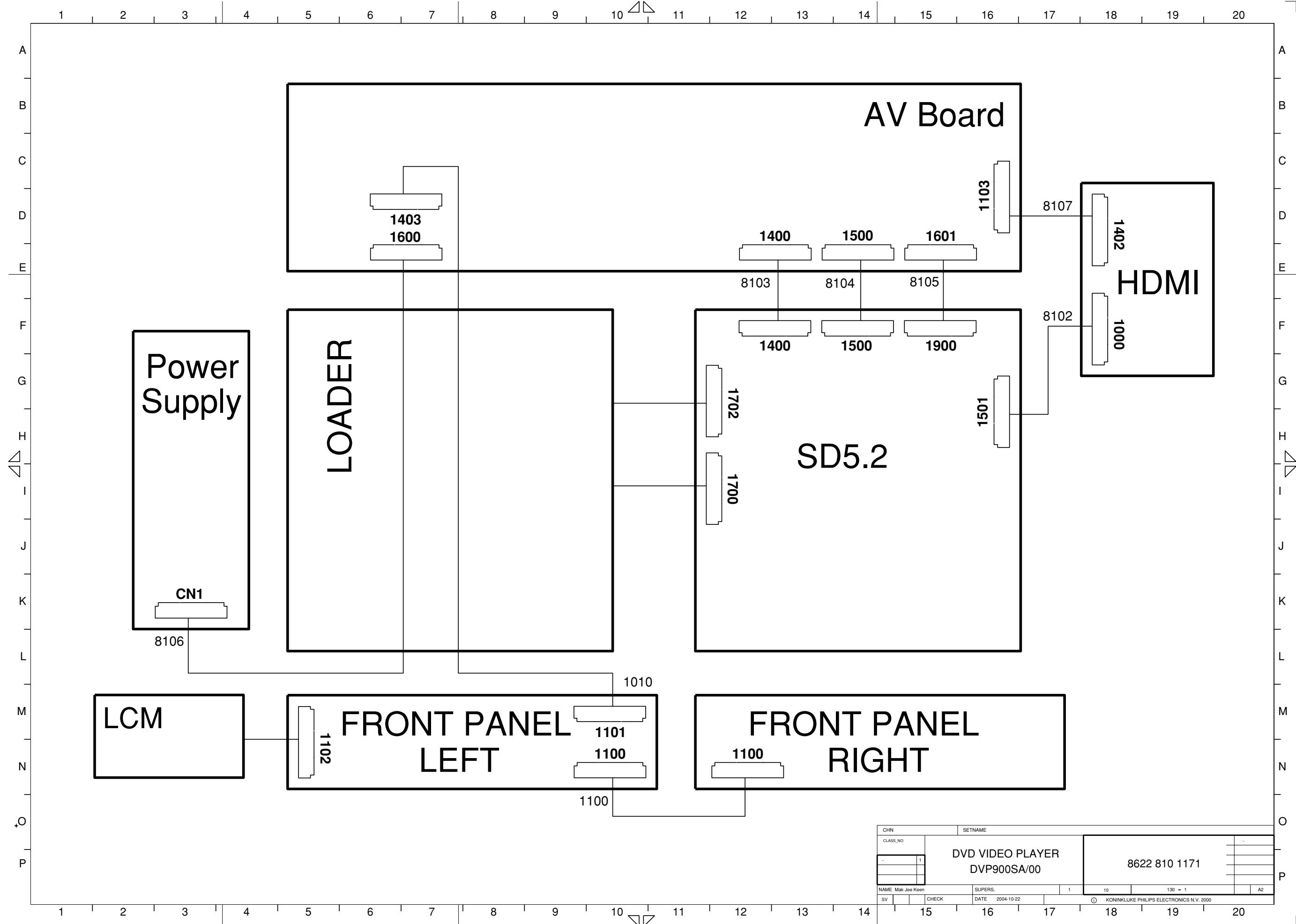
<b>Model</b>	<b>Region</b>	<b>Slash Version</b>
DVP9000S/00	Europe	22201700
DVP9000S/69	AP	32301700

To reprogram do as follows:

- 1) Power up the set.
- 2) Press buttons <PLAY> <1> <5> <9> on the RC.
- 3) The set display shows [ \_ \_ \_ \_ \_ ].
- 4) Press now successively the following keys:  
**<2> <2> <2> <0> <1> <7> <0> <0>** - for DVP9000S/00  
**<3> <2> <3> <0> <1> <7> <0> <0>** - for DVP9000S/69
- 5) Press button <PLAY> again on the RC.
- 6) Wait for the set to reboot by itself (less than 1 minute) to confirm that the set has been reprogrammed.

4-1

4-1

**SET WIRING DIAGRAM**

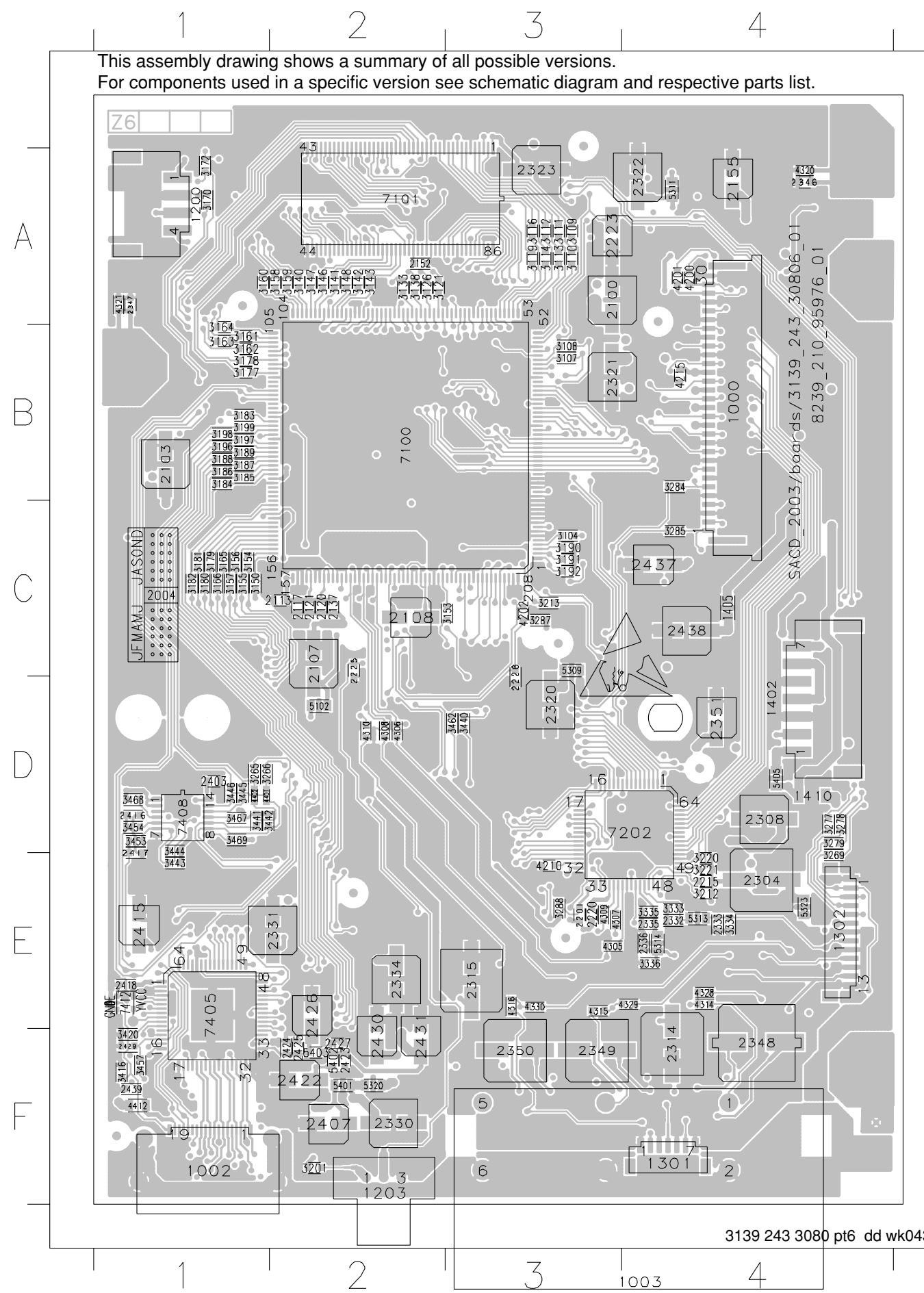
---

# PSCAN HDMI BOARD

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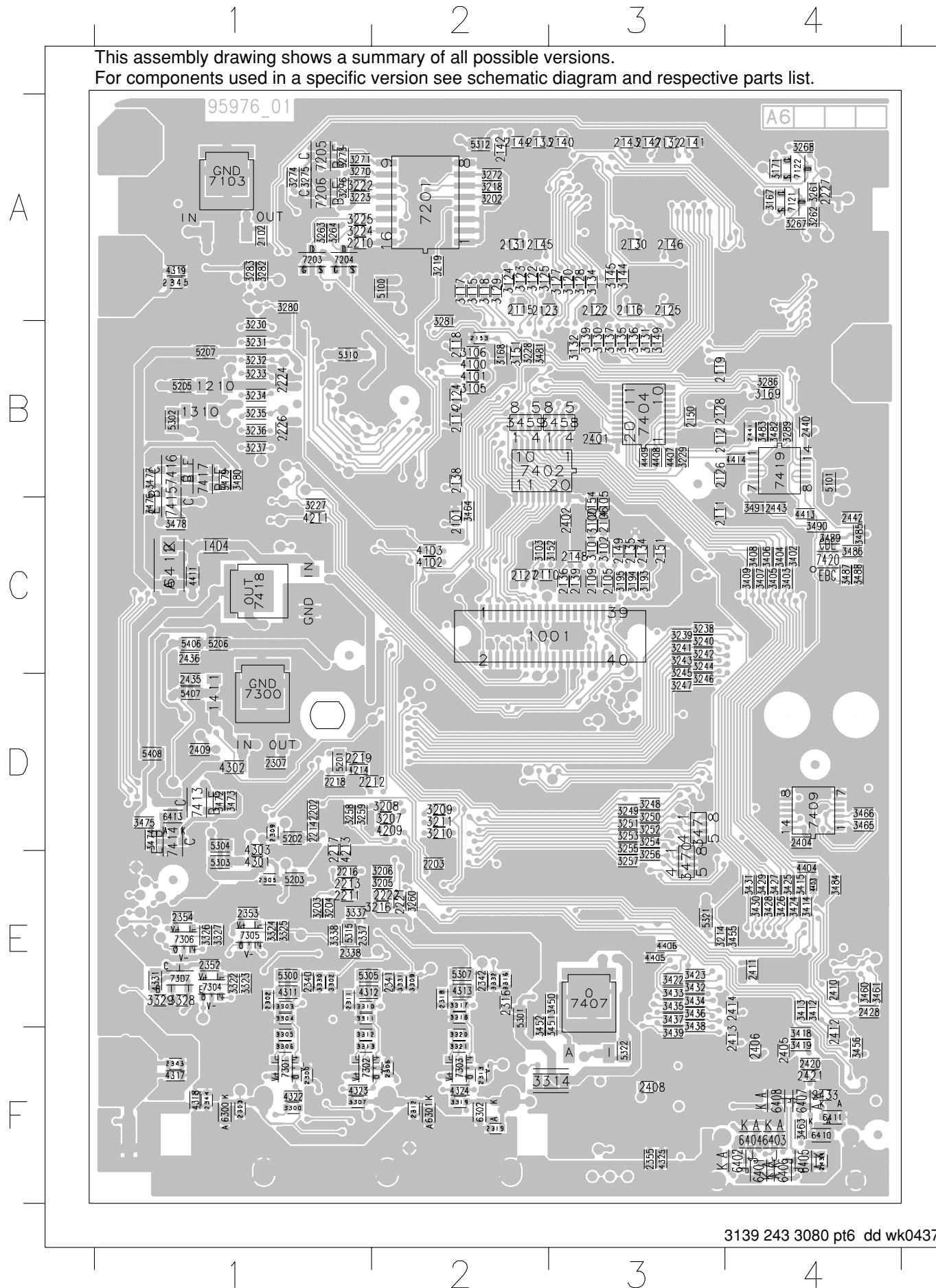
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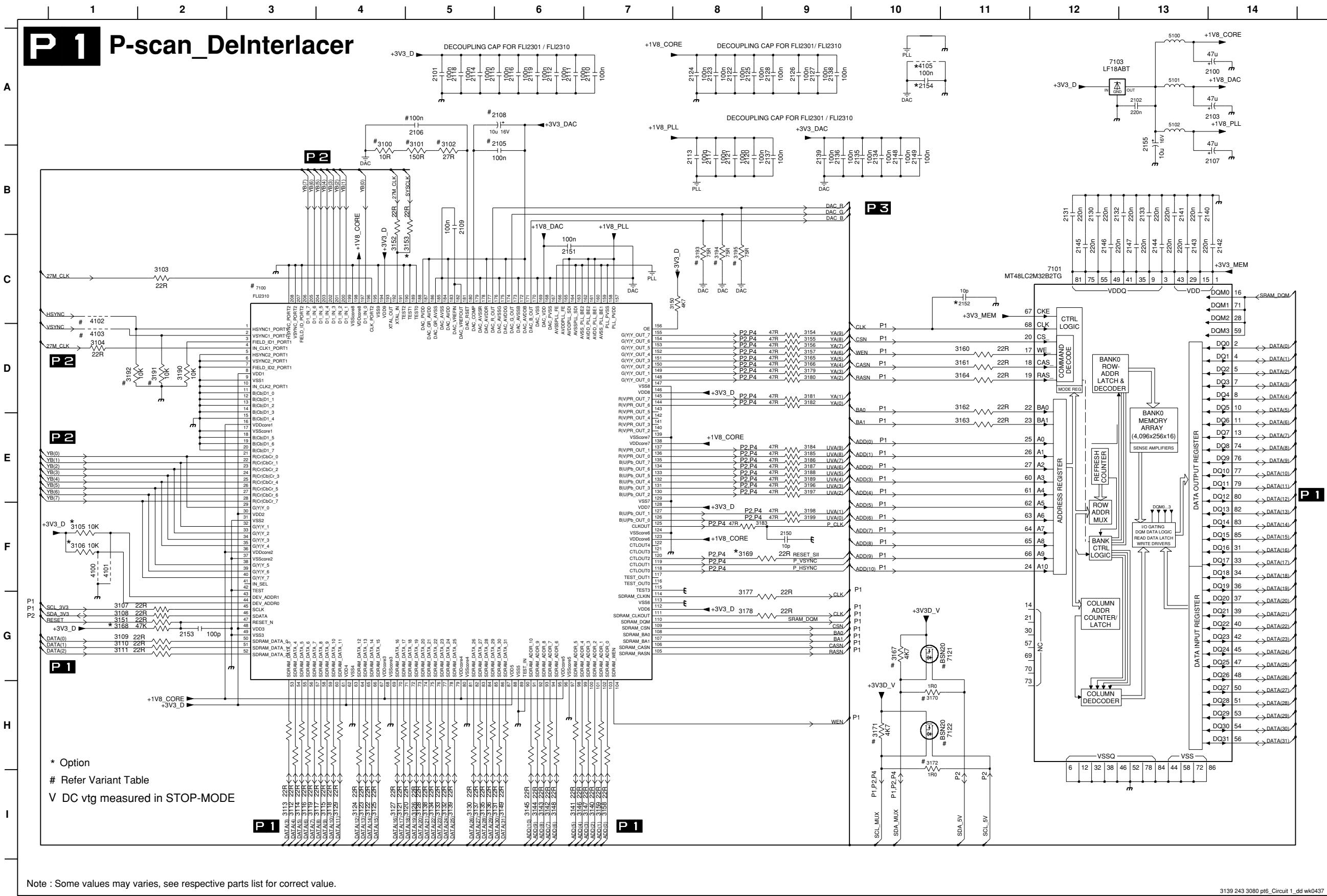
**PSCAN HDMI BOARD - TOP VIEW LAYOUT**


1000	B4	2437	C4	3201	F2	5320	F2
1002	F1	2438	C4	3212	E4	5323	E4
1200	A1	2439	F1	3213	C3	5401	F2
1203	F2	3104	C3	3220	E4	5402	F2
1301	F4	3107	B3	3221	E4	5403	F2
1302	E4	3108	B3	3265	D1	5405	D4
1402	D4	3109	A3	3266	D1	7100	B2
1405	C4	3110	A3	3269	E4	7101	A2
1410	D4	3111	A3	3277	D4	7202	D4
2100	A3	3112	A3	3278	D4	7405	E1
2103	B1	3113	A3	3279	D4	7408	D1
2107	C2	3114	A3	3284	B4	7412	E1
2108	C2	3116	A3	3285	C4		
2113	C2	3119	A3	3287	C3		
2117	C2	3121	A2	3288	E3		
2120	C2	3126	A2	3333	E4		
2121	C2	3133	A2	3334	E4		
2137	C2	3138	A2	3335	E4		
2152	A2	3140	A2	3336	E4		
2155	A4	3141	A2	3416	F1		
2201	E3	3142	A2	3420	F1		
2215	E4	3143	A2	3440	D3		
2220	E3	3146	A2	3441	D1		
2223	A3	3147	A2	3442	D1		
2225	C2	3148	A2	3443	E1		
2228	D3	3150	C1	3444	D1		
2304	E4	3153	C3	3445	D1		
2308	D4	3154	C1	3446	D1		
2314	F4	3155	C1	3453	D1		
2315	E3	3156	C1	3454	D1		
2320	D3	3157	C1	3457	F1		
2321	B3	3158	A2	3462	D3		
2322	A4	3159	A2	3467	D1		
2323	A3	3160	A1	3468	D1		
2330	F2	3161	B1	3469	D1		
2331	E2	3162	B1	4200	A4		
2332	E4	3163	B1	4201	A4		
2333	E4	3164	B1	4202	C3		
2334	E2	3165	C1	4210	E3		
2335	E4	3166	C1	4215	B4		
2336	E4	3170	A1	4305	E3		
2346	A4	3172	A1	4306	D2		
2347	A1	3177	B1	4307	E3		
2348	F4	3178	B1	4308	D2		
2349	F3	3179	C1	4309	E3		
2350	F3	3180	C1	4310	D2		
2351	D4	3181	C1	4314	E4		
2403	D1	3182	C1	4315	E3		
2407	F2	3183	B1	4316	E3		
2415	E1	3184	B1	4320	A4		
2416	D1	3185	B1	4321	A1		
2417	E1	3186	B1	4328	E4		
2418	E1	3187	B1	4329	E4		
2422	F2	3188	B1	4330	E3		
2423	F2	3189	B1	4401	D1		
2424	F2	3190	C3	4402	D1		
2425	F2	3191	C3	4412	F1		
2426	E2	3192	C3	5102	D2		
2427	F2	3196	B1	5309	C3		
2429	F1	3197	B1	5311	A4		
2430	F2	3198	B1	5313	E4		
2431	F2	3199	B1	5314	E4		

## PSCAN HDMI BOARD - BOTTOM VIEW LAYOUT



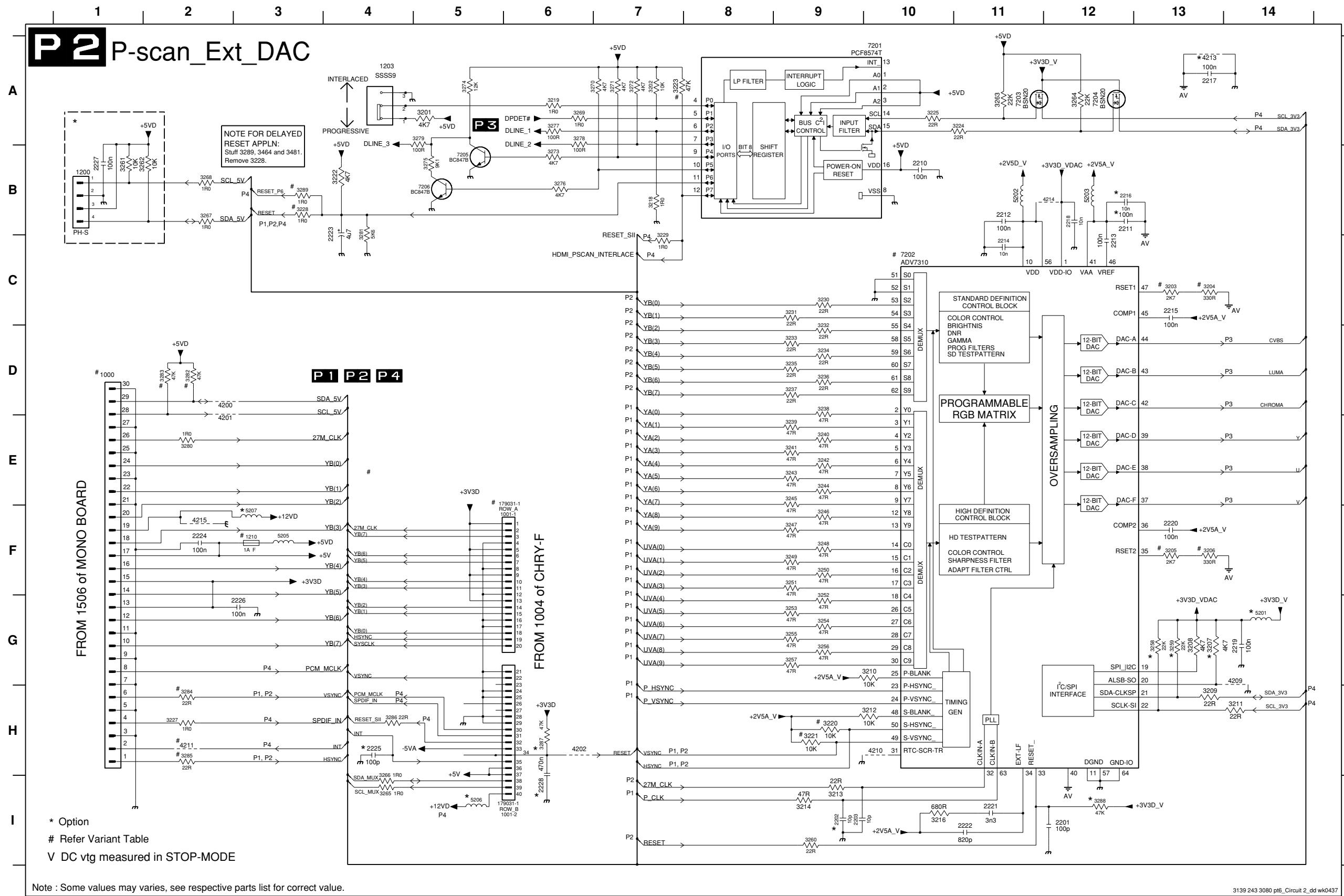
## PSCAN HDMI BOARD - CIRCUIT DIAGRAM (PART 1)



Ref Des	2105	2106	2108	3100	3101	3102	3171	3167	3170	3172	3191	3192	3193	3194	3195	4102	4103	7100	7121	7122	
Variant																					
DVDR755	100n	100n	10u 16V	10R	150R	27R	NOT USED	NOT USED	1R0	1R0	10K	10K	75R	75R	NOT USED	NOT USED	FLI2301	NOT USED	NOT USED		
YDVS1500	NOT USED	4K7	4K7	NOT USED	NOT USED	10K	10K	NOT USED	NOT USED	NOT USED	NOT USED	FLI2310	BSN20	BSN20							
YDVS2500	NOT USED	1R0	1R0	10K	10K	NOT USED	FLI2310	NOT USED	NOT USED												
YDVC950	NOT USED	USED	FLI2310	NOT USED	NOT USED																

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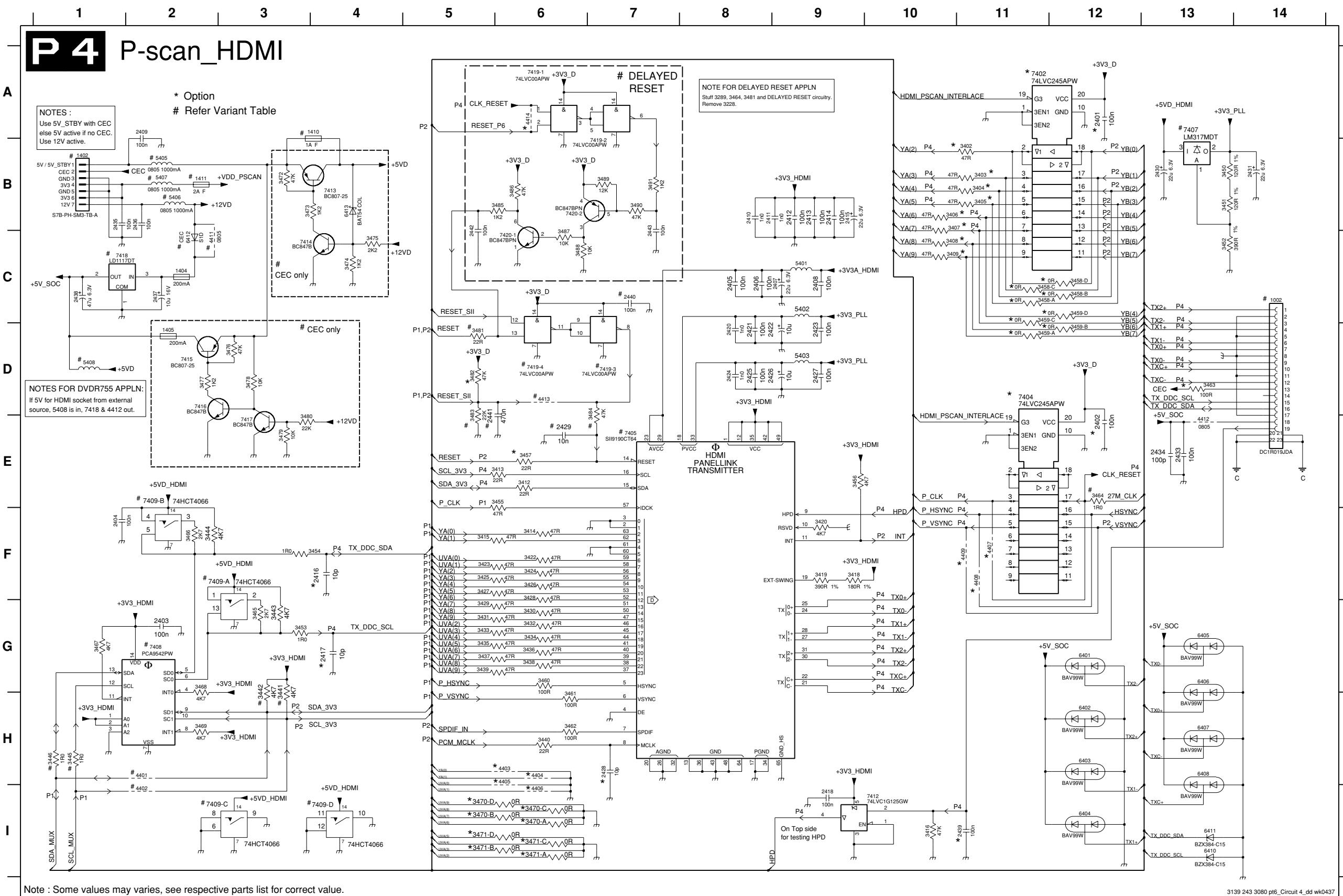
## PSCAN HDMI BOARD - CIRCUIT DIAGRAM (PART 2)



1000 D1	4201 E2
1001-1 F6	4202 H6
1001-2 I6	4209 G14
1200 B1	4210 H10
1203 A4	4211 H2
1210 F3	4213 A13
2201 I2	4214 B12
2202 I9	4215 F2
2203 I9	5201 G14
2210 B10	5202 B11
2211 B12	5203 B12
2212 B11	5205 F3
2213 C12	5206 I5
2214 C11	5214 C11
2215 C13	7201 A10
2216 B12	7202 C10
2217 A13	7203 A11
2218 B12	7204 A12
2219 G14	7205 B5
2220 I3	7206 B5
2221 I1	
2222 I1	
2223 B4	
2224 F2	
2225 H4	
2226 G3	
2227 B1	
2228 I6	
3201 A5	
3202 A7	
3203 C13	
3204 C13	
3205 F13	
3206 F13	
3207 G13	
3208 G13	
3209 H13	
3210 G10	
3211 H14	
3212 H10	
3213 I9	
3216 I10	
3218 B7	
3219 A6	
3220 H9	
3221 H9	
3222 B4	
3223 A7	
3224 A11	
3225 A10	
3227 H2	
3228 B3	
3229 C7	
3230 C9	
3231 C9	
3232 D9	
3233 D9	
3234 D9	
3235 D9	
3236 D9	
3237 D9	
3238 D9	
3239 E9	
3240 E9	
3241 E9	
3242 E9	
3243 E9	
3244 E9	
3245 E9	
3246 F9	
3247 F9	
3248 F9	
3249 F9	
3250 F9	
3251 F9	
3252 G9	
3253 G9	
3254 G9	
3255 G9	
3256 G9	
3257 G9	
3258 G13	
3259 G13	
3260 I9	
3261 B1	
3262 B2	
3263 A11	
3264 A12	
3265 I4	
3266 I4	
3267 B2	
3268 B2	
3269 A6	
3270 A7	
3271 A7	
3272 A7	
3273 B6	
3274 A5	
3275 B5	
3276 B6	
3277 A6	
3278 A6	
3279 A5	
3280 E2	
3281 B4	
3283 D2	
3284 H2	
3285 H2	
3286 H4	
3287 H6	
3288 B3	
4200 D2	



## PSCAN HDMI BOARD - CIRCUIT DIAGRAM (PART 4)



1002 C14	3468 G2
1402 B1	3469 H2
1404 C2	3470-A 16
1405 D2	3470-B 15
1410 A4	3470-C 16
1411 B2	3470-D 15
2401 A12	3471-A 16
2402 E12	3471-B 15
2403 G2	3471-C 16
2404 F1	3471-D 15
2405 C8	3472 B3
2407 C9	3474 C4
2408 C9	3475 C4
2409 A2	3476 D3
2410 B8	3477 D2
2411 B8	3478 D3
2412 B9	3479 E3
2413 B9	3480 E3
2414 B9	3481 D5
2415 B9	3482 D5
2416 F4	3483 E5
2417 G4	3484 D7
2418 I9	3485 B6
2420 D8	3486 B6
2421 D8	3487 C6
2422 D9	3488 C6
2423 D9	3489 B7
2424 D8	3490 B7
2425 D8	3491 B7
2426 D9	3492 D9
2427 D9	3493 H2
2428 H7	3494 H6
2429 E6	3495 H6
2430 B13	3496 H6
2431 B14	3497 F11
2433 E13	3498 F11
2434 E13	3499 F11
2435 B1	3500 F11
2436 B2	3501 H2
2437 C2	3502 E13
2438 C1	3503 D6
2439 H11	3504 A6
2440 C7	3505 C1
2441 E5	3506 C9
2442 C5	3507 D9
2443 C7	3508 B2
2450 B11	3509 B2
3403 B11	3510 B7
3404 B11	3510 D1
3405 B11	3501 G12
3406 B10	3502 H12
3407 B10	3503 H12
3408 C10	3504 H12
3409 C10	3505 H12
3410 C10	3506 G13
3412 E6	3507 H13
3413 E6	3508 H13
3414 F6	3509 H13
3415 F5	3510 F5
3416 H10	3511 I13
3417 E3	3512 I13
3418 F9	3513 C2
3419 F3	3514 B4
3420 F2	3515 A11
3422 F6	3516 D11
3423 F5	3517 E7
3424 F6	3518 A13
3425 F5	3519 G2
3426 F6	3520 A2
3427 F5	3521 B2
3428 B7	3522 C12
3429 G5	3523 D14
3430 G6	3524 H10
3431 G5	3525 B4
3432 G6	3526 C4
3433 G5	3527 D2
3434 G6	3528 D2
3435 G5	3529 E3
3436 G6	3530 C1
3437 G5	3531 A6
3438 G6	3532 B7
3439 G5	3533 D7
3440 H6	3534 A6
3441 G3	3535 C6
3442 G3	3536 C6
3443 G3	3537 B6
3444 F2	3538 A12
3445 H1	3539 B7
3450 B13	3540 C12
3451 B13	3541 D12
3452 C13	3542 E12
3453 G3	3543 F12
3454 F4	3544 G12
3455 E6	3545 E9
3456 E9	3546 A11
3457 E6	3547 B11
3458-A C11	3548-C11
3458-B C12	3549-C12
3458-C C11	3549-A11
3458-D C12	3549-B12
3459-A D11	3549-C11
3459-B D12	3549-D12
3459-C C11	3549-E12
3459-D C12	3549-F12
3460 G6	3549-G12
3461 H6	3549-H12
3462 H6	3549-I12
3463 D13	3549-J12
3464 E12	3549-K12
3465 G3	3549-L12
3466 F2	3549-M12
3467 G1	3549-N12

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TECHNICAL REMARKS

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# FRONT BOARD

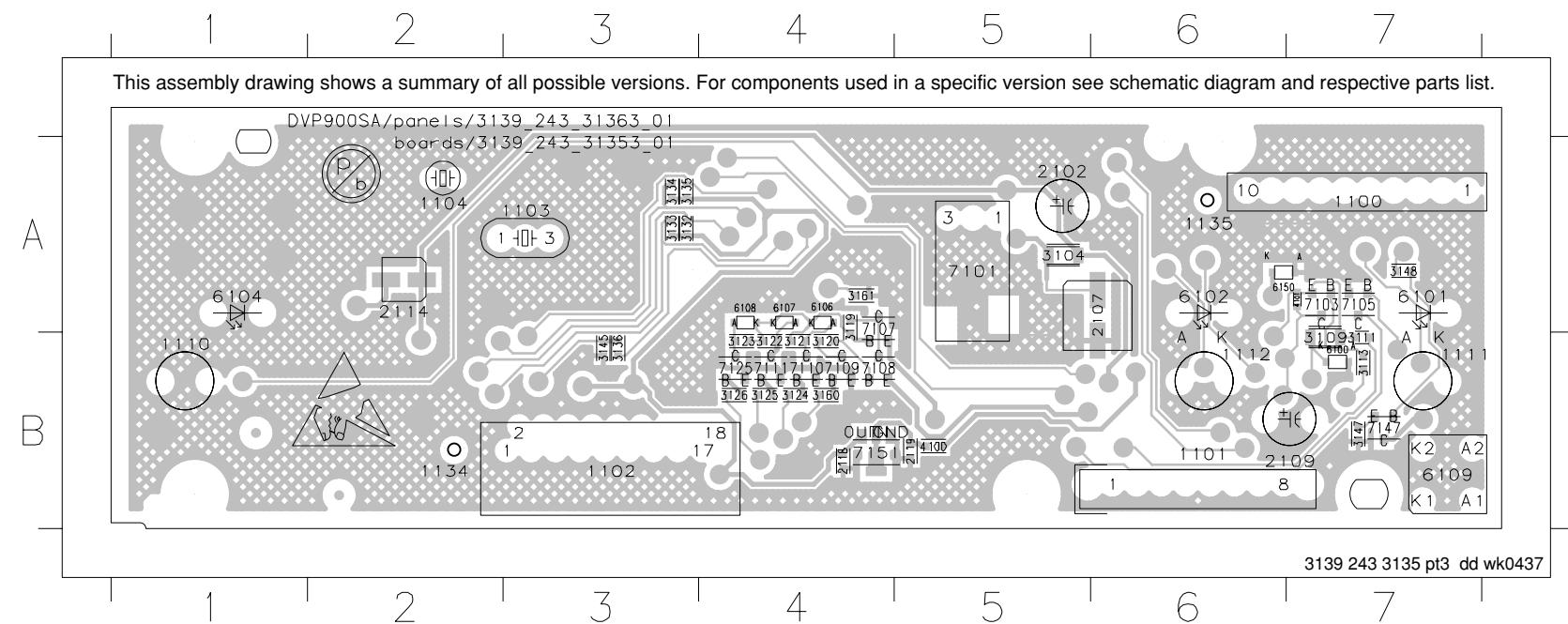
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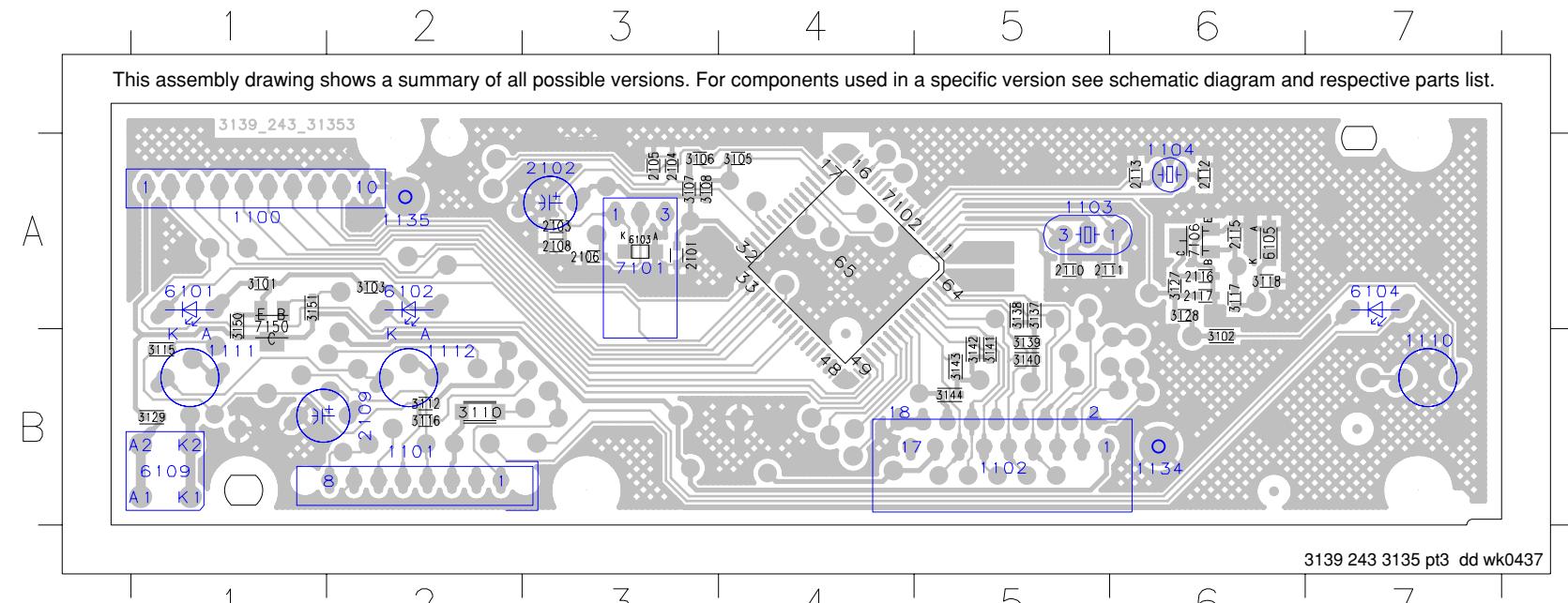
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**FRONT LEFT BOARD - TOP VIEW LAYOUT**

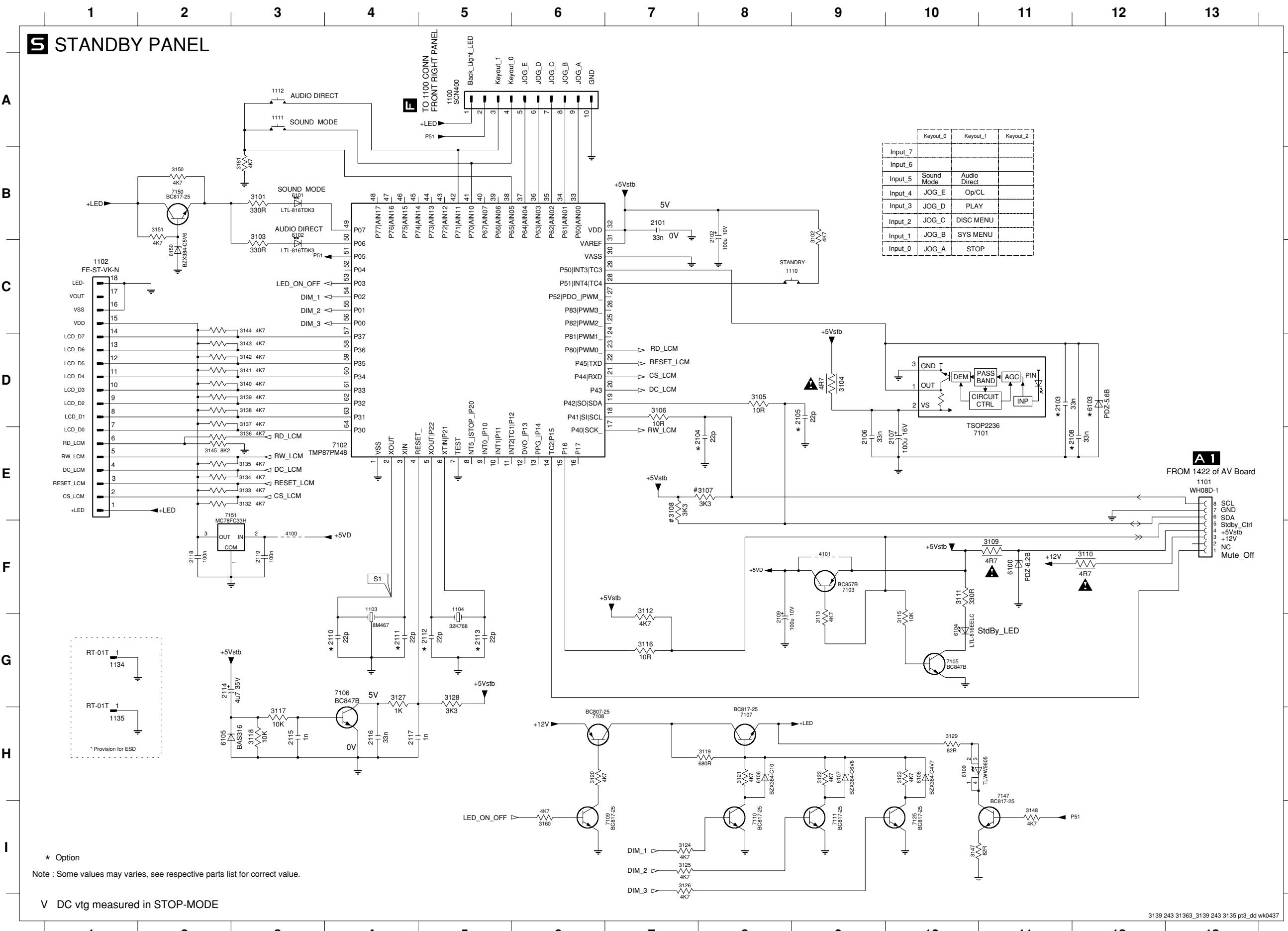
1100 A7	1111 B7	2109 B7	3111 B7	3123 B4	3134 A3	3160 B4	6102 A6	6150 A6	7109 B4
1101 B6	1112 B6	2114 A2	3113 B7	3124 B4	3135 A3	3161 A4	6104 A1	7101 A5	7110 B4
1102 B3	1134 B2	2118 B4	3119 A4	3125 B4	3136 B3	4100 B5	6106 A4	7103 A7	7111 B4
1103 A3	1135 A6	2119 B5	3120 B4	3126 B4	3145 B3	4101 A7	6107 A4	7105 A7	7125 B4
1104 A2	2102 A5	3104 A5	3121 B4	3132 A3	3147 B7	6100 B7	6108 A4	7107 A4	7147 B7
1110 B1	2107 A6	3109 B7	3122 B4	3133 A3	3148 A7	6101 A7	6109 B7	7108 B4	7151 B4

**FRONT LEFT BOARD - BOTTOM VIEW LAYOUT**

1100 A1	1102 B5	1104 A6	1111 B1	1134 B6	2102 A3	6101 A1	6104 A7	7101 A3	
1101 B2	1103 A5	1110 B7	1112 B2	1135 A2	2109 B2	6102 A2	6109 B1		
2101 A3	2106 A3	2112 A6	2117 A6	3105 A4	3110 B2	3117 A6	3129 B1	3140 B5	3144 B5
2103 A3	2108 A3	2113 A6	3101 A1	3106 A3	3112 B2	3118 A6	3137 A5	3141 B5	3150 A1
2104 A3	2110 A5	2115 A6	3102 B6	3107 A3	3115 B1	3127 A6	3138 A5	3142 B5	3151 A1
2105 A3	2111 A5	2116 A6	3103 A2	3108 A3	3116 B2	3128 A6	3139 B5	3143 B5	6103 A3
									6105 A6

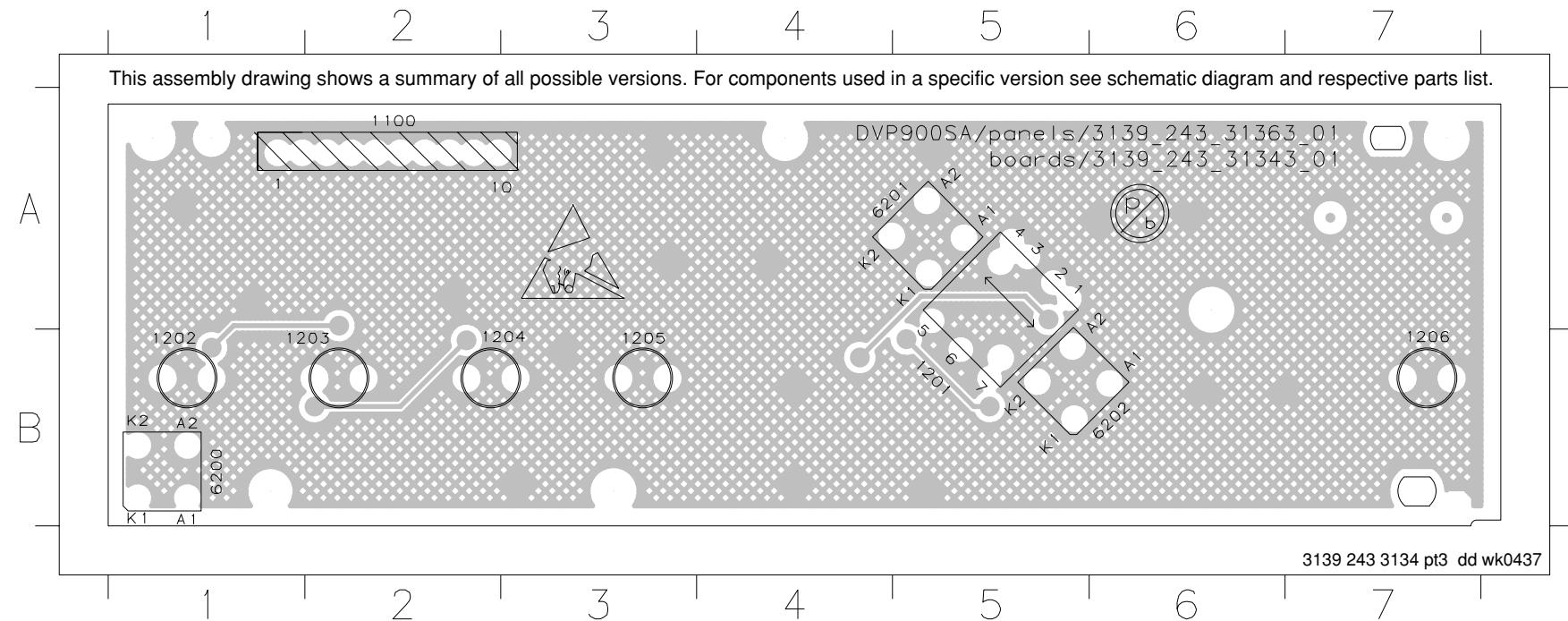


## FRONT LEFT BOARD - CIRCUIT DIAGRAM

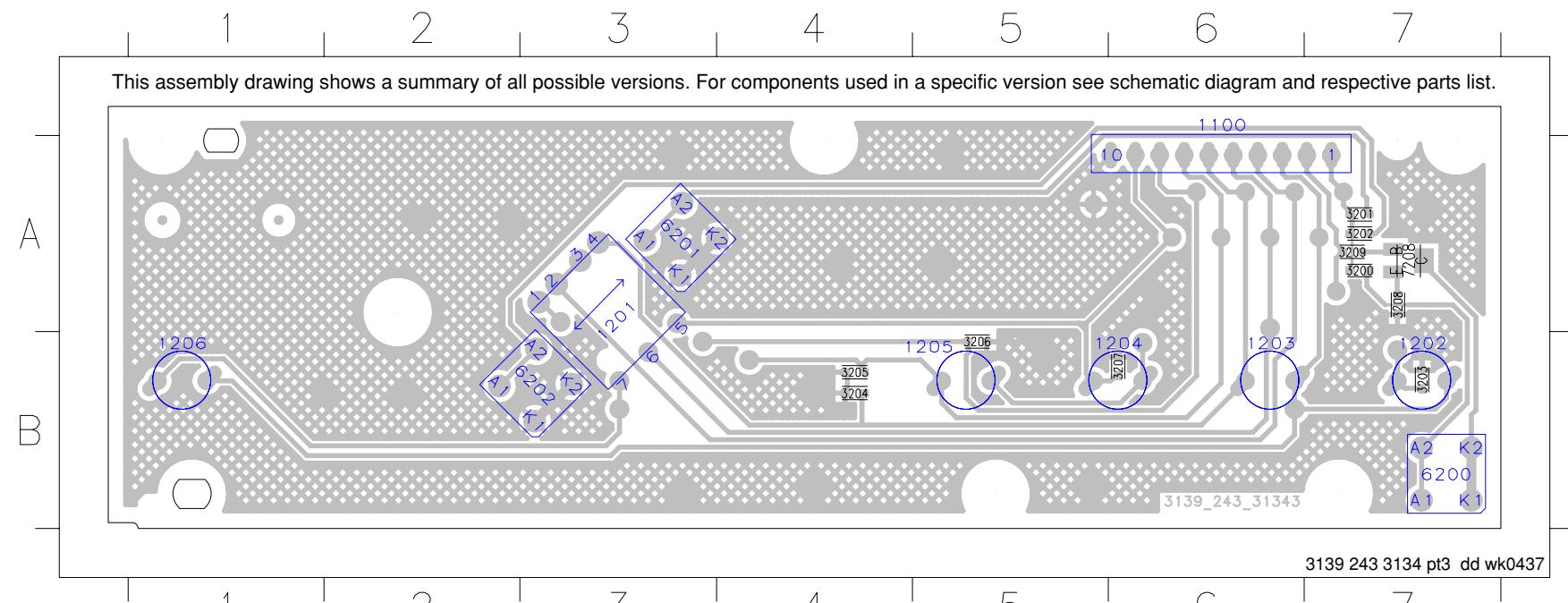


**FRONT RIGHT BOARD - TOP VIEW LAYOUT**

1100 A2 1201 B5 1202 B1 1203 B2 1204 B3 1205 B3 1206 B7 6200 B1 6201 A4 6202 B6

**FRONT RIGHT BOARD - BOTTOM VIEW LAYOUT**

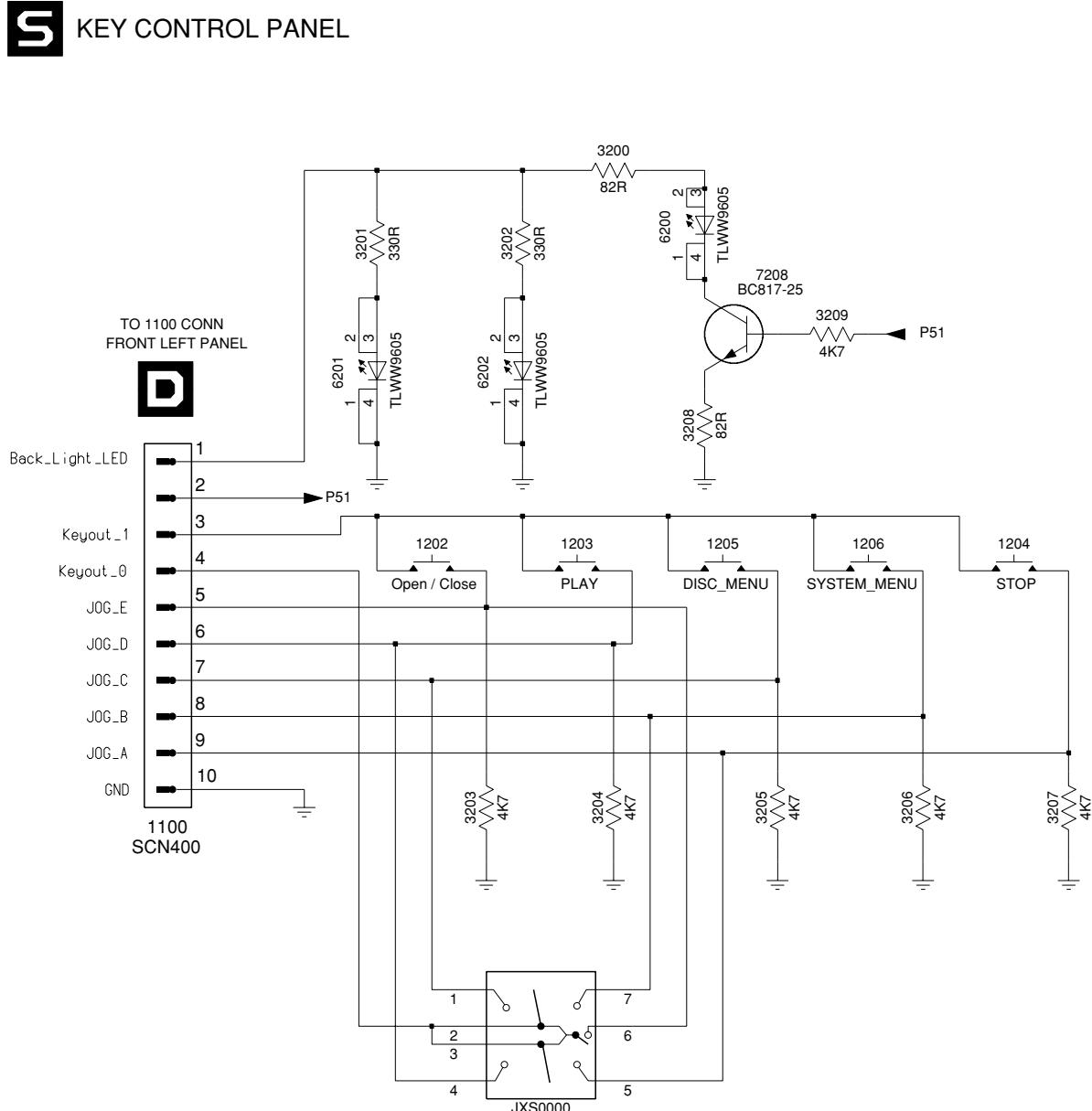
1100 A6 1201 A3 1202 B7 1203 B6 1204 B6 1205 B5 1206 B1 6200 B7 6201 A3 6202 B3  
3200 A7 3201 A7 3202 A7 3203 B7 3204 B4 3205 B4 3206 B5 3207 B6 3208 A7 3209 A7 7208 A7



## FRONT RIGHT BOARD - CIRCUIT DIAGRAM

1100 C1 1202 B2 1204 B4 1206 B4 3201 A2 3203 C2 3205 C3 3207 C4 3209 A4 6201 B2 6202 B2 7208 A3  
 1201 D2 1203 B3 1205 B3 3200 A3 3202 A2 3204 C3 3206 C4 3208 B3 6200 A3 6202 B2

1 2 3 4



3139 243 31363\_3139 243 3134 pt3\_dd wk0437

1 2 3 4

## ELECTRICAL PARTS LIST - FRONT LEFT BOARD

## MISCELLANEOUS

1001	3139 248 83821	PCBAS FRONT LEFT
1102	4822 265 11184	FLEX CONNECTOR 18P
1103	2422 540 98519	RES CER 8,467MHz
1104	4822 242 70938	RES XTL 32,768KHz
1110	4822 276 13775	TACT SWITCH
1111	4822 276 13775	TACT SWITCH
1112	4822 276 13775	TACT SWITCH

## CAPACITORS

2107	4822 124 12095	100uF 20% 16V
2114	2020 021 91729	4,7uF 20% 35V

## RESISTORS

3104	4822 117 11152	△ 4R7 5%
3109	4822 117 11152	△ 4R7 5%
3110	4822 117 11152	△ 4R7 5%

## DIODES

6100	9340 548 54115	DIO REG SM PDZ6.2B
6101	9322 190 55676	LED VS LTL-816TDK3
6102	9322 190 55676	LED VS LTL-816TDK3
6104	9322 179 76676	LED VS LTL-816EELC
6105	4822 130 11397	DIO SIG SM BAS316
6106	4822 130 11551	DIO REG SM BZX384-C10
6107	4822 130 11416	DIO REG SM BZX384-C6V8
6108	4822 130 11148	DIO REG SM BZX384-C4V7
6109	9322 209 36667	LED VS TLWW9605
6150	3198 020 55680	DIO REG SM BZX384-C5V6

## TRANSISTORS &amp; INTEGRATED CIRCUITS

7101	9322 155 82667	IR RECEIVER TSOP2236YA1
7102	3139 240 50971	IC TMP87CM48DF
7103	4822 130 60373	BC857B
7105	5322 130 60159	BC847B
7106	5322 130 60159	BC847B
7107	4822 130 42804	BC817-25
7108	3198 010 43130	BC807-25
7109	4822 130 42804	BC817-25
7110	4822 130 42804	BC817-25
7111	4822 130 42804	BC817-25
7125	4822 130 42804	BC817-25
7147	4822 130 42804	BC817-25
7150	4822 130 42804	BC817-25
7151	9322 119 42685	IC SM MC78FC33H

Note : Only the parts mentioned in this list are normal service spare parts.

## ELECTRICAL PARTS LIST - FRONT RIGHT BOARD

## MISCELLANEOUS

1002	3139 248 83811	PCBAS FRONT RIGHT
1201	2422 129 17158	SWITCH TACT NAV 1P 5POS
	JXS0000	
1202	4822 276 13775	TACT SWITCH
1203	4822 276 13775	TACT SWITCH
1204	4822 276 13775	TACT SWITCH
1205	4822 276 13775	TACT SWITCH
1206	4822 276 13775	TACT SWITCH

## DIODES

6200	9322 209 36667	LED VS TLWW9605
6201	9322 209 36667	LED VS TLWW9605
6202	9322 209 36667	LED VS TLWW9605

## TRANSISTORS &amp; INTEGRATED CIRCUITS

7208	4822 130 42804	BC817-25
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Note : Only the parts mentioned in this list are normal service spare parts.

# AV BOARD

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## ***BRIEF INTRODUCTION OF THE AV BOARD***

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The AV Board consists of the following features :

A. VIDEO OUTPUT

- CVBS
- S-Video
- Scart RGB (For Europe Model)

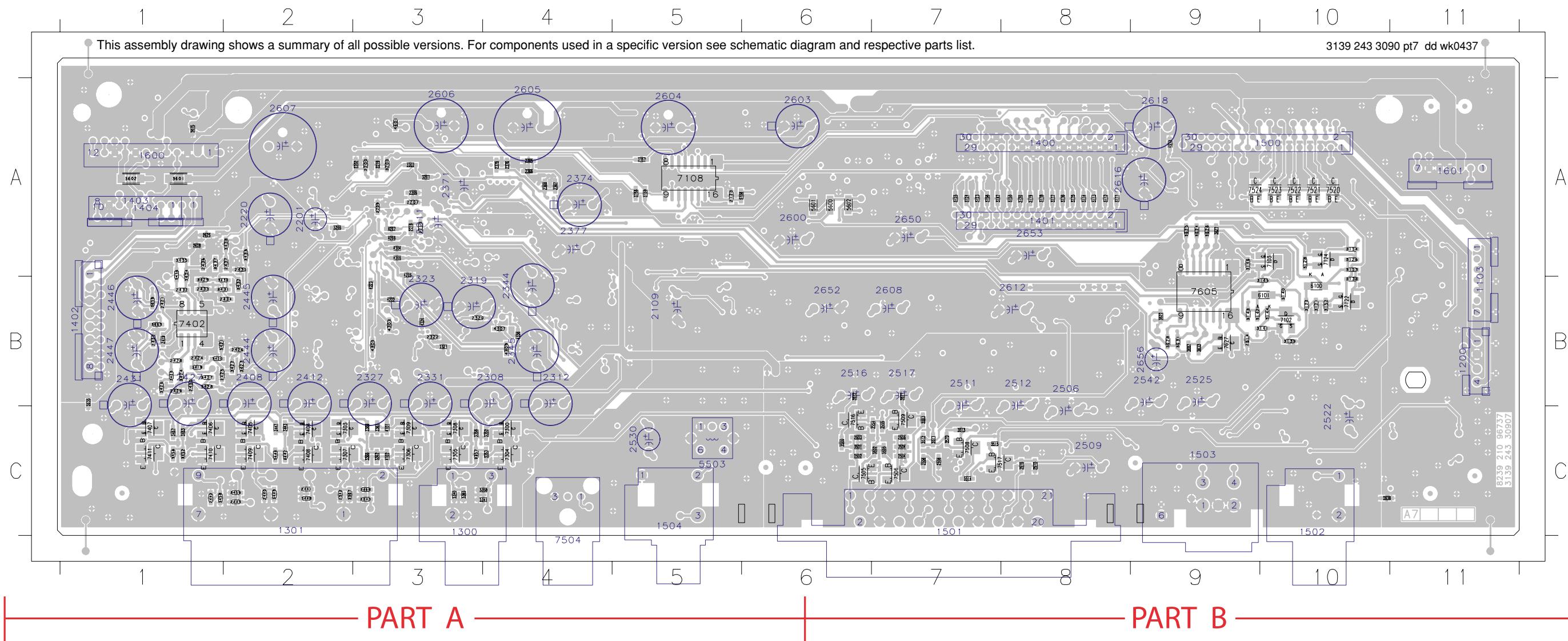
B. AUDIO OUTPUT

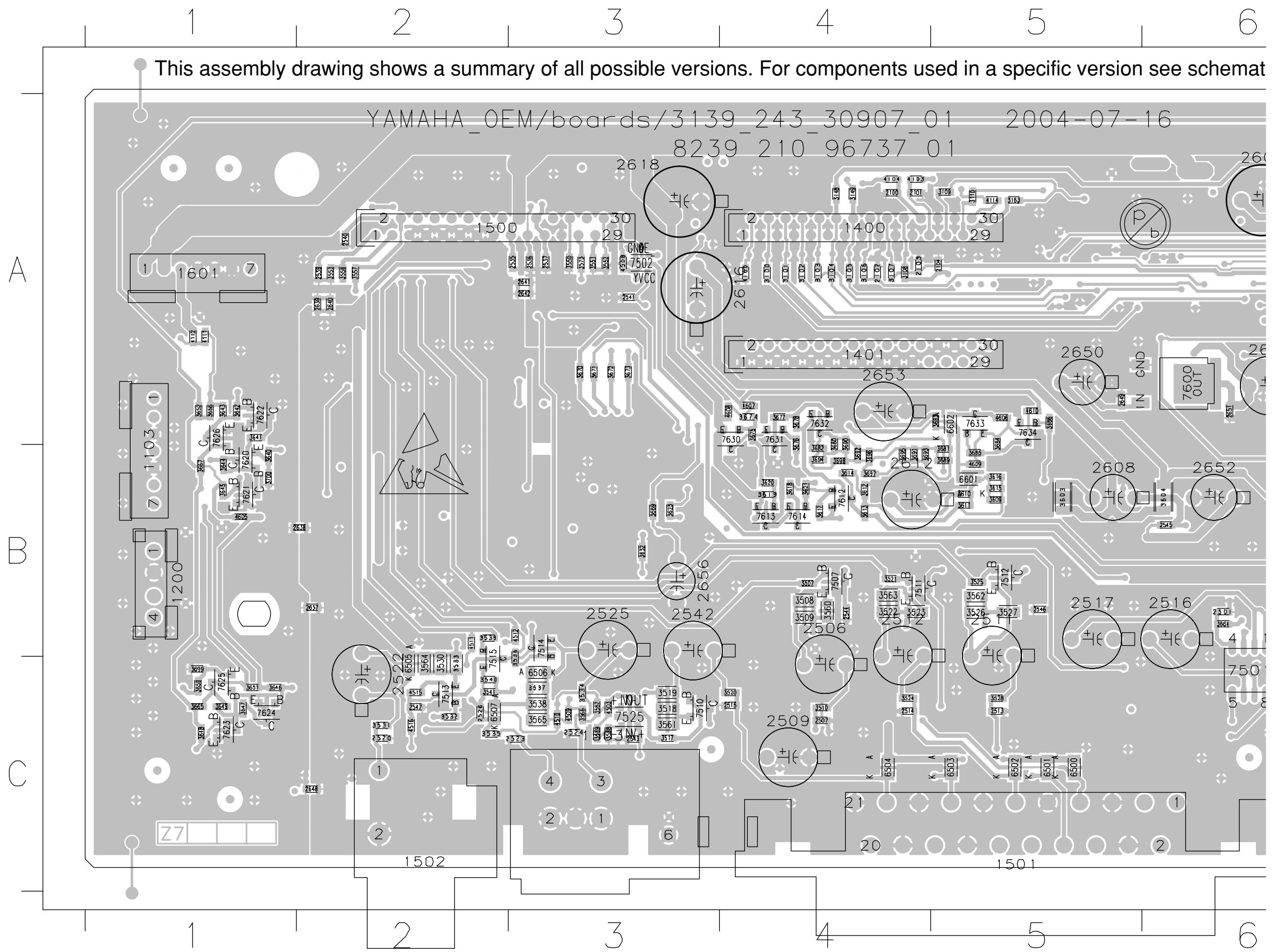
- 2 Channel / 6 Channel
- Coaxial / Optical



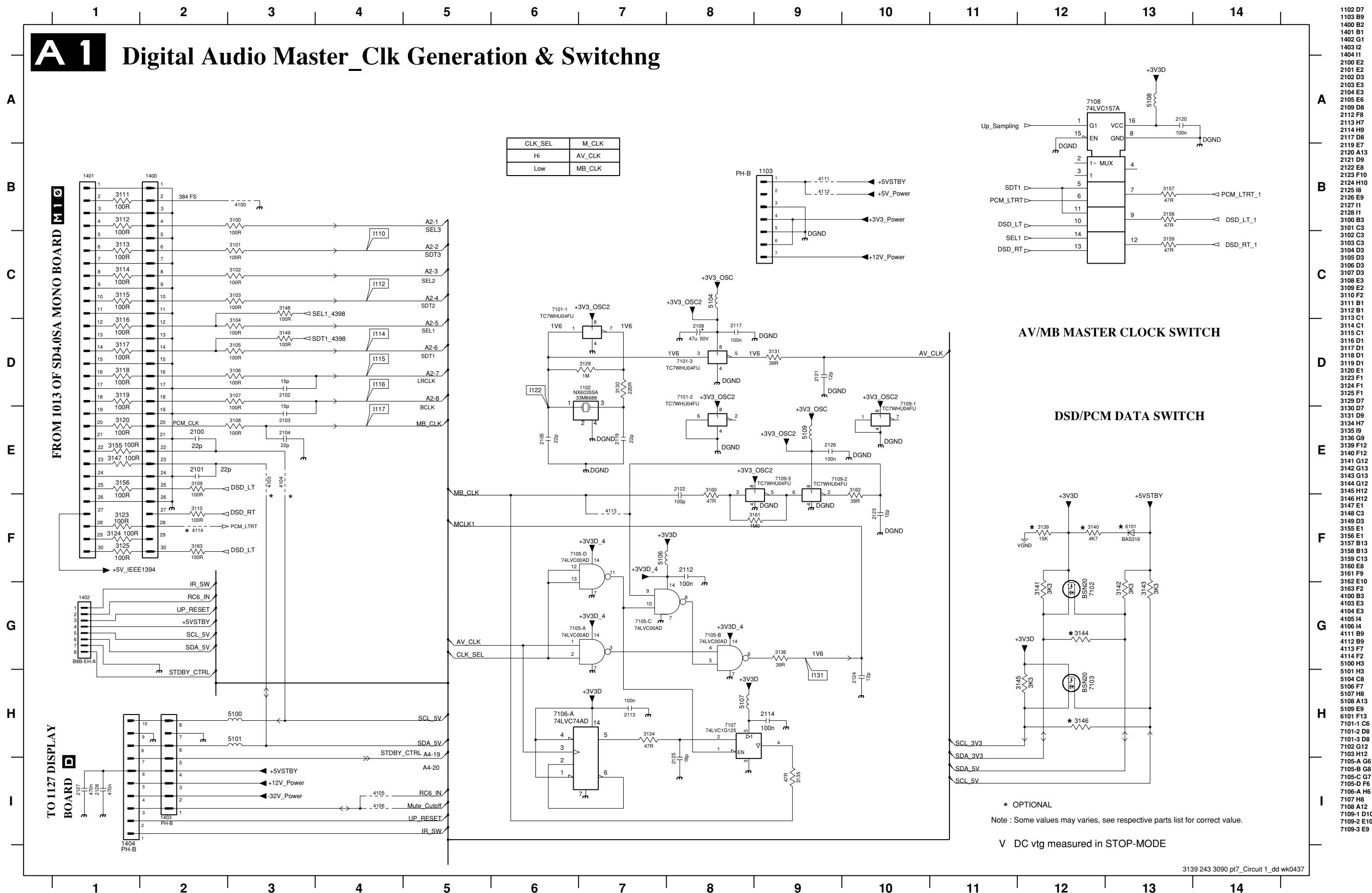
**AV BOARD - BOTTOM VIEW LAYOUT**

1103 B11	1400 A8	1404 A1	1503 C9	2109 B5	2308 B4	2327 B3	2371 A3	2412 B2	2445 B2	2509 C8	2517 B7	2542 B9	2605 A4	2612 B8	2652 B6	7504 C4		
1200 B11	1401 A8	1500 A10	1504 C5	2201 A2	2312 B4	2331 B3	2374 A4	2427 B1	2446 B1	2511 B7	2522 C10	2600 A6	2606 A3	2616 A8	2653 A8			
1300 C3	1402 B1	1501 C7	1600 A1	2211 A3	2319 B3	2344 B4	2377 A4	2431 B1	2447 B1	2512 B8	2525 B9	2603 A6	2607 A2	2618 A9	2656 B9			
1301 C2	1403 A1	1502 C10	1601 A11	2220 A2	2323 B3	2345 B4	2408 B2	2444 B2	2506 B8	2516 B6	2530 C5	2604 A5	2608 B7	2650 A7	5503 C5			
2120 A5	2340 C3	2422 B1	2437 A2	2615 A1	3118 A8	3140 B10	3156 A7	3358 C4	3423 B2	3435 A1	3456 C1	3512 C7	3622 B9	4102 C3	4309 B4	7108 A5	7404 C2	7516 C6
2200 A3	2341 C4	2424 B1	2440 C2	2625 A1	3119 A8	3141 B10	3157 A5	3361 C3	3424 B1	3436 A1	3457 C1	3513 C7	3624 A9	4204 A3	4336 B4	7122 B10	7405 C2	7517 C7
2202 A3	2342 C2	2425 B1	2441 C2	2628 A1	3120 A8	3142 B9	3158 A5	3362 C3	3425 B1	3437 A1	3458 C1	3514 C7	3625 A9	4205 A3	4513 A9	7124 A10	7406 C1	7520 A10
2207 A3	2343 C3	2426 B2	2442 C1	2635 B1	3121 B10	3143 B9	3159 A5	3365 C3	3426 B1	3438 A2	3459 C2	3515 C7	3626 A9	4206 A3	4601 A3	7300 C4	7407 C1	7521 A10
2208 A3	2365 A4	2428 B1	2443 C2	2636 C10	3122 B10	3144 B10	3200 A3	3366 C3	3427 B1	3442 C2	3500 C6	3516 C8	3627 A9	4207 A3	5108 A5	7303 C2	7408 C2	7522 A10
2209 A3	2366 A4	2429 B1	2502 C6	3111 A8	3123 A7	3145 B10	3201 A3	3367 C2	3428 B1	3443 C2	3501 C6	3554 C7	3628 B9	4222 B3	5600 A6	7304 C4	7409 C2	7523 A10
2213 A3	2367 A4	2430 B1	2503 C6	3112 A8	3124 A7	3146 A9	3202 A3	3368 C3	3429 B1	3446 C2	3502 C7	3555 C7	3631 B9	4223 B3	5601 A6	7305 C3	7410 C1	7524 A9
2218 A3	2368 A4	2432 B1	2504 C7	3113 A8	3125 A7	3147 A7	3206 A2	3369 C3	3430 B2	3447 C2	3503 C7	3570 C7	3633 B9	4224 B3	5602 A6	7306 C3	7411 C1	7605 B9
2219 A3	2375 A4	2433 B1	2505 C7	3114 A8	3126 A10	3152 B10	3352 C4	3419 B1	3431 B2	3448 C2	3504 C7	3571 B7	3658 B1	4225 A3	6100 B10	7307 C2	7505 C6	7627 B9
2320 B3	2376 A4	2434 B2	2508 C7	3115 A8	3128 A10	3153 A10	3353 C4	3420 B1	3432 B2	3449 C2	3505 C7	3572 B6	3663 B1	4303 B3	6101 B10	7308 C3	7506 C7	
2321 B3	2419 B1	2435 A2	2510 C8	3116 A8	3138 B10	3154 A10	3356 C3	3421 B1	3433 A2	3452 C1	3506 C7	3601 A1	3687 B9	4305 B3	7102 B10	7309 C3	7508 C7	
2322 B3	2421 B1	2436 A1	2614 B9	3117 A8	3139 B10	3155 A8	3357 C3	3422 B2	3434 A1	3453 C1	3511 C7	3602 A1	4101 C3	4307 B4	7103 A10	7402 B1	7509 C7	

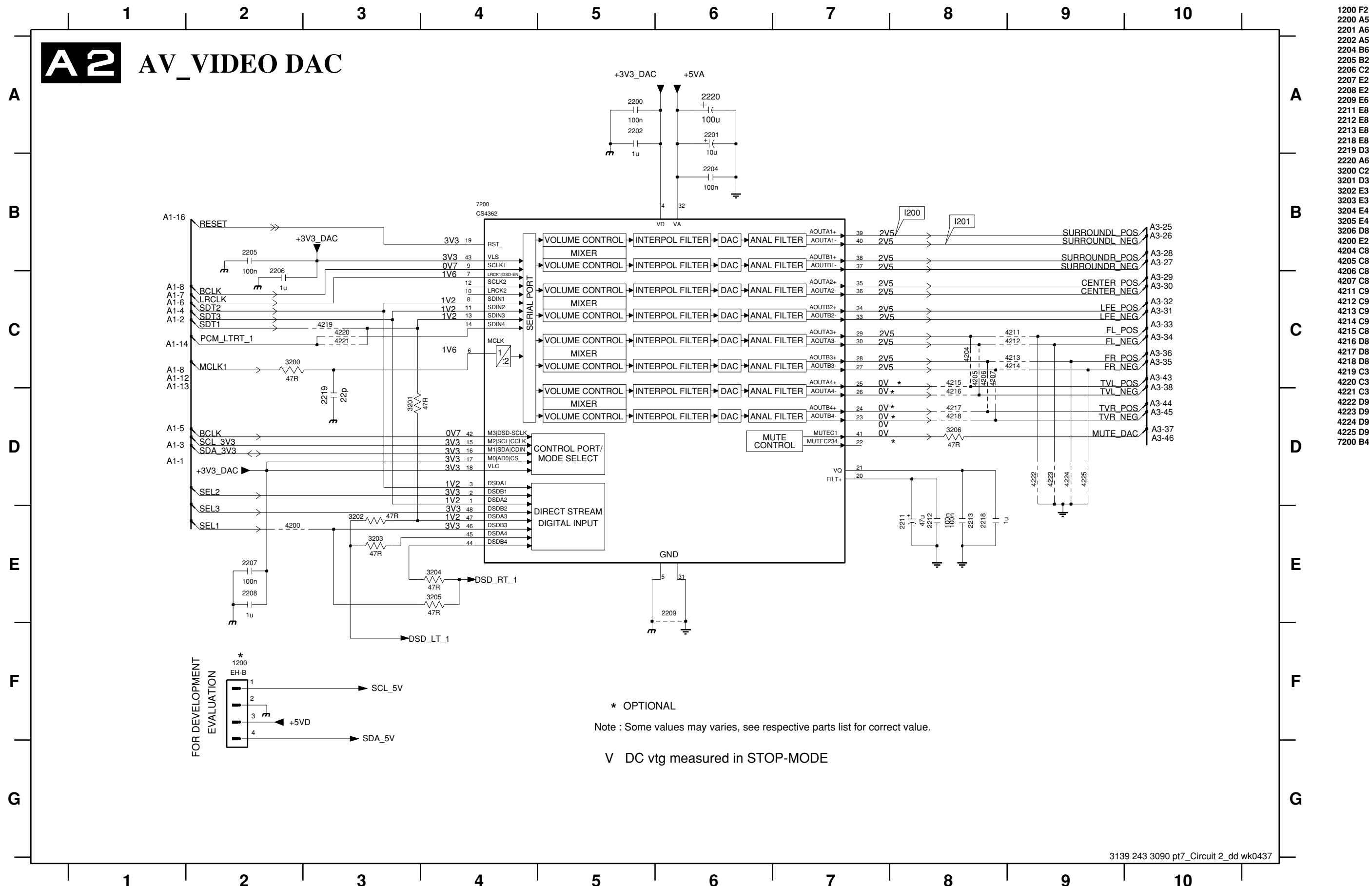


**AV BOARD - TOP VIEW LAYOUT (PART A)**


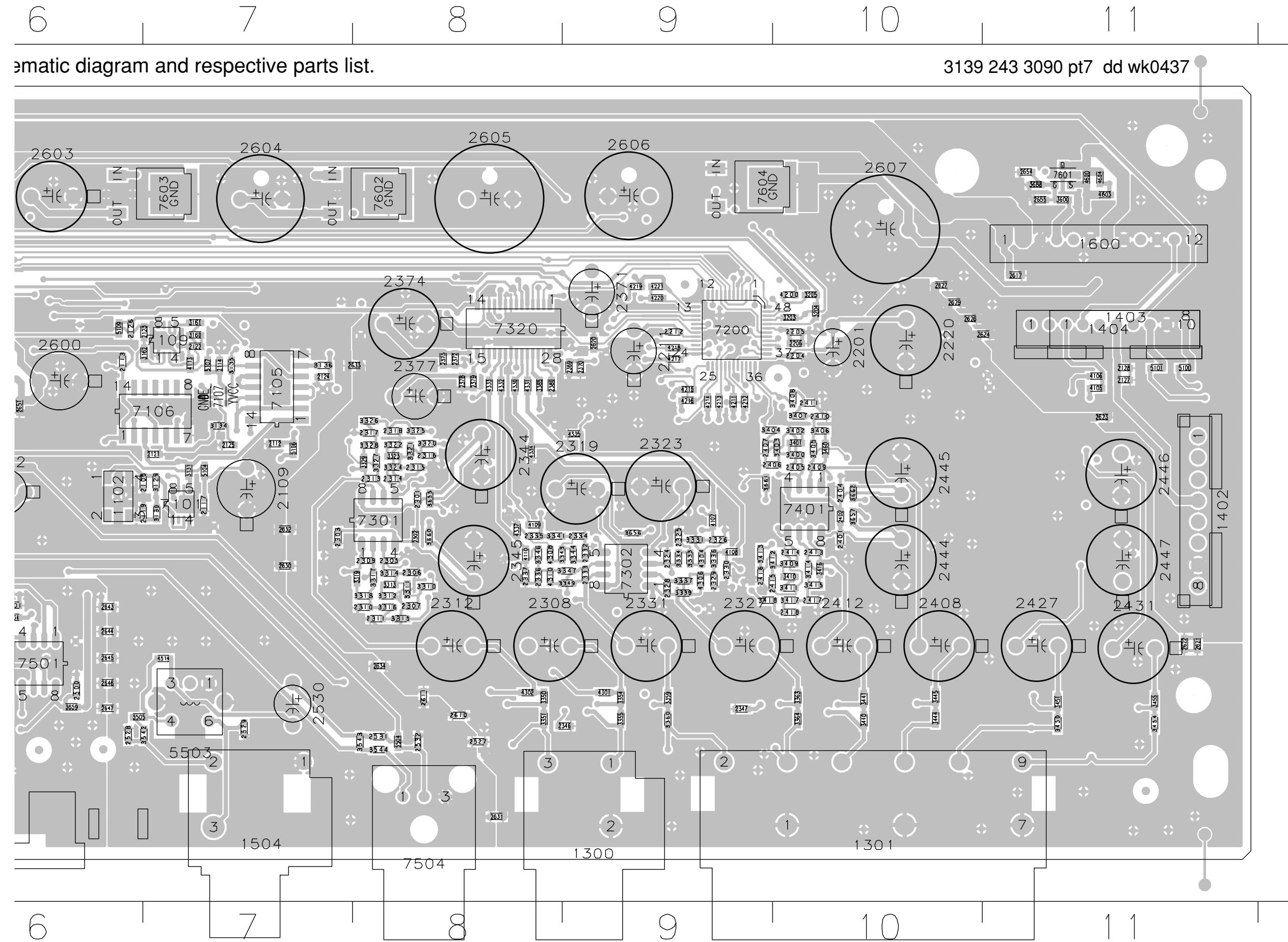
## AV BOARD - CIRCUIT DIAGRAM (PART 1)

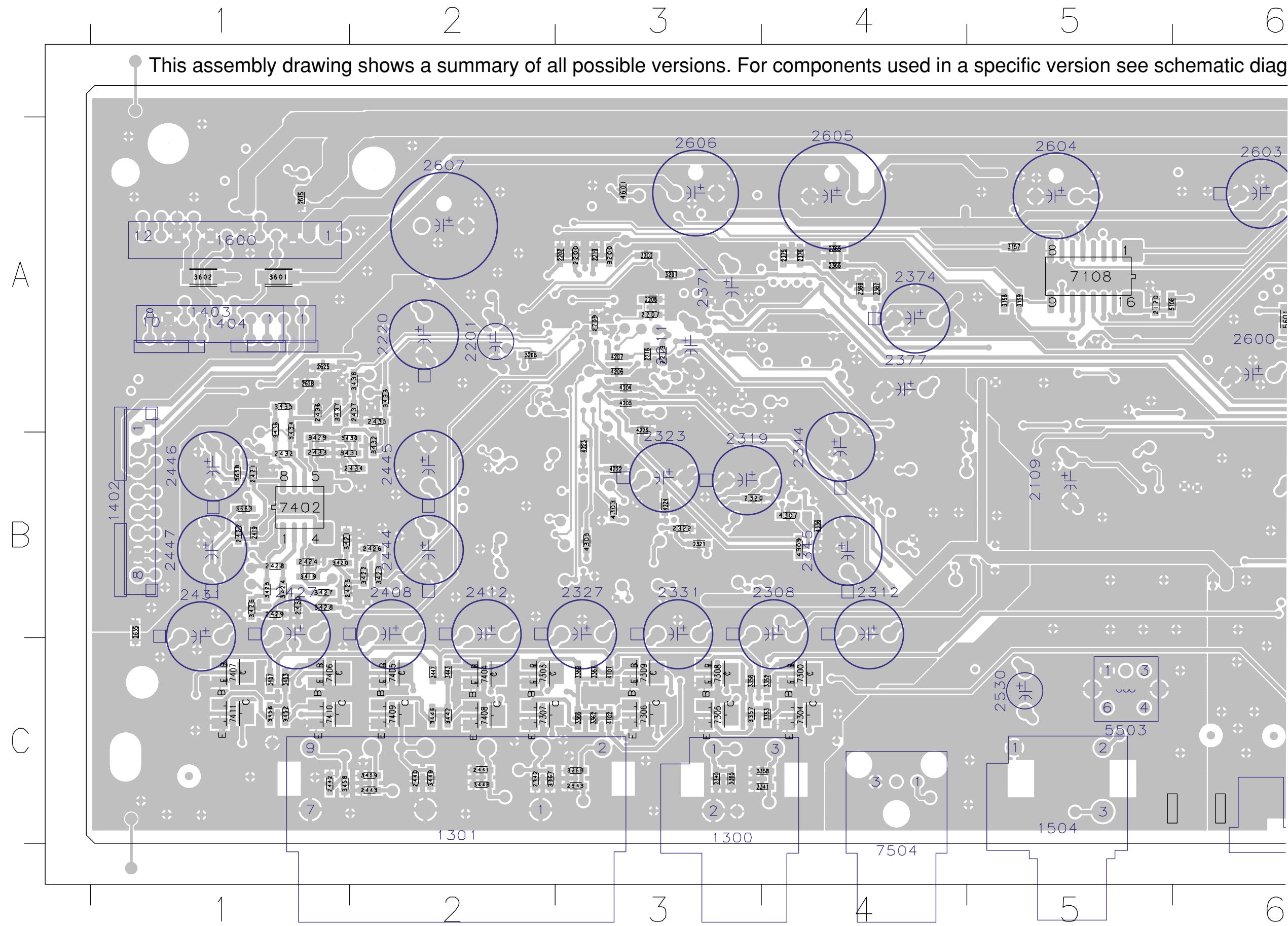


## AV BOARD - CIRCUIT DIAGRAM (PART 2)



## AV BOARD - TOP VIEW LAYOUT (PART B)



**AV BOARD - BOTTOM VIEW LAYOUT (PART A)**

## AV BOARD - CIRCUIT DIAGRAM (PART 3)

1

2

3

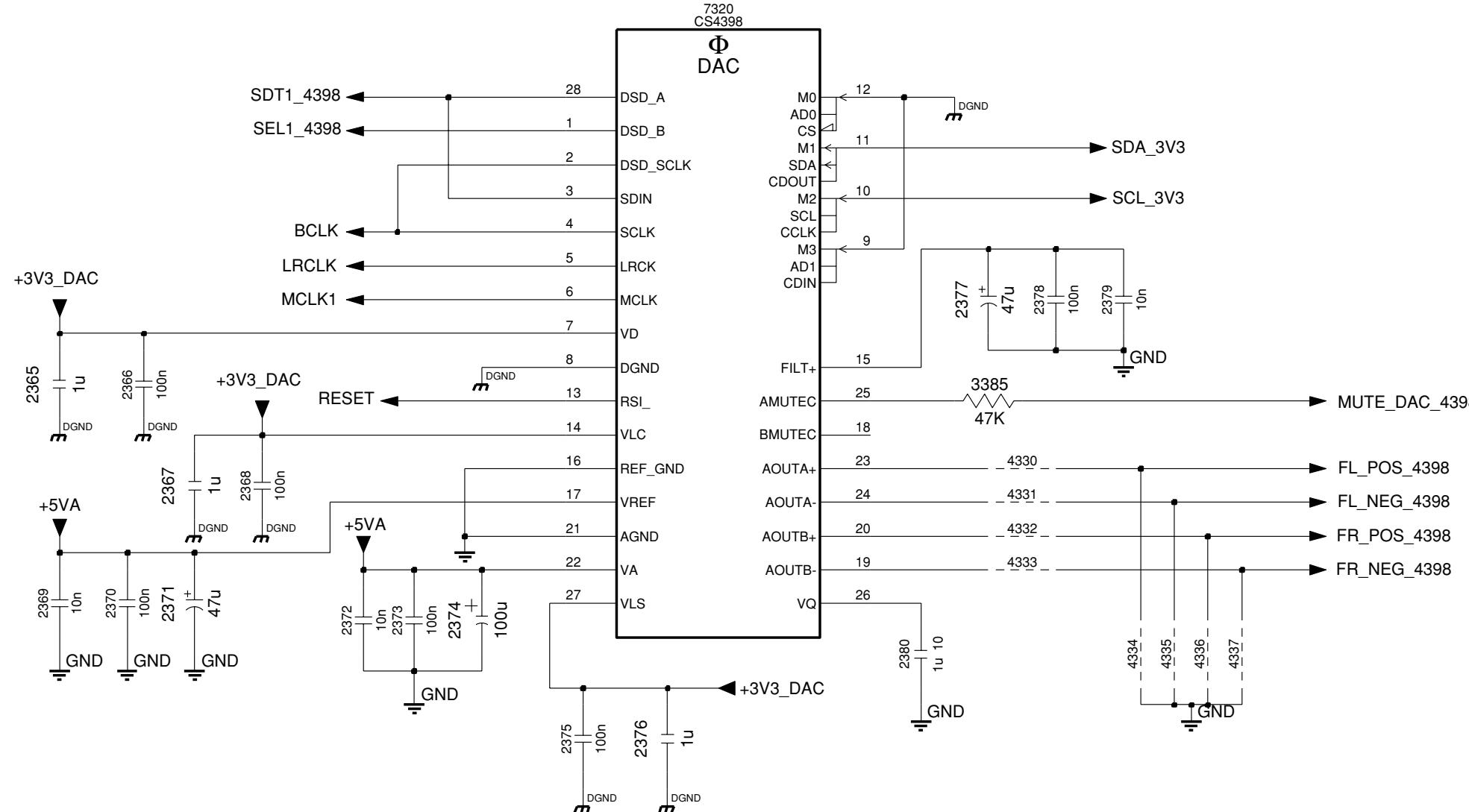
4

5

6

7

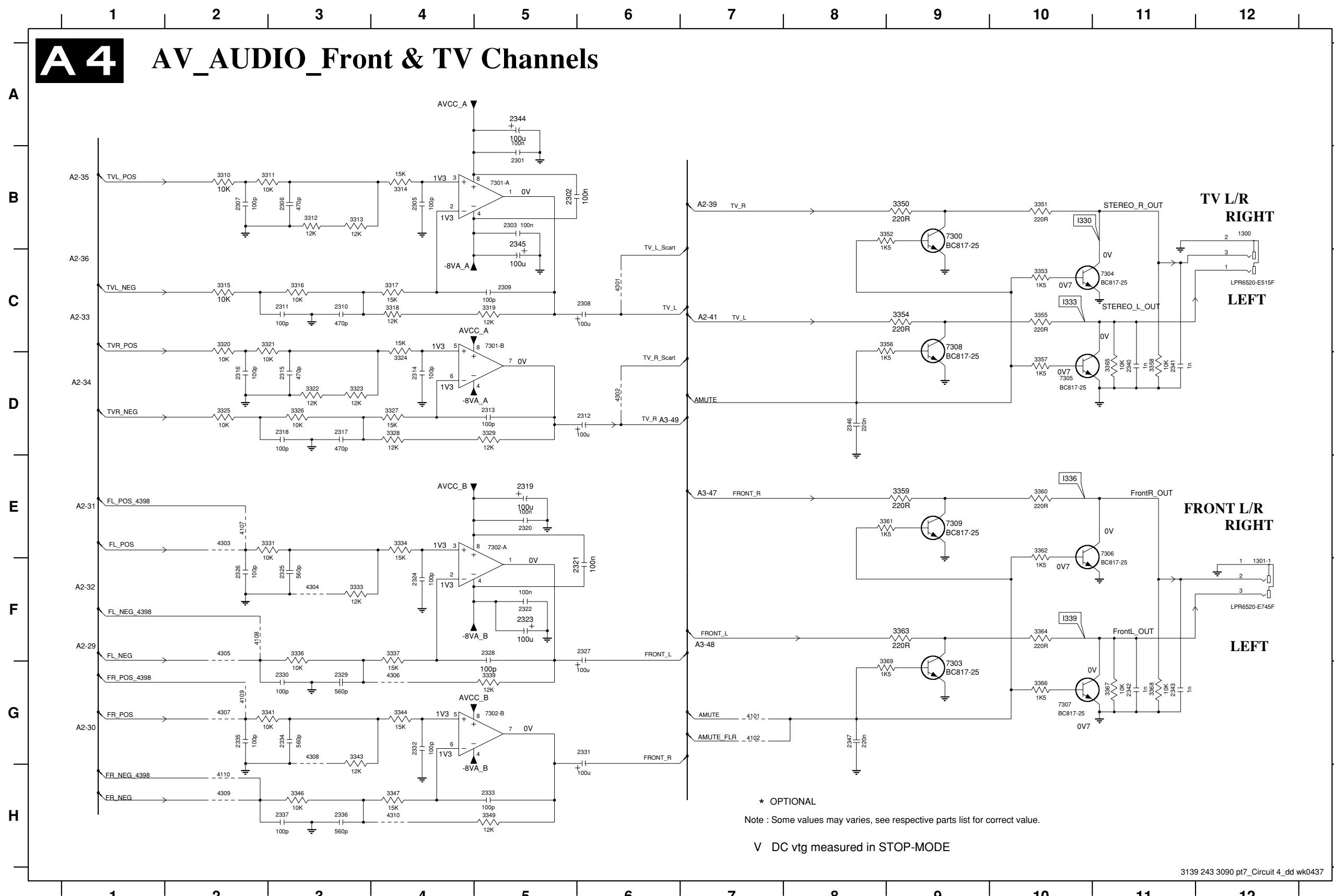
2365 C1  
 2366 C1  
 2367 C2  
 2368 C2  
 2369 D1  
 2370 D1  
 2371 D2  
 2372 D2  
 2373 D3  
 2374 D3  
 2375 D3  
 2376 D4  
 2377 B5  
 2378 B5  
 2379 B6  
 2380 D5  
 3385 C5  
 4330 C5  
 4331 C5  
 4332 C5  
 4333 D5  
 4334 D6  
 4335 D6  
 4336 D6  
 4337 D6  
 7320 A4

**A3****AV\_AUDIO\_Front & TV Channels****A****A****B****B****C****C****D****D****E****E**

Note : Some values may varies, see respective parts list for correct value.

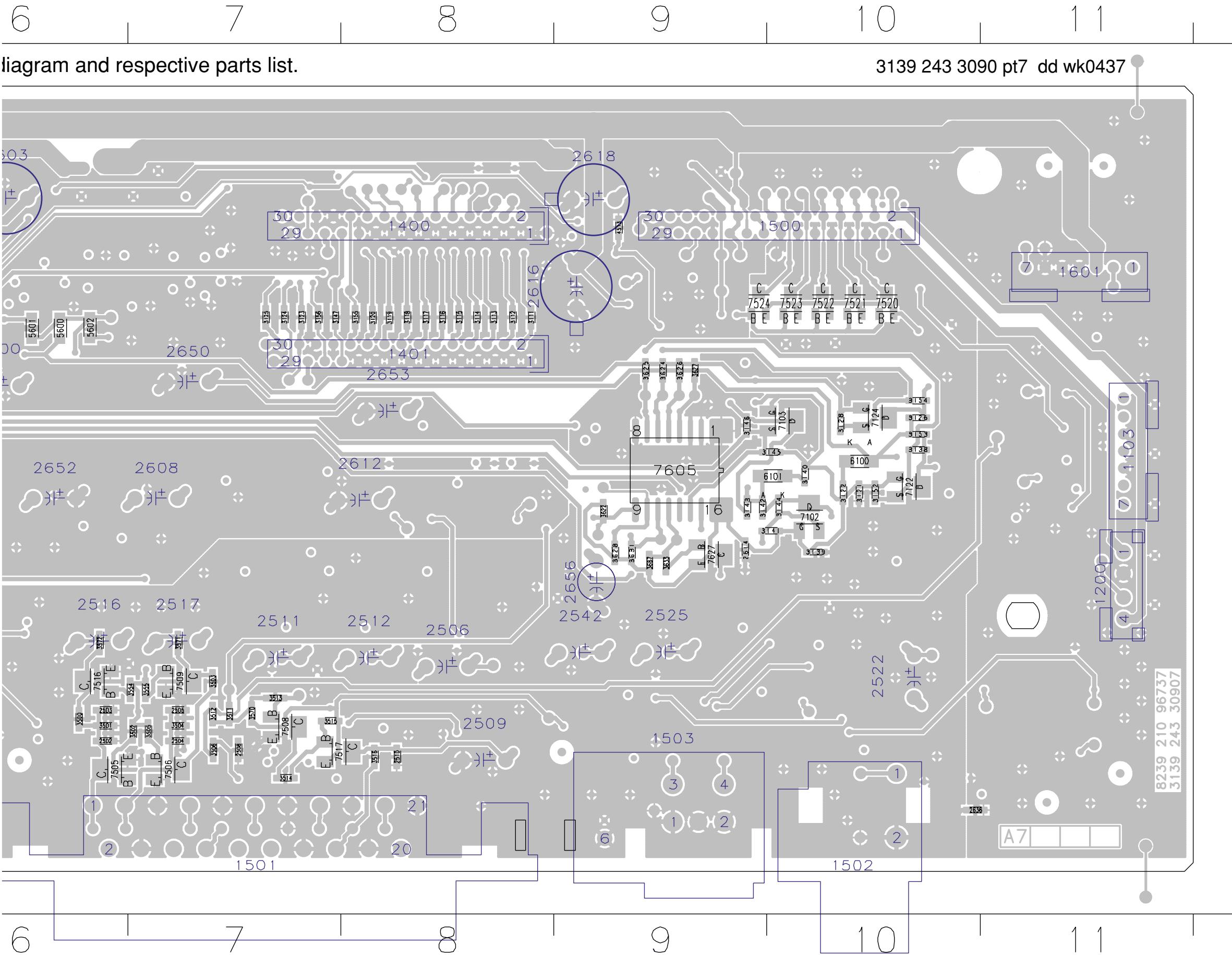
3139 243 3090 pt7\_Circuit 3\_dd wk0437

## AV BOARD - CIRCUIT DIAGRAM (PART 4)

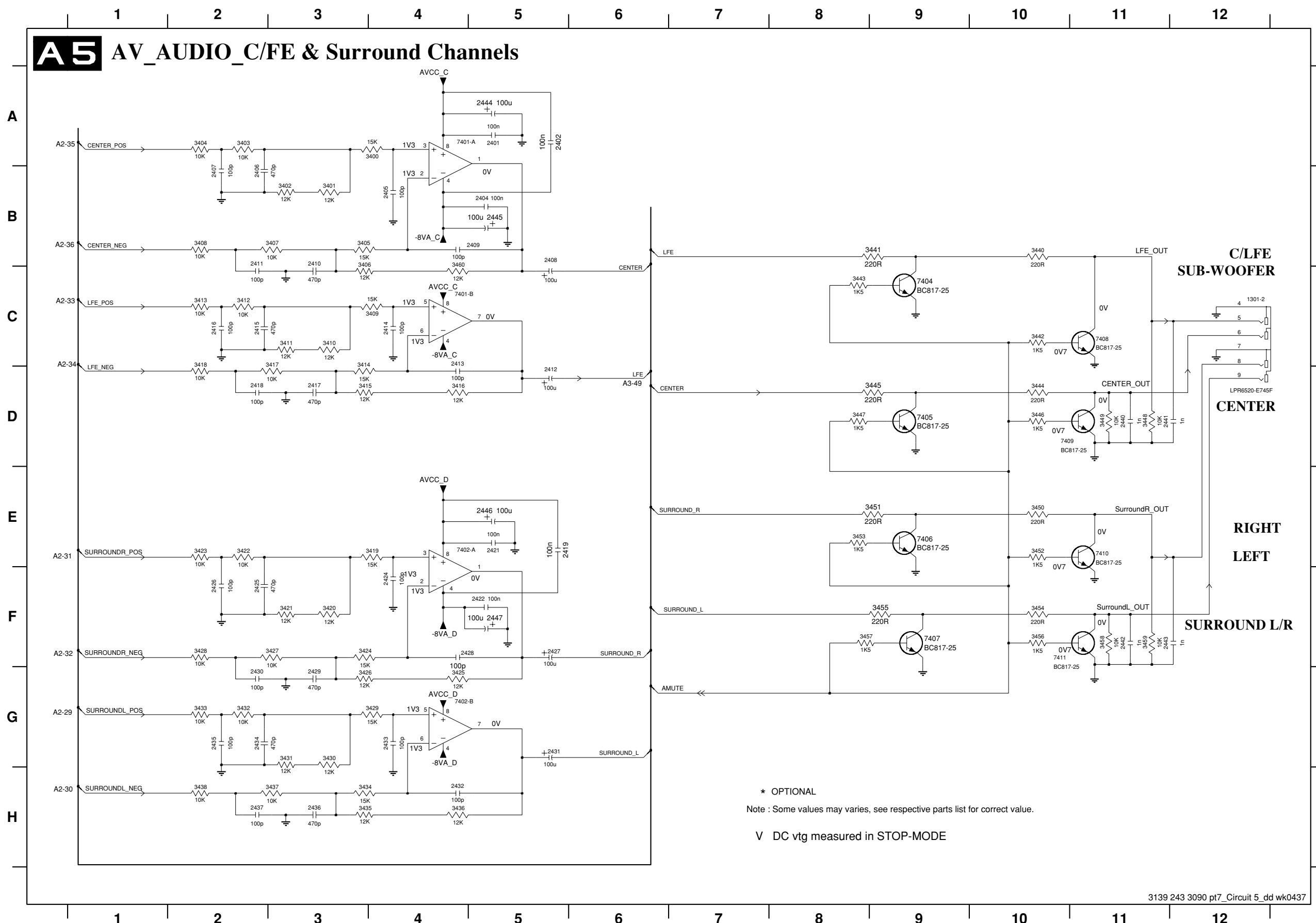


1300 B12	4102 G7
1301 F12	4107 E2
2301 B5	4108 F2
2302 B5	4109 G2
2303 B5	4110 H2
2305 B4	4301 C6
2306 B3	4302 D6
2307 B2	4303 E2
2308 C6	4304 F3
2309 C5	4305 F2
2310 C3	4306 G4
2311 C3	4307 G2
2312 D6	4308 G3
2313 D5	4310 H4
2314 D4	7300 B9
2315 D3	7301-A B5
2317 D3	7301-B C5
2318 D3	7302-A E5
2319 E5	7302-B G5
2320 E5	7303 G9
2321 F6	7304 C11
2322 F5	7305 D10
2323 F5	7306 E11
2324 F4	7307 G10
2325 F3	7308 C9
2326 F2	7309 E9
2327 F6	2328 F5
2329 G3	2330 G3
2331 G6	2332 G4
2333 G3	2334 G1
2335 G2	2340 D11
2336 H3	2341 D11
2337 H3	2342 G11
2340 D11	2343 G11
2341 D11	2344 A5
2342 G11	2345 B5
2343 G11	2346 D8
2344 A5	2347 G8
2345 B5	3310 B2
2346 D8	3311 B3
2347 G8	3312 B3
3310 B2	3313 B3
3311 B3	3314 B4
3312 B3	3315 C2
3313 B3	3316 C3
3314 B4	3317 C4
3315 C2	3318 C4
3316 C3	3319 C5
3317 C4	3320 C2
3318 C4	3321 C3
3319 C5	3322 D3
3320 C2	3323 D3
3321 C3	3324 D4
3322 D3	3325 D2
3323 D3	3326 D3
3324 D4	3327 D4
3325 D2	3328 D4
3326 D3	3329 D5
3327 D4	3331 E3
3328 D4	3333 F3
3329 D5	3334 E4
3331 E3	3336 F3
3333 F3	3337 F4
3334 E4	3339 G5
3336 F3	3341 G3
3337 F4	3343 G3
3339 G5	3344 G4
3341 G3	3346 H3
3343 G3	3347 H4
3344 G4	3349 H5
3346 H3	3350 B9
3347 H4	3351 B10
3349 H5	3352 B9
3350 B9	3353 C10
3351 B10	3354 C9
3352 B9	3355 C10
3353 C10	3356 C9
3354 C9	3357 D10
3355 C10	3358 D11
3356 C9	3359 E9
3357 D10	3360 E10
3358 D11	3361 E9
3359 E9	3362 E10
3360 E10	3363 F9
3361 E9	3364 F10
3362 E10	3365 D11
3363 F9	3366 G10
3364 F10	3367 G11
3365 D11	3368 G11
3366 G10	3369 G9
3367 G11	4101 G7

3139 243 3090 pt7\_Circuit 4\_dd wk0437

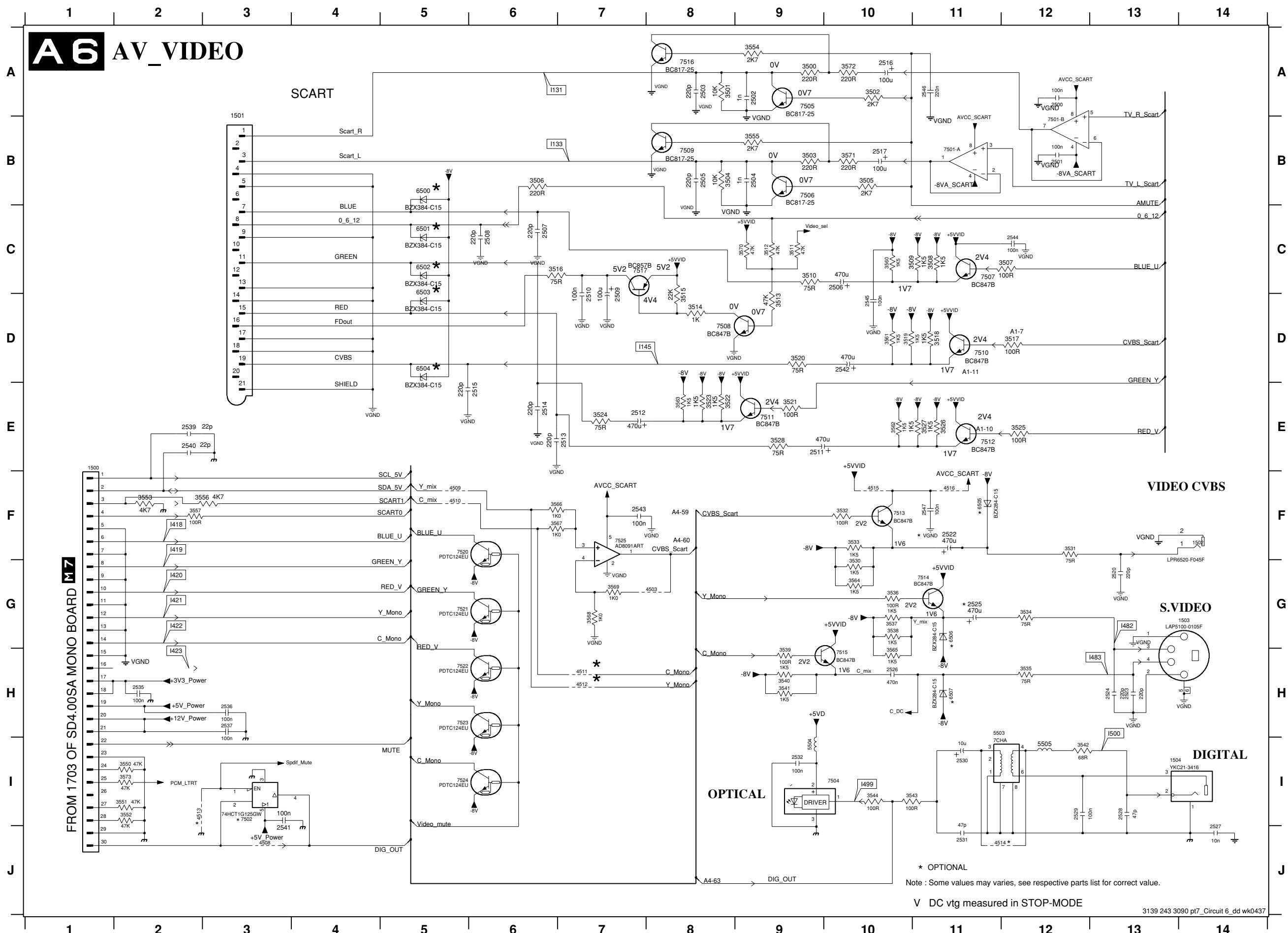
**AV BOARD - BOTTOM VIEW LAYOUT (PART B)**

## AV BOARD - CIRCUIT DIAGRAM (PART 5)

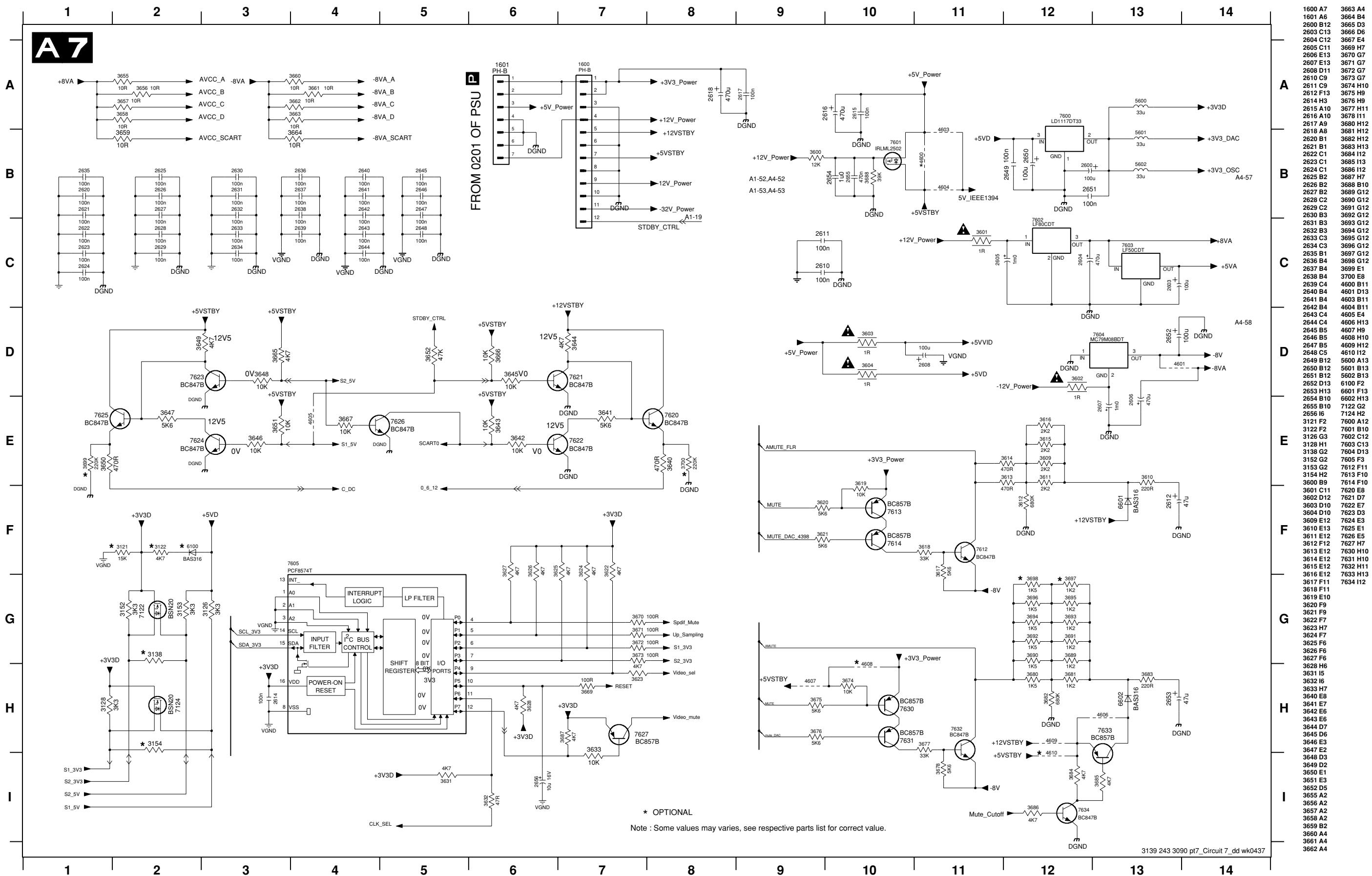


1301-2 C12	7401-B C4
2401 A5	7402-B G4
2402 A5	7404 C9
2404 B5	7405 D9
2405 B4	7406 E9
2406 B2	7407 F9
2407 B2	7408 C11
2408 B5	7409 D10
2409 B5	7410 E11
2410 B3	7411 F10
2411 B2	
2412 D5	
2413 C4	
2414 C4	
2415 C2	
2416 D3	
2417 D2	
2418 E5	
2421 E5	
2422 F5	
2424 F4	
2425 F2	
2427 F5	
2428 F4	
2429 G3	
2430 G2	
2431 G5	
2432 H4	
2433 G4	
2434 G2	
2435 G2	
2436 H3	
2437 H2	
2440 D11	
2441 D11	
2442 F11	
2444 A5	
2445 B5	
2446 E5	
2447 F5	
3400 A4	
3401 B3	
3402 B3	
3403 A2	
3405 B3	
3407 B3	
3409 C4	
3411 C3	
3413 C2	
3415 D3	
3417 D3	
3419 D2	
3421 E4	
3423 F3	
3425 E2	
3427 E2	
3429 F3	
3431 G4	
3433 G2	
3435 G2	
3437 H3	
3440 B10	
3441 B9	
3442 C10	
3443 C8	
3444 D10	
3445 D9	
3446 D10	
3447 D8	
3448 D11	
3449 D11	
3450 E10	
3451 E9	
3452 E10	
3453 E8	
3454 F10	
3455 F9	
3456 F10	
3457 F8	
3458 F11	
3459 F11	
3460 C4	
7401-A A4	

## AV BOARD - CIRCUIT DIAGRAM (PART 6)



## AV BOARD - CIRCUIT DIAGRAM (PART 7)





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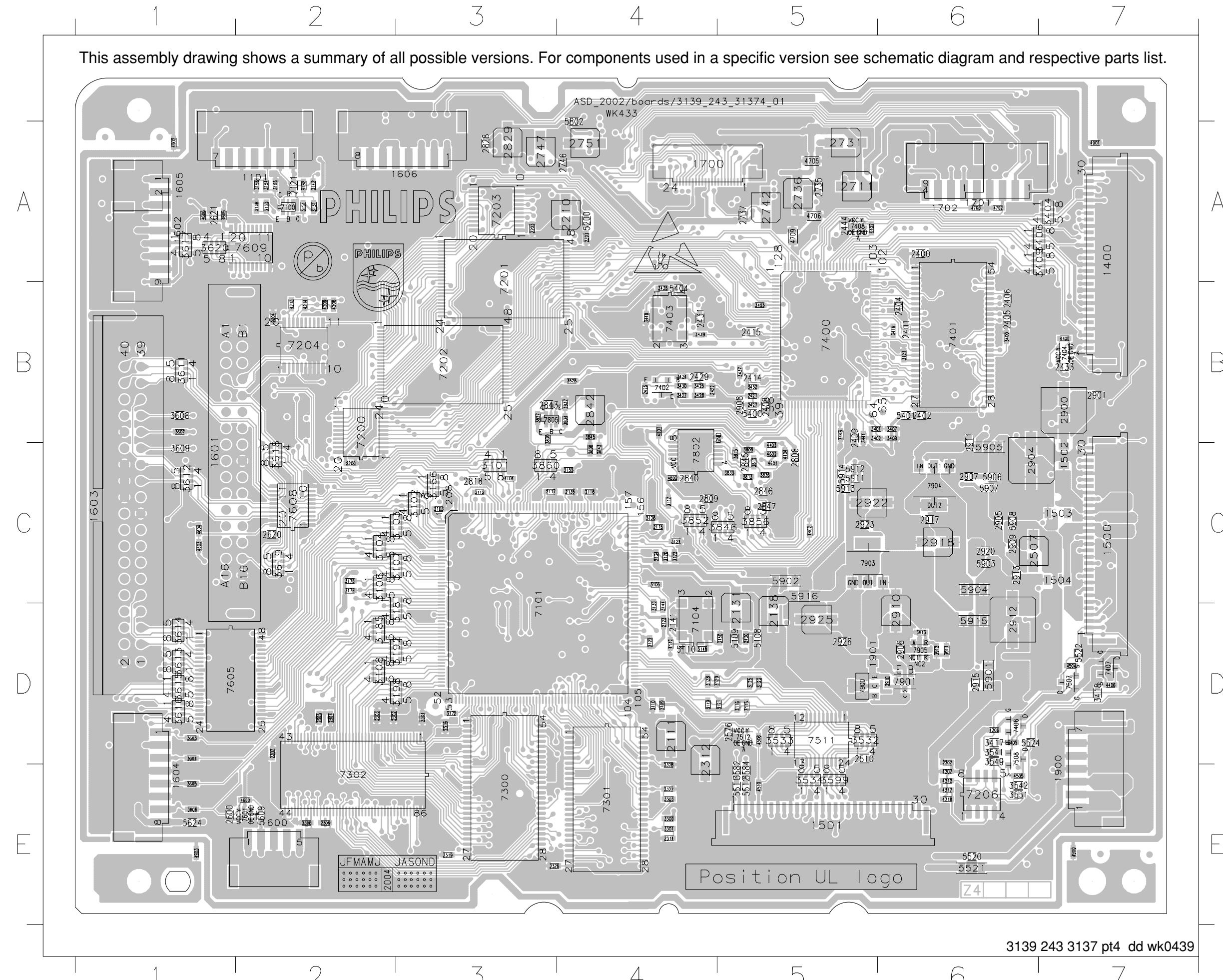
# MODULE SD5.2 (S) XSA 4FL

## DV HDMI

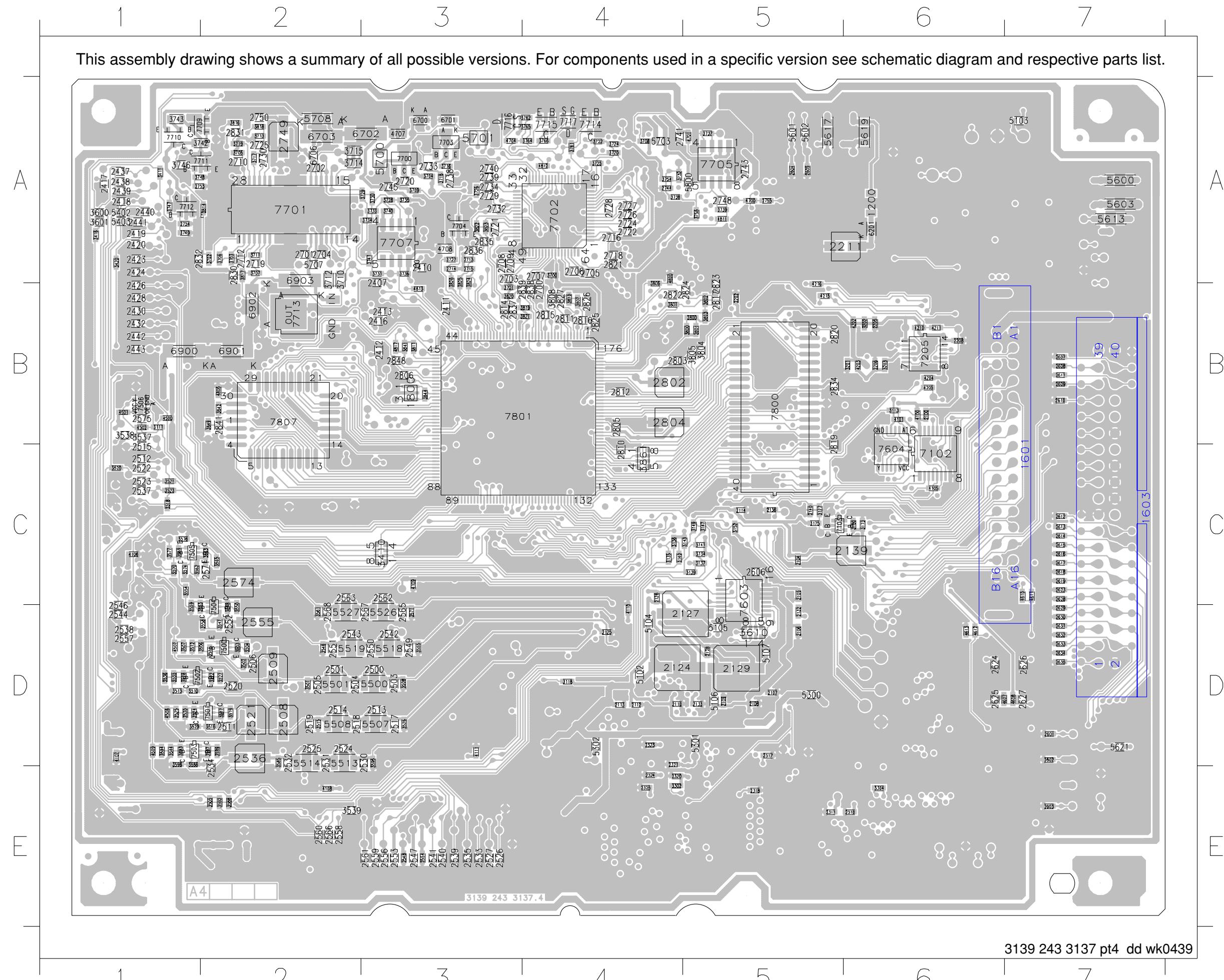
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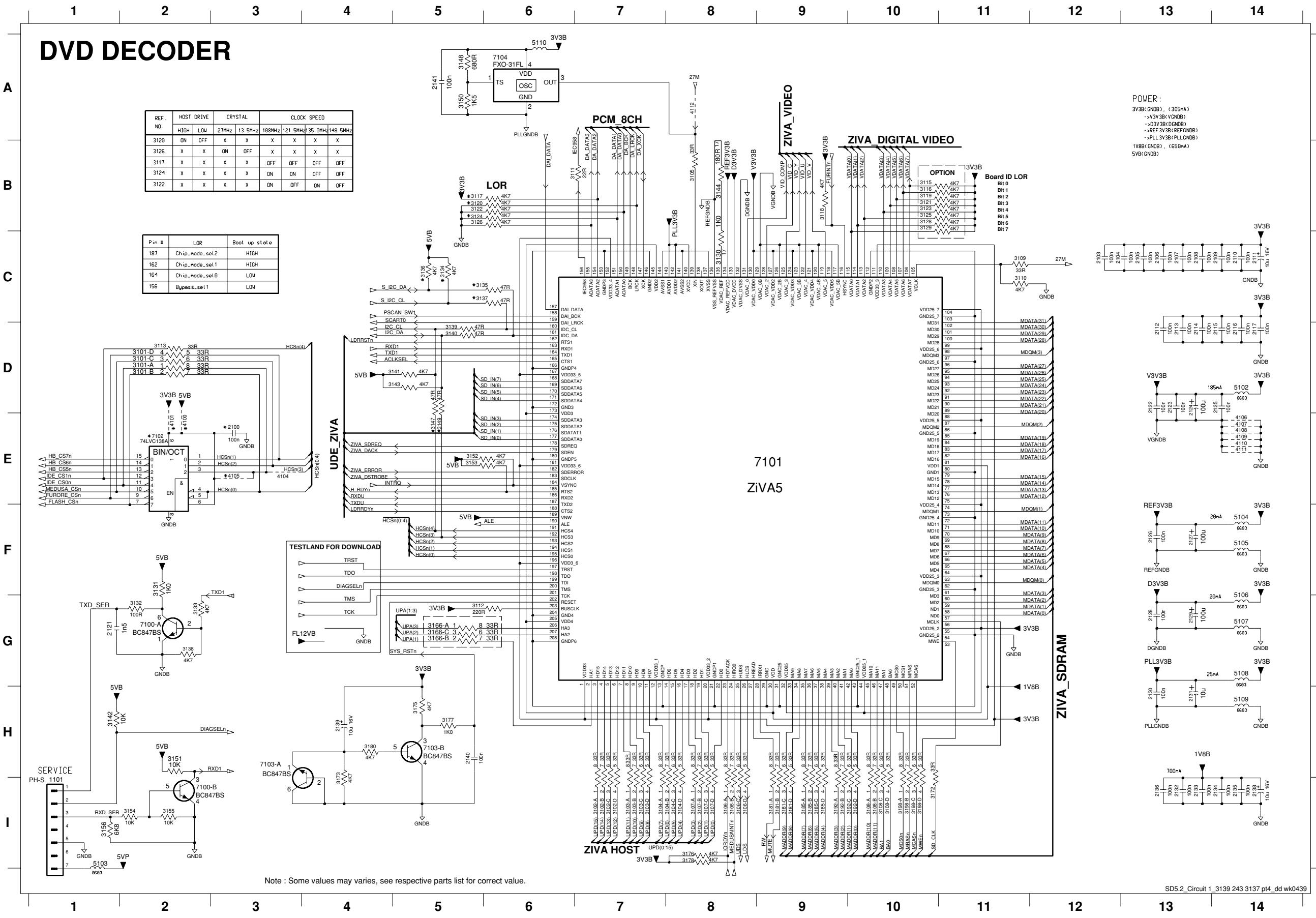
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**TOP VIEW LAYOUT**

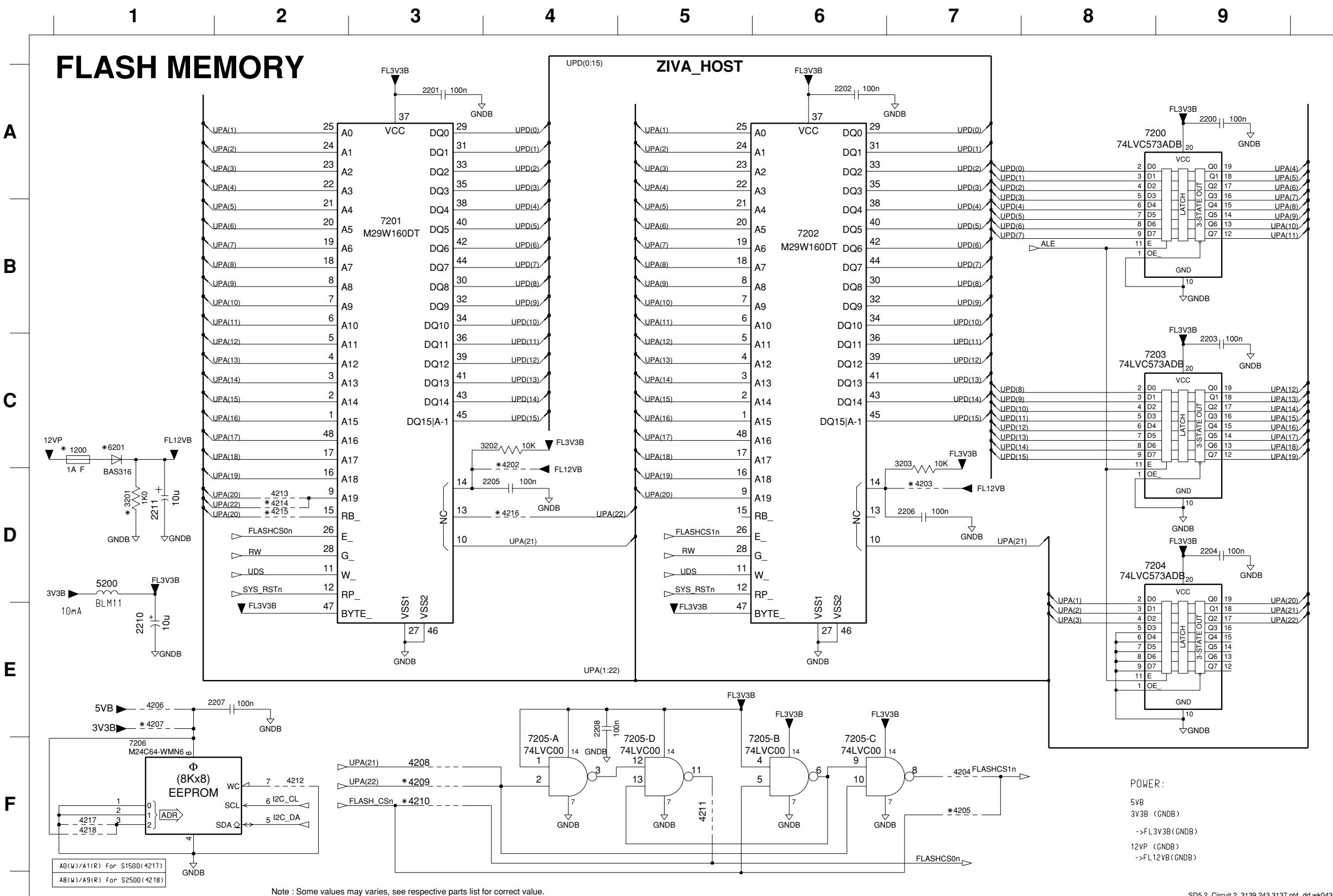


**BOTTOM VIEW LAYOUT**

**CIRCUIT DIAGRAM (PART 1)**

1101 I1	3173 I4
2100 E3	3175 H5
2103 C12	3176 I8
2104 C12	3177 H5
2105 C13	3178 I8
2106 C13	3180 H4
2107 C13	3181-A I9
2108 C13	3181-B I9
2109 C14	3181-C I9
2110 C14	3181-D I9
2111 C14	3185-A I9
2112 D13	3185-B I9
2113 D13	3185-C I9
2114 D13	3185-D I9
2115 D14	3192-A I9
2116 D14	3192-B I9
2117 D14	3192-C I10
2121 G1	3192-D I10
2122 D13	3198-A I10
2123 D13	3198-B I10
2124 D13	3198-C I10
2125 D14	3198-D I10
2126 F13	4100 E2
2127 F13	4101 E2
2128 G13	4104 E3
2129 G13	4105 E3
2130 H13	4106 E14
2131 H13	4107 E14
2132 H13	4108 E14
2133 H13	4109 E14
2134 H14	4110 E14
2135 H14	4111 E14
2136 H13	4112 A8
2138 H14	5102 D14
2139 H4	5103 I1
2140 H5	5104 F14
2141 A5	5105 F14
2101-B D2	5106 G14
3101-B D2	5107 G14
3101-D D2	5109 H14
3102-A I7	5110 A6
3102-B I7	7100-B I2
3102-C I7	7102 E8
3103-B I7	7103-B H5
3103-C I7	7104 A6
3104-B I8	7104-B H5
3104-C I8	7104-C H5
3104-D I8	7104-D H5
3105-B I8	7105-B H5
3106-A I8	7106-A H5
3106-B I8	7106-B H5
3106-C I8	7106-C H5
3106-D I8	7106-D H5
3107-A I8	7107-A H5
3107-B I8	7107-B H5
3107-C I8	7107-C H5
3107-D I8	7107-D H5
3108-A I10	7108-A H10
3108-B I10	7108-B H10
3108-C I10	7108-C H10
3108-D I10	7108-D H10
3109-C11	7109-C11
3110-C11	7110-C11
3111-B6	7112-G5
3112-B5	7113-B10
3113-B2	7117-B5
3114-B5	7118-B9
3115-B10	7119-B10
3116-B10	7120-B6
3117-B5	7121-B10
3118-B9	7122-B5
3119-B10	7123-B10
3120-B6	7124-B5
3121-B10	7125-B10
3122-B5	7126-B5
3123-B10	7127-B10
3124-B5	7128-B10
3125-B10	7129-B10
3126-B5	7130-C8
3127-B10	7131-F2
3128-B10	7132-G2
3129-B10	7133-G2
3130-C8	7134-C5
3131-F2	7135-C5
3132-G2	7136-C5
3133-G2	7137-C5
3134-C5	7138-G2
3135-C5	7139-D5
3136-C5	7140-D5
3137-C5	7141-D5
3138-G2	7142-H1
3139-D5	7143-H1
3140-D5	7144-B8
3141-D5	7145-A5
3142-H1	7146-E5
3143-D5	7147-E5
3144-B8	7148-A5
3145-E5	7149-E5
3150-A5	7151-H2
3152-E5	7153-E5
3154-L2	7155-E5
3155-L2	7156-I1
3156-I1	7157-B5
3166-A G5	7158-C5
3166-B G5	7159-C5
3166-C G5	7160-C5
3172-I10	7161-I10

## CIRCUIT DIAGRAM (PART 2)



FLASH TYPE	16Mb	32Mb	64Mb
A(0:19)	A(0:20)	A(0:21)	
ST MACRONIX Light	M29W160	M29W320	M29W641
	MX29LV160	MX29LV320	M29KW016E
			M29KW032E M29KW064E

JUMPER CONFIGURATION					
JUMPER	1 X 16Mb	1 X 32Mb	1 X 64Mb	1X64Mb LIGHT	2 X 16Mb
4202	OFF	OFF	OFF	ON	OFF
4203	OFF	OFF	OFF	OFF	OFF
4204	OFF	OFF	OFF	OFF	OFF
4205	OFF	OFF	OFF	OFF	OFF
4206	OFF	OFF	OFF	OFF	OFF
4207	OFF	OFF	OFF	OFF	OFF
4208	OFF	OFF	OFF	OFF	OFF
4209	OFF	OFF	OFF	OFF	OFF
4210	ON	OFF	OFF	ON	ON
4211	OFF	OFF	OFF	OFF	ON
4212	ON	ON	OFF	ON	ON
4213	ON	ON	OFF	ON	ON
4214	OFF	OFF	ON	OFF	OFF
4215	OFF	OFF	ON	OFF	OFF
4216	OFF	OFF	ON	ON	OFF

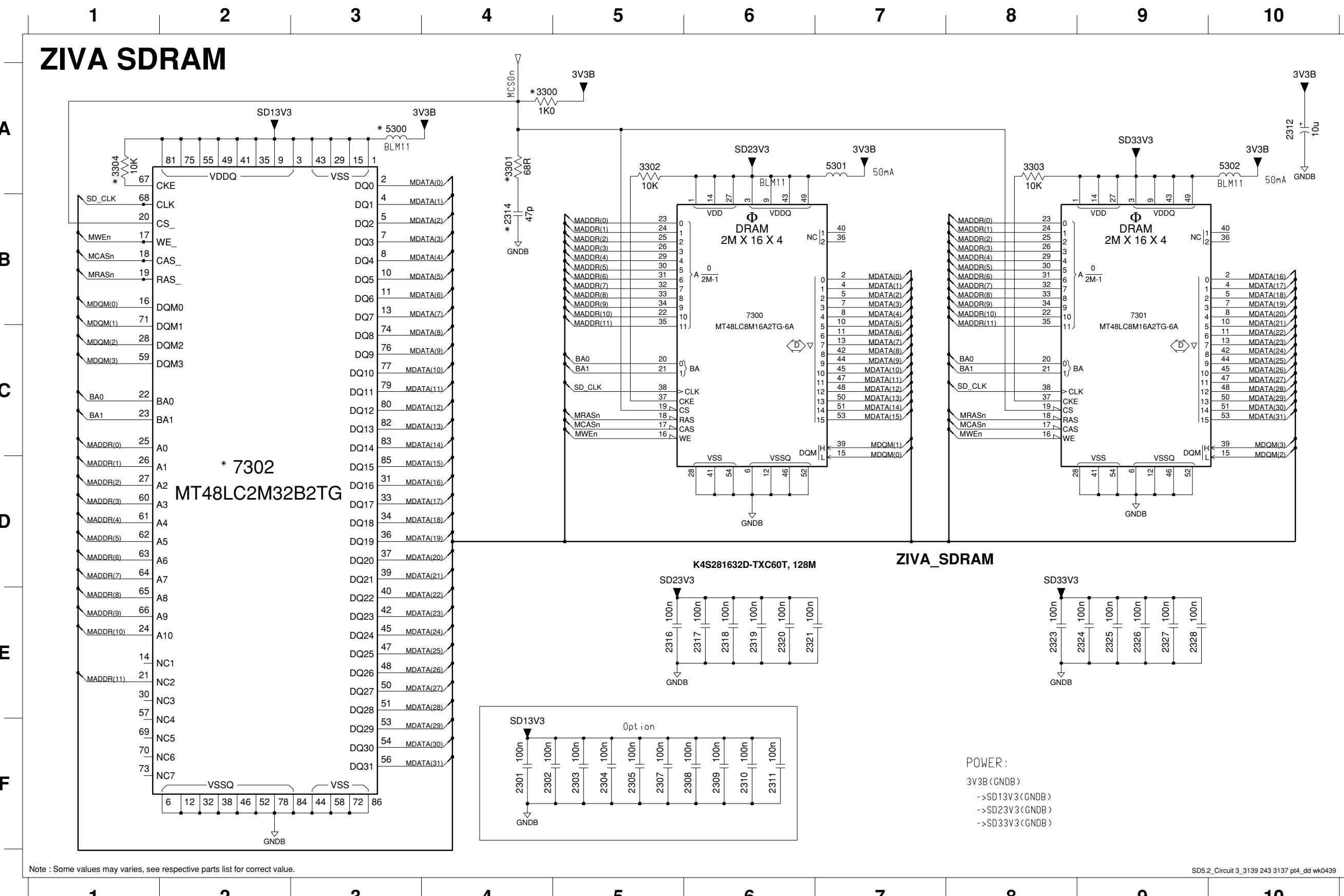
FLASH CS			
FLASH_CSn	UPA21/UPA22	FLASH_CSIn	FLASH_CSIn
1	x	1	1
0	0	0	1
0	1	1	0

1200 C1  
2200 A9  
2201 A3  
2202 A6  
2203 C9  
2204 D9  
2205 D4  
2206 D7  
2207 E2  
2208 E4  
2210 E1  
2211 D1  
3201 D1  
3202 C4  
3203 C7  
4202 C4  
4203 D7  
4204 F7  
4205 F7  
4206 E1  
4207 E1  
4208 F3  
4209 F3  
4210 F3  
4211 F5  
4212 F2  
4213 D2  
4214 D2  
4215 D2  
4216 D4  
4217 F1  
4218 F1  
5200 D1  
6201 C1  
7200 A8  
7201 B3  
7202 B6  
7203 C8  
7204 D8  
7205-A F4  
7205-B F5  
7205-C F6  
7205-D F5  
7206 F1

D

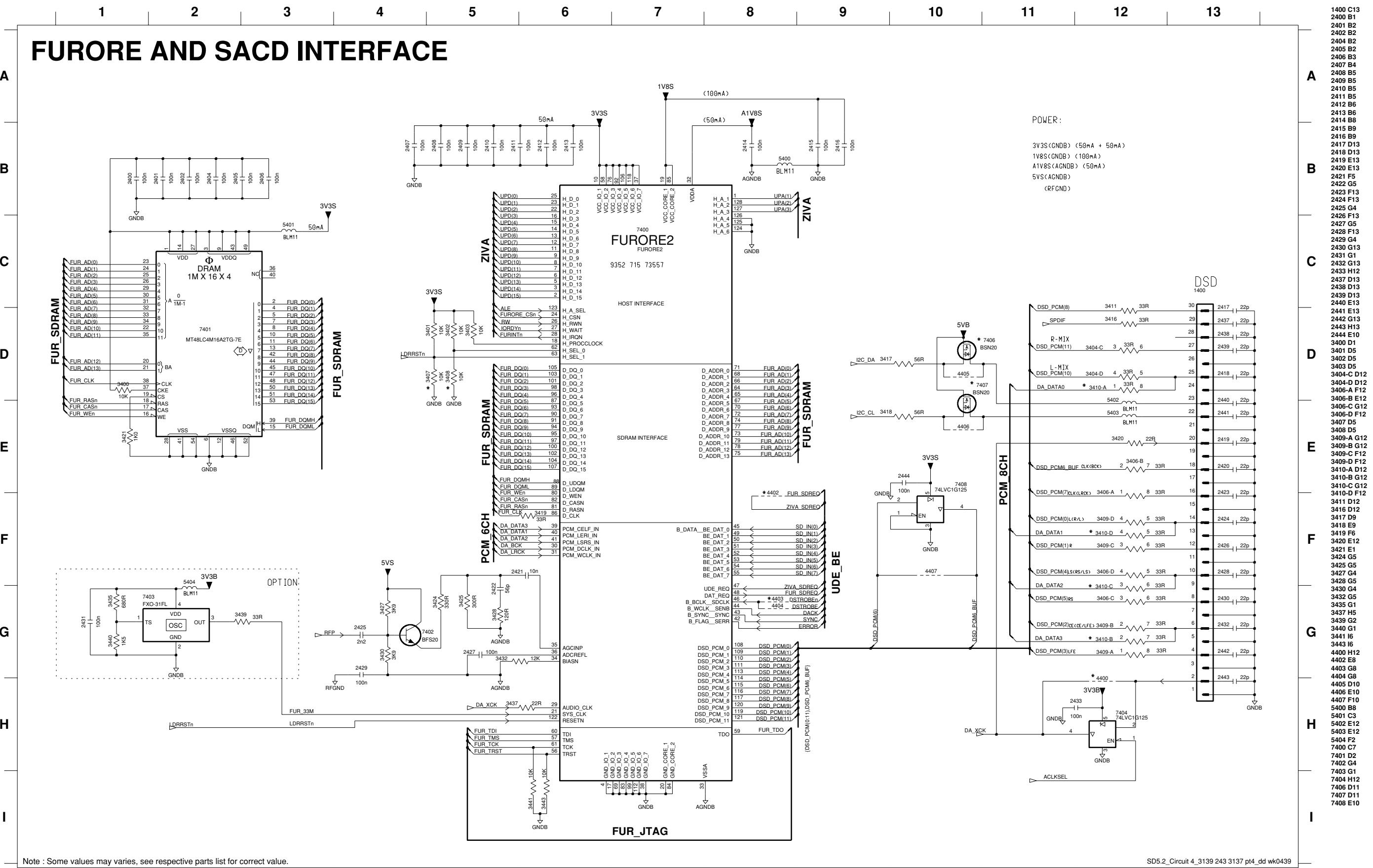
E

F

**CIRCUIT DIAGRAM (PART 3)**

2301 F4  
 2302 F4  
 2303 F5  
 2304 F5  
 2305 F5  
 2307 F5  
 2308 F6  
 2309 F6  
 2310 F6  
 2311 F6  
 2312 A10  
 2314 B4  
 2316 E5  
 2317 E6  
 2318 E6  
 2319 E6  
 2320 E6  
 2321 E6  
 2323 E8  
 2324 E9  
 2325 E9  
 2326 E9  
 2327 E9  
 2328 E9  
 3300 A4  
 3301 A4  
 3302 A5  
 3303 A8  
 3304 A1  
 5300 A3  
 5301 A7  
 5302 A10  
 7300 B6  
 7301 B9  
 7302 D2

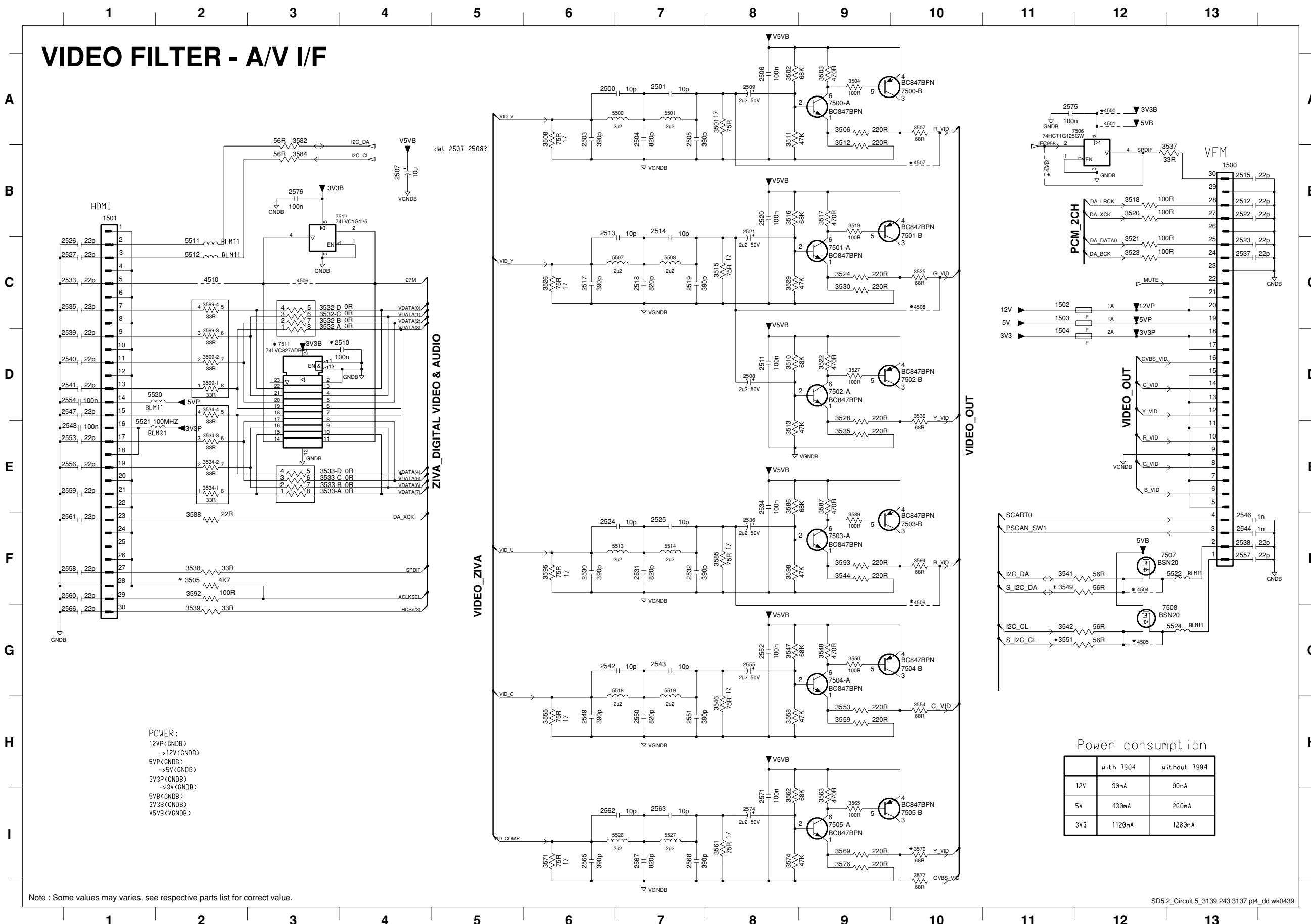
## CIRCUIT DIAGRAM (PART 4)



Note : Some values may varies, see respective parts list for correct value.

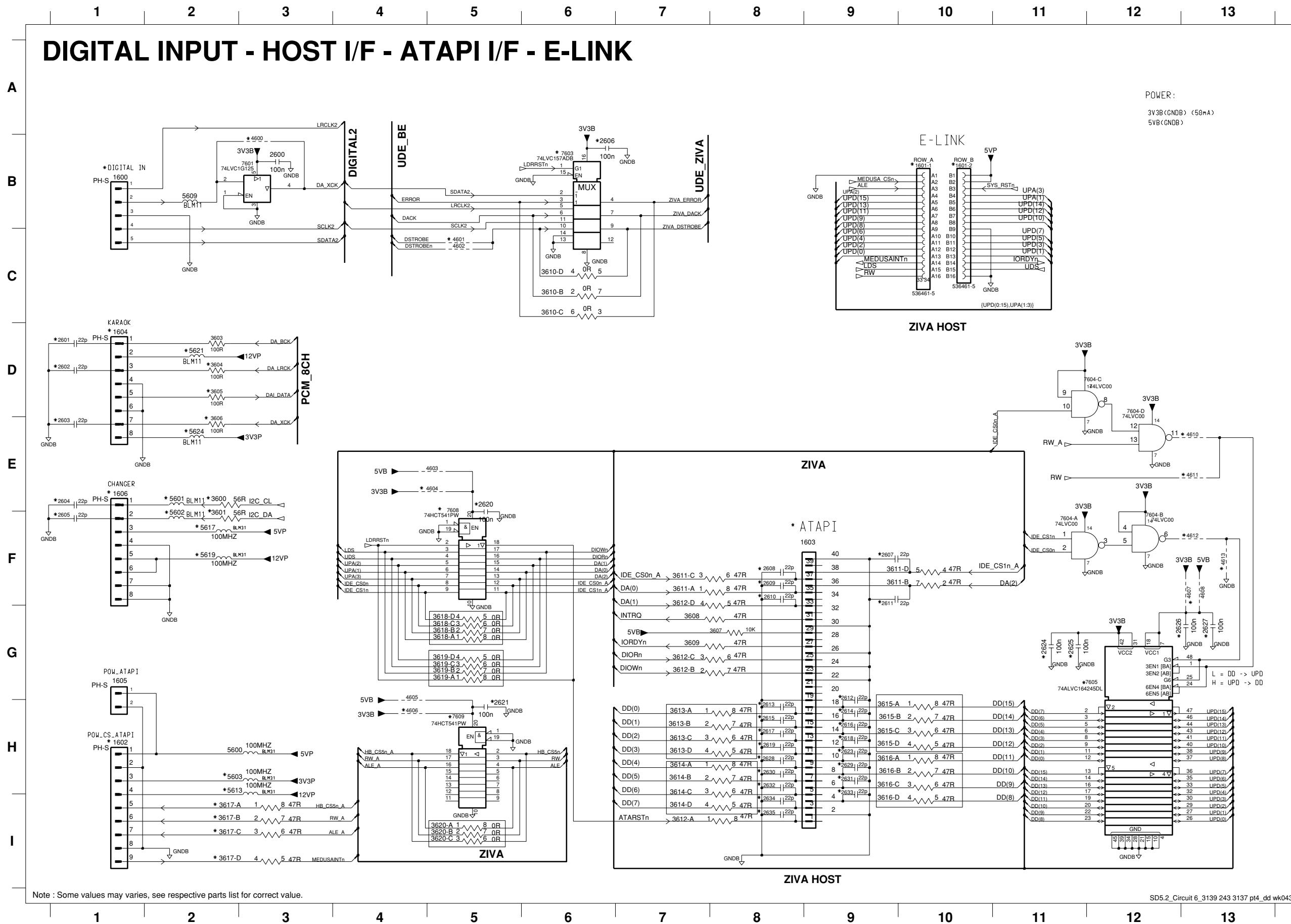
SD5.2\_Circuit 4\_3139 243 3137 pt4\_dd wk0439

## CIRCUIT DIAGRAM (PART 5)



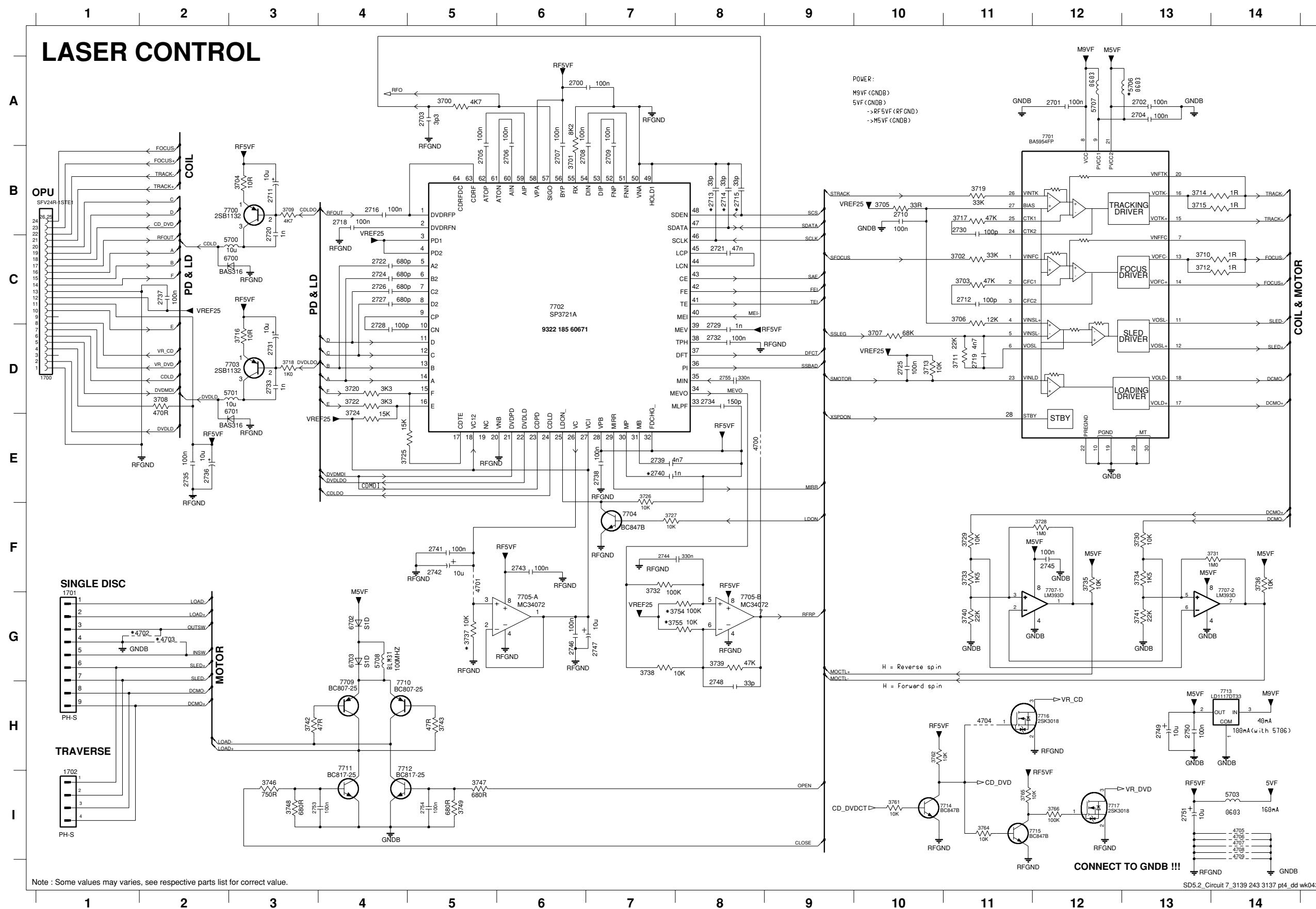
1500_B13	3537_B13
1501_B1	3538_F2
1502_C11	3539_G2
1503_C11	3541_F11
1504_D11	3542_G11
2500_A6	3544_F9
2501_A7	3546_H8
2503_A6	3547_G8
2504_A7	3548_G9
2505_A7	3549_F11
2506_A8	3550_G9
2507_B4	3551_G11
2508_D8	3553_H9
2509_A8	3554_H10
2510_D4	3555_H6
2511_D8	3558_H8
2512_B13	3559_H9
2513_B6	3561_I8
2514_B7	3562_I8
2515_B13	3563_I9
2516_C6	3565_I9
2517_C7	3569_I9
2519_I10	3570_I10
2520_B8	3571_I6
2521_B8	3574_I8
2522_B13	3576_I9
2523_C13	3577_I10
2524_F6	3582_A3
2525_F7	3584_B3
2526_C1	3585_F8
2527_C1	3586_E8
2530_F6	3587_E9
2531_F7	3588_F2
2533_C1	3592_F2
2534_E8	3593_F9
2535_C1	3594_F10
2536_F8	3595_F6
2537_C13	3598_F8
2538_F13	3599_1_D2
2539_D1	3599_2_D2
2540_D1	3599_3_D2
2541_D1	3599_4_C2
2542_G6	4500_A12
2543_G7	4501_A12
2544_F13	4502_B11
2546_F13	4504_F12
2547_D1	4505_G12
2548_E1	4506_C3
2549_H6	4507_B10
2550_H7	4508_C10
2551_H7	4509_F10
2552_G8	4510_G12
2553_E1	4511_G12
2554_D1	4511_A7
2555_G8	4512_G12
2556_E1	4513_G12
2557_F13	4514_G12
2558_E1	4515_G12
2559_E1	4516_G12
2560_F1	4517_G12
2561_F1	4518_G12
2562_G7	4519_G12
2563_I7	4520_G12
2565_I6	4521_E1
2566_G1	4522_F13
2567_I7	4523_G13
2568_I7	4524_G13
2571_I8	4527_I7
2574_I8	4528_A9
2575_A11	4529_B10
2576_B3	4530_C9
2591_A8	4531_B10
3502_A8	4532_D9
3503_A9	4532_B10
3504_A9	4533_A9
3505_F2	4534_B10
3506_A9	4535_A9
3507_A10	4536_B10
3508_A6	4537_B10
3510_D8	4538_B10
3511_A8	4539_B10
3512_A9	4540_F13
3513_E8	4541_G13
3515_C8	4542_G13
3516_B8	4543_G13
3517_B9	4544_G13
3518_B12	4545_G13
3519_B9	4546_G13
3520_B12	4547_G13
3521_C12	4548_G13
3522_D9	4549_G13
3523_C12	4550_G13
3524_C9	4551_G13
3525_C10	4552_G13
3526_C6	4553_G13
3527_D9	4554_G13
3528_B9	4555_G13
3529_C8	4556_G13
3530_C9	4557_G13
3532_A_D3	4558_G13
3532_B_C3	4559_G13
3532_D_C3	4560_G13
3533_A_E3	4561_G13
3533_C_E3	4562_G13
3533_E3	4563_G13
3534_1_E2	4564_G13
3534_2_E2	4565_G13
3534_3_E2	4566_G13
3534_4_D2	4567_G13
3535_E9	4568_G13
3536_D10	4569_G13

## CIRCUIT DIAGRAM (PART 6)

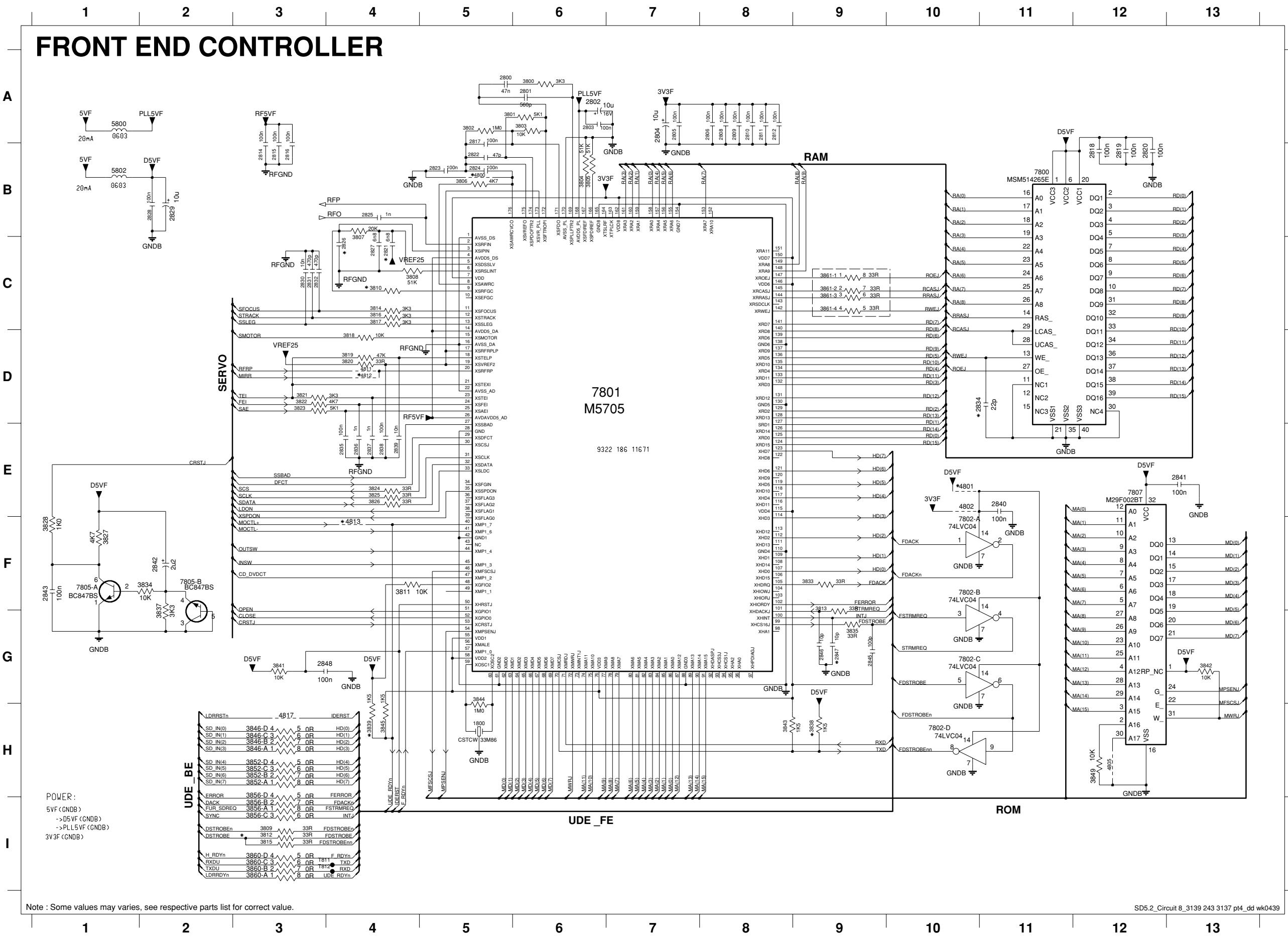


1600 B1	5609 B2
1601-1 B10	5613 H2
1601-2 B10	5617 F2
1602 H1	5619 F2
1603 F8	5621 D2
1604 D1	5624 E2
1605 G1	5626 E1
1606 E1	5623 B6
2600 B3	7604-A F11
2602 D1	7604-C D12
2603 E1	7604-D D12
2604 E1	7605 G11
2605 F1	7608 F5
2606 B6	7609 H5
2607 F9	
2608 F8	
2609 F8	
2610 F8	
2611 F9	
2612 G9	
2613 H8	
2614 H9	
2615 H8	
2616 H9	
2617 H8	
2618 H9	
2619 H8	
2620 E5	
2621 H5	
2623 H9	
2624 G11	
2625 G11	
2626 G12	
2627 G13	
2628 H8	
2630 H9	
2631 H9	
2632 H8	
2633 H9	
2634 I8	
2635 I8	
3600 E2	
3601 F2	
3603 D2	
3604 D2	
3605 D2	
3606 E2	
3607 G8	
3608 G7	
3609 G7	
3610-B C6	
3610-C C6	
3610-D C6	
3611-A F7	
3611-B F9	
3611-C F7	
3611-D F9	
3612-A I7	
3612-B G7	
3612-D F7	
3613-A H7	
3613-B H7	
3613-C H7	
3613-D H7	
3614-A H7	
3614-B H7	
3614-C H7	
3614-D I7	
3615-A H9	
3615-B H9	
3615-C H9	
3615-D H9	
3616-A H9	
3616-B H9	
3616-C H9	
3616-D I9	
3617-A I2	
3617-B I2	
3617-C I2	
3617-D I2	
3618-A G5	
3618-B G5	
3618-C G5	
3618-D G5	
3619-A G5	
3619-B G5	
3619-C G5	
3619-D G5	
3620-A I5	
3620-B I5	
4600 B3	
4601 C5	
4602 C5	
4603 E5	
4604 E5	
4605 G4	
4606 H4	
4607 F13	
4608 F13	
4611 E13	
4612 F13	
4613 F13	
5600 H2	
5601 E2	
5602 F2	

## CIRCUIT DIAGRAM (PART 7)



## CIRCUIT DIAGRAM (PART 8)

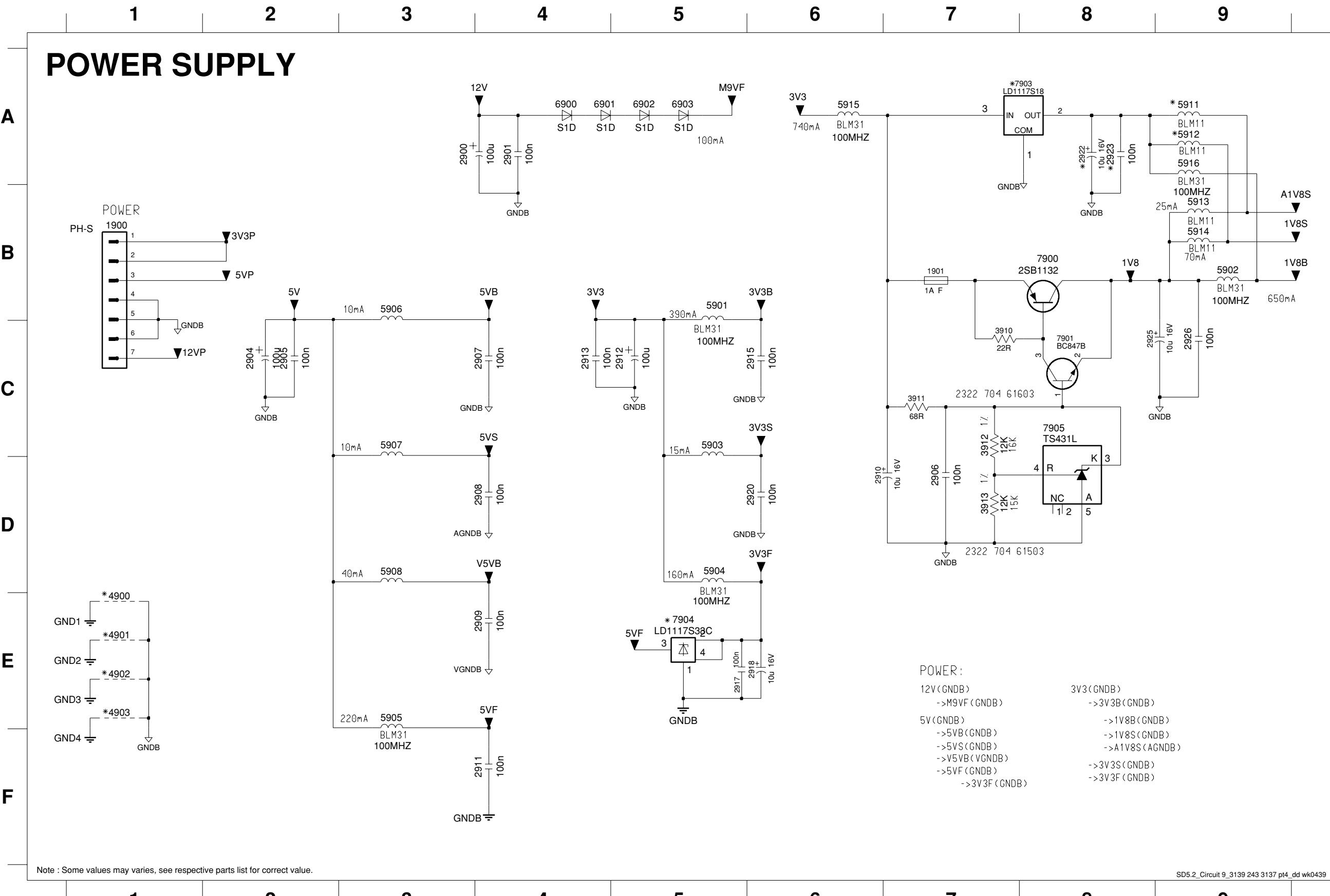


1800 H5	4813 F4
2800 A5	4817 H3
2801 A6	5800 A1
2802 A6	5802 B1
2803 A6	7800 B11
2804 A7	7801 D7
2805 A7	7802-A F10
2806 A8	7802-B F10
2808 A8	7802-C G10
2809 A8	7802-D H10
2810 A8	7805-A F1
2811 A8	7805-B F2
2812 A8	7807 E12
2814 B3	T81113
2815 B3	T81213
2816 B3	
2817 A5	
2818 B12	
2820 B12	
2821 C4	
2822 B5	
2823 B5	
2824 B5	
2825 B4	
2826 C4	
2827 C4	
2828 B2	
2830 C3	
2831 C3	
2832 C3	
2834 D11	
2835 E4	
2836 E4	
2837 E4	
2838 E4	
2839 E4	
2840 E11	
2841 E13	
2842 F2	
2843 F1	
2845 G9	
2846 G9	
2847 G9	
2848 G3	
3800 A6	
3801 A5	
3802 A5	
3803 A6	
3804 B6	
3805 B6	
3806 B5	
3807 C4	
3808 C4	
3809 I3	
3810 C4	
3811 F4	
3812 I3	
3813 F9	
3814 C4	
3815 I3	
3816 C4	
3817 C4	
3818 D4	
3819 D4	
3820 D4	
3821 D3	
3822 D3	
3823 D3	
3824 E4	
3825 E4	
3826 E4	
3827 F1	
3828 F1	
3833 F9	
3834 F2	
3835 G9	
3837 G2	
3838 H9	
3839 H4	
3841 G3	
3842 G13	
3843 H8	
3844 G5	
3845 H4	
3846-A H3	
3846-B H3	
3846-C H3	
3846-D H3	
3849 H12	
3852-A H3	
3852-B H3	
3852-C H3	
3852-D H3	
3856-A I3	
3856-B I3	
3856-C I3	
3856-D I3	
3860-A I3	
3860-B I3	
3860-C I3	
3860-D I3	
3861-C 9	
3861-2 C9	
3861-3 C9	
3861-4 C9	
4800 B5	
4801 E10	
4802 E10	
4805 H12	
4811 D4	
4812 D4	

Note : Some values may varies, see respective parts list for correct value.

SD5.2\_Circuit 8\_3139 243 3137 pt4\_dd wk0439

## CIRCUIT DIAGRAM (PART 9)

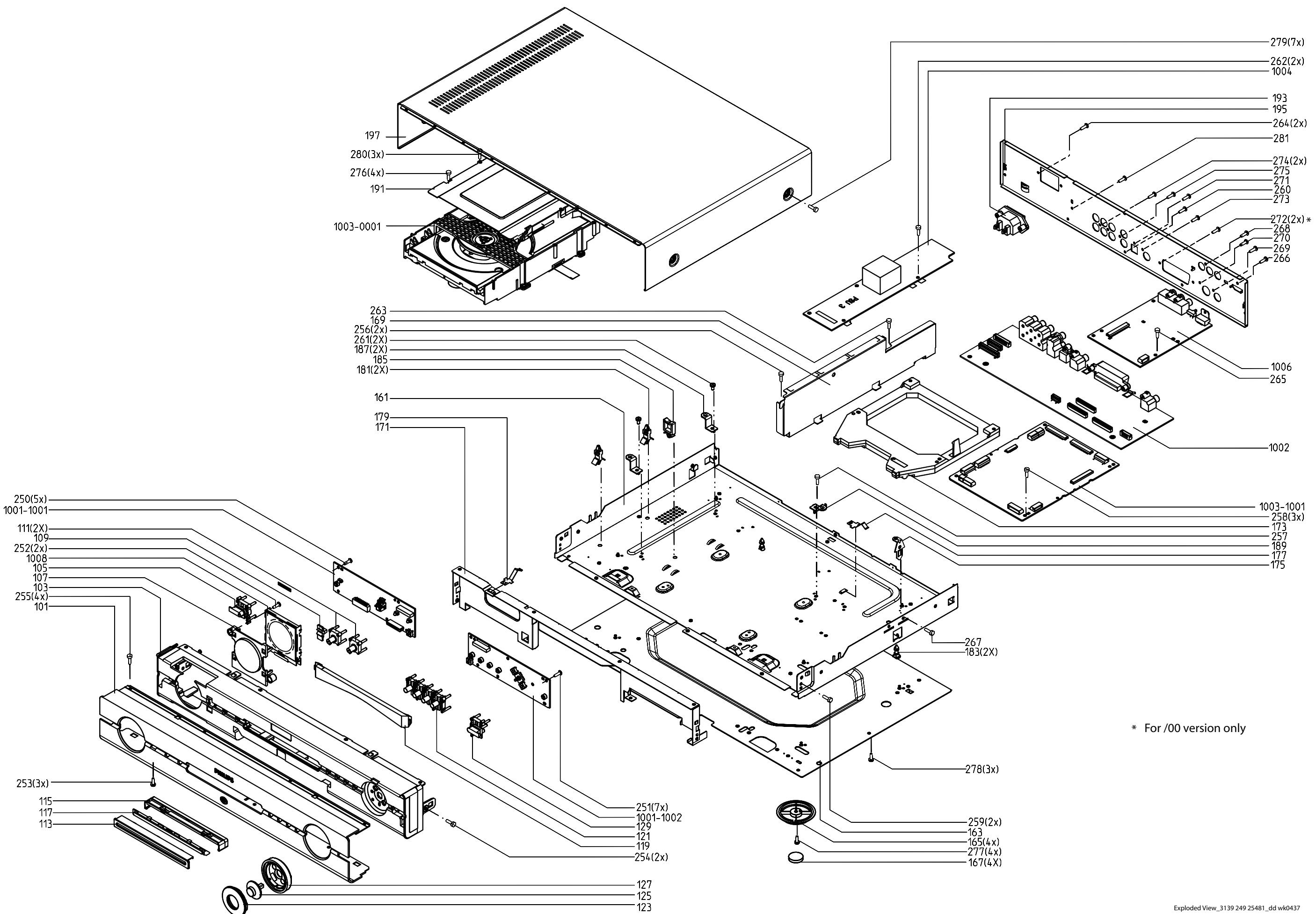


1900 B1  
1901 B7  
2900 A3  
2901 A4  
2904 C2  
2905 C2  
2906 D7  
2907 C4  
2908 D4  
2909 E4  
2910 D6  
2911 F4  
2912 C5  
2913 C4  
2915 C6  
2917 E5  
2918 E6  
2920 D6  
2922 A8  
2923 A8  
2925 C8  
2926 C9  
3910 C7  
3912 C7  
3913 D7  
4900 E1  
4901 E1  
4902 E1  
4903 E1  
5901 B5  
5902 B9  
5903 C5  
5904 D5  
5905 E3  
5906 B3  
5907 C3  
5908 D3  
5911 A9  
5912 A9  
5913 B9  
5914 B9  
5915 A6  
5916 A9  
6900 A4  
6901 A4  
6902 A5  
6903 A5  
7900 B8  
7901 C8  
7903 A8  
7904 E5  
7905 C8

Note : Some values may varies, see respective parts list for correct value.

SD5.2\_Circuit 9\_3139 243 3137 pt4\_dd wk0439



SET MECHANICAL EXPLODED VIEW

***MECHANICAL & ACCESSORIES PARTS LIST - MAIN UNIT***

0101	3139 241 60092	PANEL ORNAMENTAL AL
0103	3139 244 05552	CABINET FRONT
0105	3139 244 05541	BUTTON PWR STDBY CHROME
0107	3139 244 05531	WINDOW DISPLAY
0109	3139 244 05521	WINDOW IR
0111	3139 244 05512	CAP AUD DIR/SOUND CHROME
0113	3139 241 60081	COVER TRAY ORNAMENTAL AL
0115	3139 244 05502	COVER TRAY
0117	3139 244 05491	LIGHT BAR DVD TRAY
0121	3139 244 05471	BUTTON SET CONTROL
0123	3139 244 05461	COVER JOYSTICK FUNCTION
0125	3139 244 05452	JOYSTICK FUNCTION
0127	3139 244 05441	LIGHT RING FUNCTION
0129	3139 244 05431	BUTTON MENU
0165	9965 000 21117	PLASTIC FOOT
0167	3103 304 74201	RUBBER FEET
0173	3139 244 05561	BRACKET LOADER
0177	3139 111 01470	SPRING GROUNDING
0179	3139 111 01470	SPRING GROUNDING
0181	2422 015 19105	SPACER LOCKING
0183	3139 240 40061	SPACER - 8MM
0185	2422 015 16892	SADDLE WIRE
0193	2422 030 00408	MAINS SOCKET
0333	3139 248 72131	REMOTE CONTROL
0336	2422 070 00005 △	MAINS CORD
0338	2422 076 00304	CABLE CINCH 1M5 YE/RD/WH /69
0340	3103 308 92610	CABLE AUDIO 2X2RCA MALE /69
0341	4822 321 61579	VIDEO-CABLE /69
0342	2422 076 00468 △	CABLE SCART 1M1 SCART 21P /00
1001	3139 248 83591	PCBAS FRONT
1002	3139 248 83491	PCBAS AV /00
1002	3139 248 83481	PCBAS AV /69
1004	3139 248 71951 △	MODULE PSU BIL PERF2003 WR HE
1006	3139 248 82981	PCBAS PSCAN HDMI
1008	3139 247 10921	LCM MODULE
8102	3139 111 04261	FFC FOIL 30P/220/30P BD 1MMP
8103	3139 241 00251	FFC FOIL 30P/080/30P BD 1MMP
8104	3139 241 00251	FFC FOIL 30P/080/30P BD 1MMP

Note : Only the parts mentioned in this list are normal service spare parts.

***SCREW LISTS - MAIN UNIT***

250	D3 x 10
251	D3 x 10
252	D3 x 10
253	M3 x 6
254	M3 x 6
255	M3 x 6
256	M3 x 6
257	M3 x 6
258	M3 x 6
259	M3 x 6
260	M3 x 6
261	M3 x 6
262	M3 x 6
263	M3 x 6
264	D3 x 10
265	M3 x 6
266	M3 x 6
267	M3 x 6
268	D3 x 10
269	D3 x 10
270	D3 x 10
271	D3 x 10
272	D3 x 10 /00
273	D3 x 10
274	D3 x 10
275	D3 x 10
276	M3 x 18
277	M3 x 6
278	M3 x 6
279	M3 x 6
280	D3 x 10
281	M3 x 6

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# PSU BOARD

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## ***(For Information only)***

It is not recommended for component repair on this board but to replace the board when it becomes defective.  
Therefore no service parts list is published in this chapter.

The only service part available for replacement is:  
Module PSU BIL PERF2003 WR HE ..... 3139 248 71951

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## PSU BOARD - CIRCUIT DIAGRAM

