

VAE8015

VAE8020

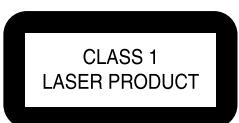
# Service

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# Service Manual



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# PHILIPS

# 1. Technical Specifications

## VAE8010 functionality:

- Loading of 8 cm and 12 cm discs by a motorized tray
- Disc type recognition and in case of a DVD+RW disc laser power calibration
- Servo control for disc rotation, sledge movements, tilt, focus and actuator position
- EFM+ encoding / decoding for DVD, and EFM decoding for CD
- Writes and read DVD+RW discs and reads DVD, CD and CD-R/RW discs
- Linking control, header insertion and sector number updating at record
- Interfacing to the MPEG back-end (S2B) for control and (I2S and V4) for data
- The back-end has to provide MPEG data processing, data buffering, construction
- of logical format for Lead-in, Data area and Lead-out part of the DVD+RW dis

## 2. Safety Instructions, Warnings and Notes

### 2.1 Safety Instructions

#### 2.1.1 General Safety

Safety regulations require that during a repair:

- Connect the unit to the mains via an isolation transformer.
- Replace safety components, indicated by the symbol **▲**, only by components identical to the original ones. Any other component substitution (other than original type) may increase risk of fire or electrical shock hazard.

Safety regulations require that after a repair, you must return the unit in its original condition. Pay, in particular, attention to the following points:

- Route the wires/cables correctly, and fix them with the mounted cable clamps.
- Check the insulation of the mains lead for external damage.
- Check the electrical DC resistance between the mains plug and the secondary side:
  1. Unplug the mains cord, and connect a wire between the two pins of the mains plug.
  2. Set the mains switch to the 'on' position (keep the mains cord unplugged!).
  3. Measure the resistance value between the mains plug and the front panel, controls, and chassis bottom.
  4. Repair or correct unit when the resistance measurement is less than  $1 \text{ M}\Omega$ .
  5. Verify this, before you return the unit to the customer/user (ref. UL-standard no. 1492).
  6. Switch the unit 'off', and remove the wire between the two pins of the mains plug.

#### 2.1.2 Laser Safety

This unit employs a laser. Only qualified service personnel may remove the cover, or attempt to service this device (due to possible eye injury).

#### Laser Device Unit

Type	: Semiconductor laser GaAlAs
Wavelength	: 650 nm (DVD) : 780 nm (VCD/CD)
Output Power	: 20 mW (DVD+RW writing) : 0.8 mW (DVD reading) : 0.3 mW (VCD/CD reading)
Beam divergence	: 60 degree

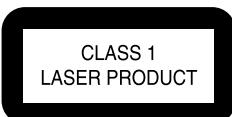


Figure 2-1 Class 1 Laser Product

**Note:** Use of controls or adjustments or performance of procedure other than those specified herein, may result in hazardous radiation exposure. Avoid direct exposure to beam.

### 2.2 Warnings

#### 2.2.1 General

- All ICs and many other semiconductors are susceptible to electrostatic discharges (ESD, symbol **▲**). Careless handling during repair can reduce life drastically. Make sure that, during repair, you are at the same potential as the mass of the set by a wristband with resistance. Keep components and tools at this same potential. Available ESD protection equipment:
  - Complete kit ESD3 (small tablemat, wristband, connection box, extension cable and earth cable) 4822 310 10671.
  - Wristband tester 4822 344 13999.
- Be careful during measurements in the live voltage section. The primary side of the power supply (pos. 1005), including the heatsink, carries live mains voltage when you connect the player to the mains (even when the player is 'off!'). It is possible to touch copper tracks and/or components in this unshielded primary area, when you service the player. Service personnel must take precautions to prevent touching this area or components in this area. A 'lightning stroke' and a stripe-marked printing on the printed wiring board, indicate the primary side of the power supply.
- Never replace modules, or components, while the unit is 'on'.

#### 2.2.2 Laser

- The use of optical instruments with this product, will increase eye hazard.
- Only qualified service personnel may remove the cover or attempt to service this device, due to possible eye injury.
- Repair handling should take place as much as possible with a disc loaded inside the player.
- Text below is placed inside the unit, on the laser cover shield:

CAUTION VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN AVOID EXPOSURE TO BEAM ADVARSEL. SYNLIG OG USYNLIG LASERSTRÅLING VED ÅBNING UNDGÅ UDSETTELSE FOR STRÅLING ADVARSEL. SYNLIG OG USYNLIG LASERSTRÅLING NÄR DEKSEL ÄPNES UNNGÅ EKSPOSERING FÖR STRÅLEN VARO! AVATT AV ESSÄ OLET ALTTIINA NÄKYVÄLLE JA NÄKYMÄTTÖMÄLLE LASER SÄTELYILLE. ÄLÄ KATSO SÄTEESEN VORSICHT! SICHTBARE UND UNSICHTBARE LASERSTRÄHLUNG WENN ABDECKUNG GEÖFFNET NICHT DEM STRAHL AUSSETZEN DANGER VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN AVOID DIRECT EXPOSURE TO BEAM ATTENTION RAYONNEMENT LASER VISIBLE ET INVISIBLE EN CAS D'OUVERTURE EXPOSITION DANGEREUSE AU FAISCEAU
--

Figure 2-2 Warning text

### 3. Directions For Use

Not applicable

## 4. Mechanical Instructions

Index of this chapter:

1. General
2. Disassembly
3. Re-assembly

**Note:** Figures below can deviate slightly from the actual situation, due to the different set executions.

### 4.1 General

- Follow the disassemble instructions in described order.
- Do not place the unit with its PWB on a hard surface (e.g. table), as it could damage the components on it. Always place something soft (a towel or foam cushion) under it.
- Never touch the lens of the laser.
- Take sufficient ESD measures during (dis)assembly.

### 4.2 Disassembly

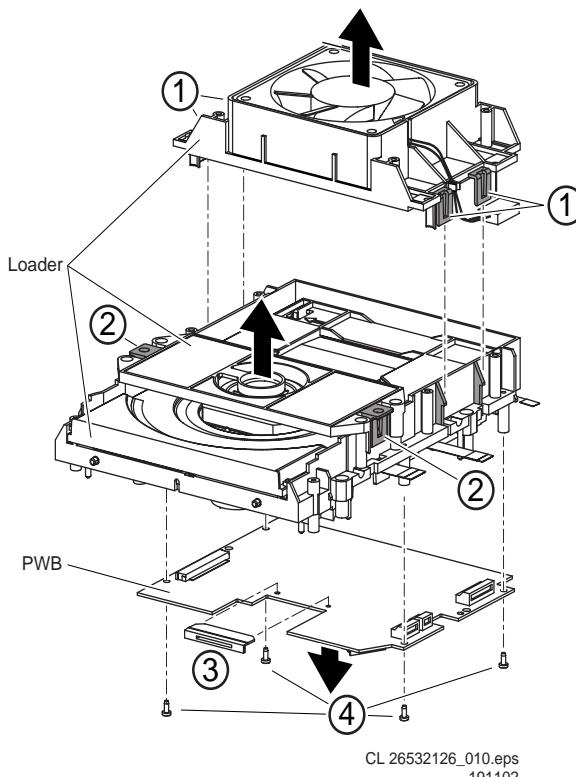


Figure 4-1 Basic Engine disassembly (part 1)

You can divide the Basic Engine into the following parts:

1. Loader (fan, clamp, and tray assy).
2. PWB (or 'mono board').
3. DVD-Module (OPU, turntable motor, and sledge-motor assy).

#### 4.2.1 Loader

1. Disconnect the 2-wire fan cable from the PWB.
2. Remove the fan assy, by releasing the four side clamps [1] while moving it upwards.
3. Remove the clamp assy, by releasing the two side clamps [2] while moving it upwards.

#### 4.2.2 PWB

1. Flip the module 180 degrees, so you can access the PWB.
2. Disconnect the four flex foils from the PWB connectors (1100, 1300, 1302, and 1303) at the component side. For the flex foil on connector 1100, you first must remove the cable clamp [3]. The easiest way to do this is to push down the two fixation pins of the clamp (via the holes in the PWB) by means of a pencil or small screwdriver.
3. Disconnect the remaining cables (tray- and fan-motor cable) at the solder side of the PWB
4. Remove the four screws (Torx 8) that hold the PWB [4].
5. Now you can remove the board.

#### 4.2.3 DVD-M

**Caution:** Never try to align the DVD-Module! !! Only the factory can do this properly. Service engineers are only allowed to exchange the sledge motor assy.

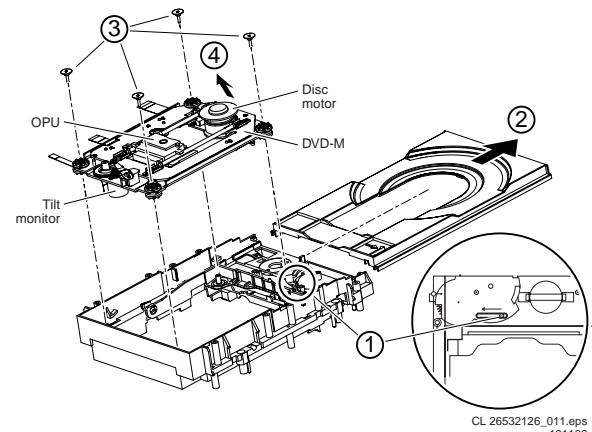


Figure 4-2 Basic Engine disassembly (part 2)

1. Slide the 'tray pin' in the direction of the arrow [1], in order to release the disc tray.
2. Flip the module 180 degrees and pull out the tray [2]. Now you can access the DVD-Module.
3. Remove the four screws [3] with a Torx 6 screwdriver, and lift the DVD-M upwards [4] at the side of the disc-motor. It hinges in the bracket at the side of the tilt-motor.

#### 4.2.4 Sledge-motor Assy

**Caution:** Never try to align the DVD-Module!!! Only the factory can do this properly. Service engineers are only allowed to exchange the sledge motor assy.

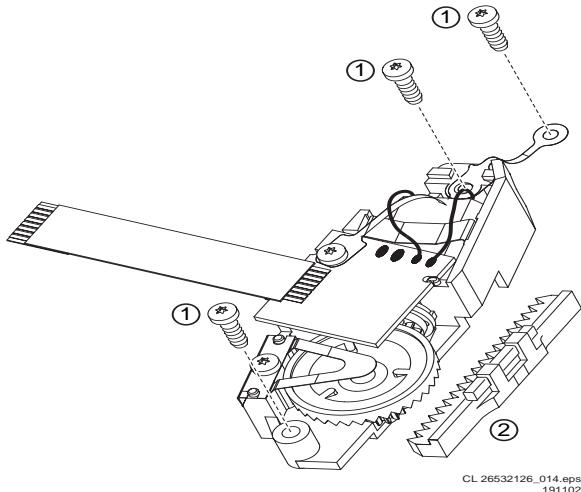


Figure 4-3 Sledge-motor assy

1. Place the DVD-Module, with the laser facing downwards, on a soft surface.
2. Remove the three screws [1] that hold the sledge-motor assy, and lift the assy upwards. You can replace it now.
3. If necessary, it is now also possible to replace the sledge-rack [2] that is hinged in the sledge assy.

### 4.3 Re-assembly

To re-assemble the module, do all processes in reverse order.

Be sure to:

- **Sledge-motor assy:** Mesh the teeth of the sledge motor and sledge rack properly, during mounting of the sledge-motor assy.
- DVD-M: Point the laser up (towards the tray), when you mount the DVD-M in the bracket.
- **Complete module:** Place all wires/cables in their original positions

## 5. Service Modes, Error Codes and Fault Finding

Index of this chapter:

1. General
2. Start-up Measurements
3. Diagnostic Software
4. Nuclei Error Codes
5. Fault Finding

### 5.1 General

- Impedance of measuring-equipment should be > 1 MOhm.
- For testing the Basic Engine, connect it to a DVD-recorder of the DVDR1000, 900, or 800 series.
- Most tests are done by software commands. Together with the software command you will find a Ref.# nbr. This is the number of the diagnostic nucleus used for this test. You can find information that is more detailed in the chapter 'Diagnostic Nuclei'.
- Levels: Most measurements are digital measurements. The signal levels specification in this document are defined as follows:
  - low < 0.3V
  - high > 3.0V
  - LOW < 0.4V
  - HIGH > 4.5V

### 5.2 Start-up Measurements

#### 5.2.1 Power Supply Check

Table 5-1 Table of supply input voltages.

Signal	Test point	Description	Specifi-cations	Toler-ance	Unit
+3V3	I007	Input of supply	+3.3	1%	V
+5	I008	Input of supply	+5	1%	V
+4V6	I010	Input of supply	+4.6	1%	V
-5	I011	Input of supply	-5	1%	V
+12	I012	Input of supply	+2	1%	V
GND	I015, I016	Input of supply			

Table 5-2 Table of derived supply voltages.

Signal	Test point	Description	Specifi-cations	Toler-ance	Unit
D3V3	I006	Digital 3V3	+3.3	2%	V
A3V3	I007	Analogue 3V3	+3.3	2%	V
+5V	I008	Analogue 5V	+5	2%	V
D5V	I009	Digital 5V	+5	2%	V
+3V3E	F001	OPU 3V3	+3.3	2%	V
-5	I011	Neg. voltage	-5	2%	V
+12	I012	12V	+12	2%	V
Vbias	I014	Bias voltage	-1.7	2%	V
GND	I017, I018	Input of supply	0	0 %	V

The module operates in power 'off' and power 'on' only. There is no standby mode at module level. In power 'off', the module does not respond to any communication or signal.

Before starting the measurement, connect the power supply to the mono board via connector 1000, and the PC interface cable to the Service Interface connector of the 'test recorder'.

#### 5.2.2 Oscillator Check

Table 5-3 Table of clock signals.

Signal	Test point	Description	Spec.	Toler-ance	Unit
OSCOUT	I219	Ref. Clock MACE	8.483	+/- 70 kHz	MHz
CROUT	I443	Ref. Clock HDR65	8.501	+/- 70 kHz	MHz
PSEN	I223	OEN Flash ROM	5.324	+/- 70 kHz	MHz
RA/FO/SL	I326/I334/I343	Servo clock	2.120	+/- 20 kHz	MHz

On the mono board, there are two external oscillators (OSCOUT and CROUT), which are the reference for all clock signals derived in several ICs.

To check whether the program (in the MACE microprocessor) is running after power 'on', you can monitor the PSEN (OEN of Flash ROM) on I223 (see test point overview in chapter 6). You can measure the Servo clocks at I326 (RAdial), I334 (FOCUS), and I343 (SLedge).

### 5.3 Diagnostic Software

Due to the complexity of a DVD recorder, the time to find a defect in the recorder can become long. To reduce this time, the recorder has been equipped with Diagnostic and Service software (DS). The DS offers functionality to diagnose the DVDR hardware and tests the following:

- Interconnections between components.
- Accessibility of components.
- Functionality of the audio and video paths.

This is also valid for the Basic Engine.

One can access this functionality via several interfaces:

1. End user/Dealer script interface.
2. Player script interface.
3. Menu and command interface.

This part describes all interfaces from the outside world to the diagnostic software, how to use these interfaces, and how to access them.

First some definitions:

- **Diagnostic Nucleus.** Part of the Diagnostic Software. Each nucleus contains an atomic and software independent diagnostic test, testing a functional part of the DVD player hardware on component level.
- **Script.** Part of the Diagnostic Software. Each script contains a sequence of Diagnostic Nuclei to be executed.
- **Service PC.** PC used by a service or repair person to communicate with the Diagnostic Software in the DVD player.

#### 5.3.1 End User/Dealer Script Interface

The End user/Dealer script interface gives a diagnosis on a stand-alone DVD recorder; no other equipment is needed. During this mode, a number of hardware tests (nuclei) are automatically executed to check if the recorder is faulty. The diagnosis is simply a 'fail' or 'pass' message. If the message 'FAIL' appears on the display, there is apparently a failure in the recorder. If the message 'PASS' appears, the nuclei in this mode have been executed successfully. There can be still a failure in the recorder because the nuclei in this mode do not cover the complete functionality of the recorder.

**Note:** As this mode is meant for a complete DVD Recorder, and does not add much for testing the Basic Engine, reference is made to the appropriate DVD Recorder Service Manual for a detailed description:

- DVDR1000: 3122 785 11600
- DVDR990: 3122 785 12430
- DVDR98x: 3122 785 11970
- DVDR8xx: 3122 785 12200

### 5.3.2 Player Script Interface

The Player script will give the opportunity to perform a test that will determine which of the DVD recorder's modules are faulty, to read the error log and to perform an endurance loop test. To successfully perform the tests, connect the DVD recorder to a TV set. To be able to check results of certain nuclei, the player script expects some interaction of the user (i.e. to approve a test picture or a test sound). Some nuclei (e.g. nuclei that test functionality of the DVDR module) require that a DVD+RW disc is inserted. Only tests within the scope of the diagnostic software will be executed hence only faults within this scope can be detected.

See note above (paragraph 5.3.1).

### 5.3.3 Menu and Command Interface

Each nucleus contains an atomic and independent diagnostic test, testing a functional part of the DVD player hardware on component level. Each Nucleus returns a result message to its caller. Some tests (e.g. generating a colour bar) can only return an "OK" result. Internal communication will be done via a uniform interface between the diagnostic Engine, Scripts, and the Diagnostic Nuclei.

The Diagnostic Engine can only operate if a certain (minimal) set of hardware is functioning properly. To test this set of hardware, a set of basic diagnostic nuclei is embedded in the DVD player. Each basic diagnostic nucleus will only test that part of the hardware which is required for execution of the diagnostic Engine, e.g. a RAM test will only test that part of RAM that is used by the diagnostic engine. After the Diagnostic Engine is operational, it is possible to do a full RAM diagnostic. All basic diagnostic nuclei start with prefix 'Basic'. In the overview, each Diagnostic Nucleus consists of a reference number, a reference name, and remarks. Reference number and name are coupled and one of them is enough for unique identification.

#### Nuclei Numeration

Each nucleus has a unique number of four digits. This number is the input of the command mode.

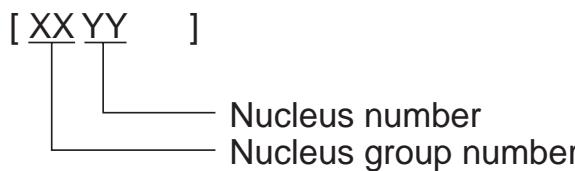


Figure 5-1 Nucleus code

The following groups are defined:

Table 5-4 Nucleus groups

Group number	Group name
0	Basic / Scripts
1	Host decoder
2	Audio / Video encoder (DVDR only)
3	VSM (DVDR only)

Group number	Group name
4	NVRAM
5	Front Panel
6	Basic Engine
7	Analogue board (DVDR only)
8	DVIO (DVDR only)
9	Loop nuclei (DVDR only)
10	Library sub nuclei (I2C nuclei)
11	User interface
12	Eurore (SACD only)
13	DAC (SACD only)
14	Miscellaneous

For testing the Basic Engine, group number 6 is defined

#### Error handling

Each nucleus returns an error code. This code contains six numerals, which means:

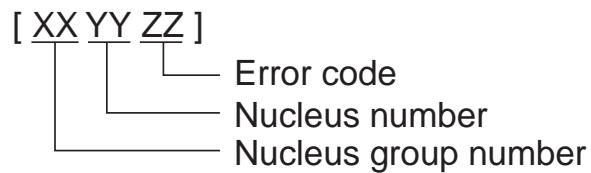


Figure 5-2 Error code

The nucleus group numbers and nucleus numbers are the same as above.

#### Hardware required

- Service PC.
- One free COM port on the Service PC.
- Special cable to connect DVD recorder to Service PC.

The service PC must have a terminal emulation program (e.g. OS2 Warp Terminal, ProComm, or HyperTerminal) installed and must have a free COM port (e.g. COM1).

Activate the terminal emulation program and check that the port settings for the free COM port are:

- 19200 bps,
- 8 data bits,
- No parity,
- 1 stop bit,
- No flow control.

Connect the free COM port via a special cable to the RS232 port of the DVD recorder. This special cable will also connect the test pin, which is available on the connector, to ground (i.e. activate test pin). Code number of PC interface cable: 3122 785 90017.

#### Command Mode Interface

##### Activation

Connect the recorder to the mains. The following text will appear on the screen of the terminal (program):

```
DVD Video Recorder Diagnostic Software version 48
Basic SDRAM Data bus test passed
Basic SDRAM Address bus test passed
Basic SDRAM Device test passed
(M) enu, (C) ommand or (S) 2B-interface? [M] : @ c ↵
DD:>
```

CL 16532095\_073.eps  
150801

Figure 5-3 Opening screen for Command Mode

The first line indicates that the Diagnostic software has been activated and contains the version number. The next lines are the successful result of the SDRAM interconnection test and the basic SDRAM test. The last line allows the user to choose between the three possible interface forms.

If pressing 'C' has made a choice for Command Interface, the prompt ("DD>") will appear. The diagnostic software is now ready to receive commands. The commands that can be given are the numbers of the nuclei.

#### *Command Overview*

We provide an overview of the nuclei and their numbers. This overview is preliminary and subject to modifications.

**Table 5-5 Nuclei overview**

Ref. #	Function name	Description
600	DS_BE_S2B_Pass	It switches the RS232 port and the S2B port in pass-through mode. This means that the player hangs. The only way to exit this nucleus is via a power off of the player
601	DS_BE_S2B_Engine	It checks the S2B interface with the Basic Engine by sending an 'echo' command
602	DS_BE_Version	It returns the version number of the Basic Engine
603	DS_BE_Reset	It resets the Basic Engine
604	DS_BE_FocusOn	It puts the laser of the basic engine into focus (focus loop)
605	DS_BE_FocusOff	It switches the focus loop off
606	DS_BE_DiscMotorOn	It switches the disk motor (= spindle motor) on
607	DS_BE_DiscMotorOff	It switches the disk motor (= spindle motor) off
608	DS_BE_RadialOn	It closes the radial loop
609	DS_BE_RadialOff	It opens the radial loop
615	DS_BE_TrayIn	It closes the disc tray
616	DS_BE_TrayOut	It opens the disc tray
617	DS_BE_WriteRead	It writes data to the BE which is stored on a DVD disc and read it back from the DVD disc. This is only done when the result of the self-test contains no errors.
618	DS_BE_WriteReadEndlessLoop	It writes data to the BE which is stored on a DVD disc and read it back from the DVD disc, while repeating in an endless loop. This is only done when the result of the self-test contains no errors. Errors are stored in NVRAM.
625	DS_BE_SledgeMotorSlow	It moves the sledge full stroke several times slow enough to allow visual inspection of unhampered movement
626	DS_BE_Tilt	It tests the tilt mechanism control loop, or allow its proper functioning to be measured
627	DS_BE_ReadEeprom	It reads one data byte from the EEPROM
629	DS_BE_OptimiseJitter	It performs jitter optimisation

Ref. #	Function name	Description
630	DS_BE_RadialATLSCalibration	It allows the radial loop to be calibrated.
631	DS_BE_GetStatisticsInfo	It retrieves the statistical information from the Basic Engine
632	DS_BE_ResetStatisticsInfo	It erases the statistical information
633	DS_BE_ReadErrorlog	It retrieves the error log from the Basic Engine
634	DS_BE_ResetErrorlog	It erases the fatal error log and the cumulative error log
638	DS_BE_GetSelfTestResult	It retrieves the result of the Self Test of the Basic engine, which is executed during power-on.
639	DS_BE_RadialInit	It allows the radial initialisation
640	DS_BE_GetOPUInfo	It retrieves the OPU number from the EEPROM of the Basic engine.
641	DS_BE_WriteReadPlusR	It writes data to the BE which is stored on a DVD disc and read it back from the DVD disc. This is only done when the result of the self-test contains no errors.
642	DS_BE_WriteReadPlusREndlessLoop	It writes data to the BE which is stored on a DVD disc and read it back from the DVD disc, while repeating in an endless loop. This is only done when the result of the self-test contains no errors. Errors are stored in NVRAM.

#### **Menu Mode Interface**

##### *Activation*

Connect the recorder to the mains. The following text will appear on the screen of the terminal (program):

```
DVD Video Recorder Diagnostic Software version 48
Basic SDRAM Data bus test passed
Basic SDRAM Address bus test passed
Basic SDRAM Device test passed

(M) menu, (C) ommand or (S) 2B-interface? [M] : @ M ↵

Main Menu

1. Digital Board      ->
2. Analogue Board    ->
3. Front Panel        ->
4. Basic Engine       ->
5. DVIO               ->
6. Progressive Scan Board ->
7. Loop tests         ->
8. Log                ->
9. Scripts            ->

Select>
```

CL 16532095\_074.eps  
150801

**Figure 5-4 Opening screen for Menu Mode**

The first line indicates that the Diagnostic software has been activated and contains the version number. The next lines are the successful result of the SDRAM interconnection test and the basic SDRAM test. The last line allows the user to choose between the three possible interface forms.

If pressing 'M' has made a choice for Menu Interface, the Main Menu will appear.

*Menu Structure*

1. Digital Board
2. Analogue Board
3. Front Panel
- 4. Basic Engine**
  1. Reset
  2. S2B Pass-through
  3. S2B Echo
  4. Focus On
  5. Focus Off
  6. Version
  7. Self Test
  8. Get Self Test Result
  9. Basic Engine Test
  10. Laser Test
  11. Focus Test
  12. Tilt Test
  13. Optimise Jitter
  14. Statistics Info
  15. Log
    1. Read Error Log
    2. Reset Error Log
  16. Spindle Motor
    1. Spindle Motor On
    2. Spindle Motor Off
    3. Spindle Motor Test
  17. Radial
    1. Radial On
    2. Radial Off
    3. Radial Initialisation
    4. Radial ATLS Calibration
  18. Sledge
    1. Sledge test
    2. Sledge test slow
  19. Tray
    1. Tray In
    2. Tray Out
5. DVIO
6. Progressive Scan Board
7. Loop Tests
8. Log
9. Scripts

**5.4 Nuclei Error Codes**

In the following table the error codes will be described.

**Table 5-6** Nuclei error codes overview

Error #	Description
60000	The player hangs, the RS232 port and the S2B port were successfully switched in pass-through mode. The only way to exit is to power 'off' the player
60100	The S2B interface with the Basic Engine was successfully checked
60101	The Basic Engine returned an error number
60102	Parity error from Basic Engine to Serial
60103	Communication time-out error
60104	Unexpected response from Basic Engine
60105	The Echo loop could not be closed
60106	A wrong echo pattern was received
60200	The version number of the Basic Engine was successfully returned
60201	The Basic Engine returned an error number
60202	Parity error from Basic Engine to Serial
60203	Communication time-out error
60204	Unexpected response from Basic Engine
60205	The Front Panel failed

Error #	Description
60300	The Basic Engine was successfully reset
60301	Basic-Engine time-out error
60400	The focus on test was successfully completed
60401	The Basic Engine returned an error number
60402	Parity error from Basic Engine to Serial
60403	Communication time-out error
60404	Unexpected response from Basic Engine
60405	Focus loop could not be closed
60500	The focus off test was successfully completed
60501	The Basic Engine returned an error number
60502	Parity error from Basic Engine to Serial
60503	Communication time-out error
60504	Unexpected response from Basic Engine
60600	The disk motor was successfully switched on
60601	The Basic Engine returned an error number
60602	Parity error from Basic Engine to Serial
60603	Communication time-out error
60604	Unexpected response from Basic Engine
60700	The disk motor was successfully switched off
60701	The Basic Engine returned an error number
60702	Parity error from Basic Engine to Serial
60703	Communication time-out error
60704	Unexpected response from Basic Engine
60800	The Radial loop was successfully closed
60801	The Basic Engine returned an error number
60802	Parity error from Basic Engine to Serial
60803	Communication time-out error
60804	Unexpected response from Basic Engine
60805	The Radial loop could not be closed
60900	The Radial loop was successfully opened
60901	The Basic Engine returned an error number
60902	Parity error from Basic Engine to Serial
60903	Communication time-out error
60904	Unexpected response from Basic Engine
61500	The tray was successfully closed
61501	The Basic Engine returned an error number
61502	Parity error from Basic Engine to Serial
61503	Communication time-out error
61504	Unexpected response from Basic Engine
61600	The tray was successfully opened
61601	The Basic Engine returned an error number
61602	Parity error from Basic Engine to Serial
61603	Communication time-out error
61604	Unexpected response from Basic Engine
61700	The data was successfully written on and read from a DVD disc
61701	The tray-in command failed
61702	The read-TOC command failed
61703	The VSM interrupt initialisation failed
61704	The set irq command failed
61705	No disc or wrong disc inserted

Error #	Description
61706	The rec-pause command failed
61707	The VSM BE out DMA initialisation failed
61708	The VSM BE out initialisation failed
61709	The VSM BE out DMA start failed
61710	The VSM BE out start failed
61711	The rec command failed
61712	The VSM out underrun error occurred
61713	The record complete interrupt was not raised
61714	The get irq command failed
61715	There was no interrupt raised by BE
61716	The VSM DMA did not finished
61717	The stop command after writing failed
61718	The VSM Sector processor initialisation failed
61719	The VSM sector processor DMA initialisation failed
61720	The VSM sector processor DMA start failed
61721	The VSM sector processor start failed
61722	The seek command failed
61723	The VSM sector processor error occurred
61724	The read timeout occurred
61725	The stop command after reading failed
61726	There was a difference found in data at a specific disc sector
61727	The result of the self test contains errors
61728	An error interrupt was raised by BE
61729	The calibrate-record command failed
61800	This test succeeded
61801	I2c initialisation failed
61802	The result of the self test contains errors
62500	Everything went well
62501	The Basic Engine returned an error number
62502	Parity error from Basic Engine to Serial
62503	Communication time-out error
62504	Unexpected response from Basic Engine
62600	Everything went well
62601	The Basic Engine returned an error number
62602	Parity error from Basic Engine to Serial
62603	Communication time-out error
62604	Unexpected response from Basic Engine
62700	The data byte was successfully read from the EEPROM
62701	The Basic Engine returned an error number
62702	Parity error from Basic Engine to Serial
62703	Communication time-out error
62704	Unexpected response from Basic Engine
62705	The user entered an invalid input
62900	This nucleus succeeded
62901	The Basic Engine returned an error number
62902	Parity error from Basic Engine to Serial
62903	Communication time-out error
62904	Unexpected response from Basic Engine
62905	Jitter command could not be completed
63000	The adaptive track-loss slicer calibration was successfully executed
63001	The Basic Engine returned an error number
63002	Parity error from Basic Engine to Serial
63003	Communication time-out error

Error #	Description
63004	Unexpected response from Basic Engine
63100	The statistics were retrieved successfully from the Basic Engine
63101	The Basic Engine returned an error number
63102	Parity error from Basic Engine to Serial
63103	Communication time-out error
63104	Unexpected response from Basic Engine
63200	The statistical information was successfully erased
63201	The Basic Engine returned an error number
63202	Parity error from Basic Engine to Serial
63203	Communication time-out error
63204	Unexpected response from Basic Engine
63300	The error log was successfully retrieved from the Basic Engine
63301	The Basic Engine returned an error number
63302	Parity error from Basic Engine to Serial
63303	Communication time-out error
63304	Unexpected response from Basic Engine
63400	The fatal error log and the cumulative error log were successfully erased
63401	The Basic Engine returned an error number
63402	Parity error from Basic Engine to Serial
63403	Communication time-out error
63404	Unexpected response from Basic Engine
63800	The result of the self test was successfully retrieved
63801	The Basic Engine returned an error number
63802	Parity error from Basic Engine to Serial
63803	Communication time-out error
63804	Unexpected response from Basic Engine
63805	The result of the self test contains errors
63900	The radial initialisation was successfully executed
63901	The Basic Engine returned an error number
63902	Parity error from Basic Engine to Serial
63903	Communication time-out error
63904	Unexpected response from Basic Engine
64000	The result of the self test was successfully retrieved
64001	The Basic Engine returned an error number
64002	Parity error from Basic Engine to Serial
64003	Communication time-out error
64004	Unexpected response from Basic Engine
64100	The data was successfully written on and read from a DVD disc
64101	The tray-in command failed
64102	The read-TOC command failed
64103	The VSM interrupt initialisation failed
64104	The set irq command failed
64105	No disc or wrong disc inserted
64106	The rec-pause command failed
64107	The VSM BE out DMA initialisation failed
64108	The VSM BE out initialisation failed
64109	The VSM BE out DMA start failed
64110	The VSM BE out start failed
64111	The rec command failed
64112	The VSM out underrun error occurred

Error #	Description
64113	The record complete interrupt was not raised
64114	The get irq command failed
64115	There was no interrupt raised by BE
64116	The VSM DMA did not finished
64117	The stop command after writing failed
64118	The VSM Sector processor initialisation failed
64119	The VSM sector processor DMA initialisation failed
64120	The VSM sector processor DMA start failed
64121	The VSM sector processor start failed
64122	The seek command failed
64123	The VSM sector processor error occurred
64124	The read timeout occurred
64125	The stop command after reading failed
64126	There was a difference found in data at a specific disc sector
64127	The result of the self test contains errors
64128	An error interrupt was raised by BE
64129	The calibrate-record command failed
64130	To many retries
64131	BE update RAI command after writing failed
64132	BE find first recordable address command failed
64133	DVD+R disc is full
64200	This test succeeded
64201	I2C initialisation failed
64202	The result of the self test contains errors

## 5.5 Fault Finding

Below you will find faultfinding trees for all the main parts of the Basic Engine.

## 5.5.1 Basic Engine

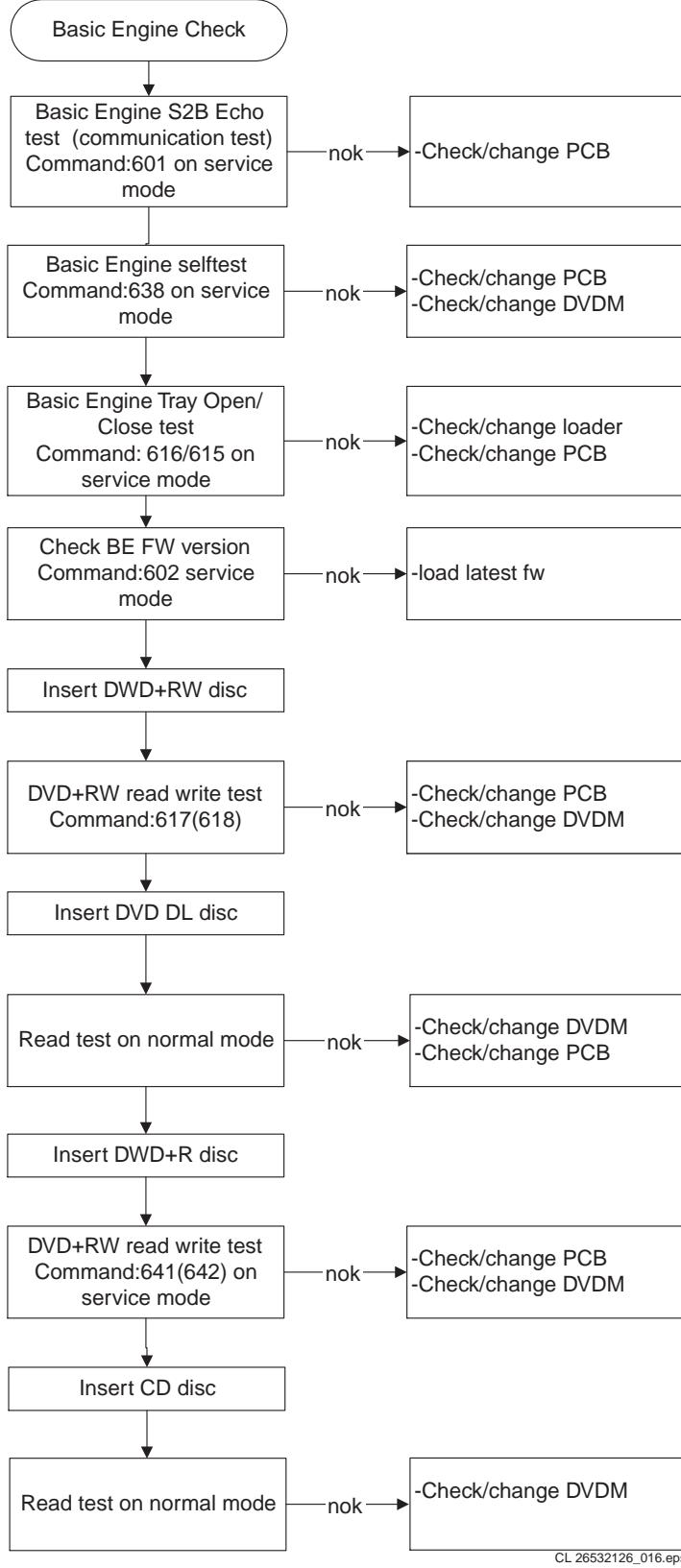
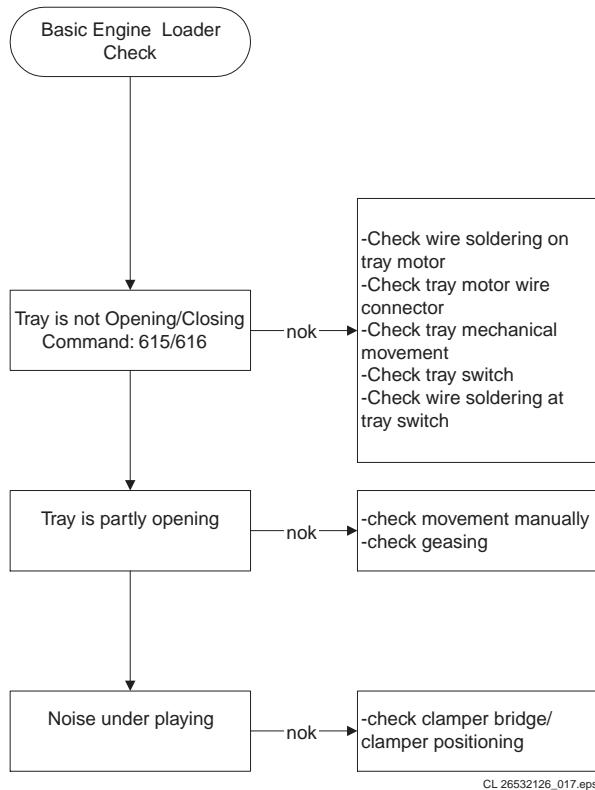
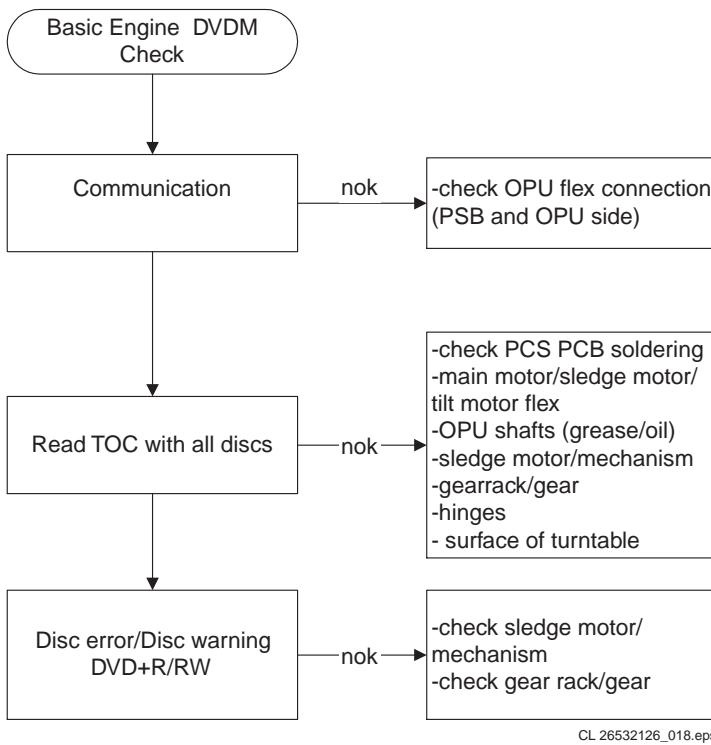
**BASIC ENGINE FUNCTIONAL TEST**

Figure 5-5 Basic Engine functional testing

## 5.5.2 Loader

**Loader Checking****Figure 5-6 Loader testing**

## 5.5.3 DVDM

**DVDM checking****Figure 5-7 DVD-M testing**

## 5.5.4 PWB

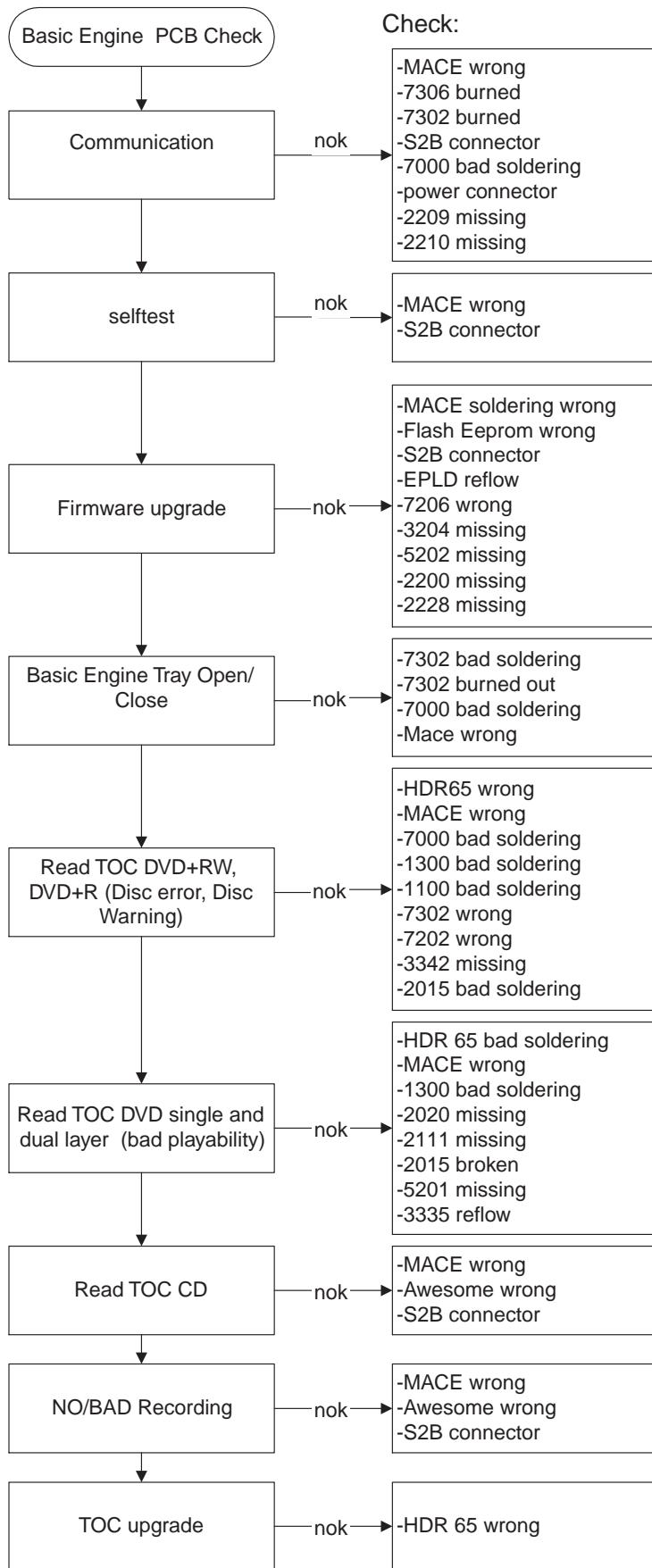
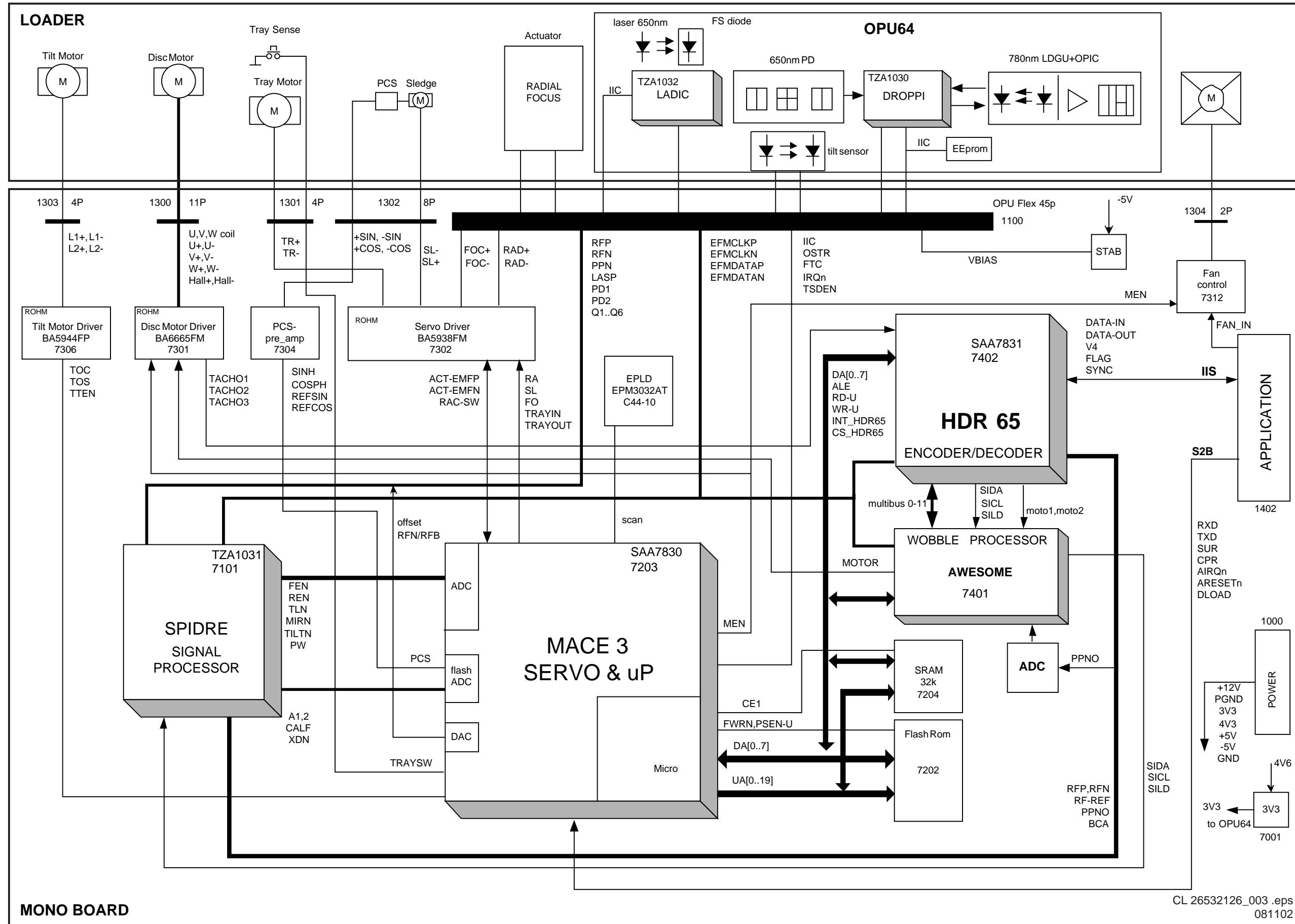
**PCB Checking**CL 26532126\_019.eps  
191102

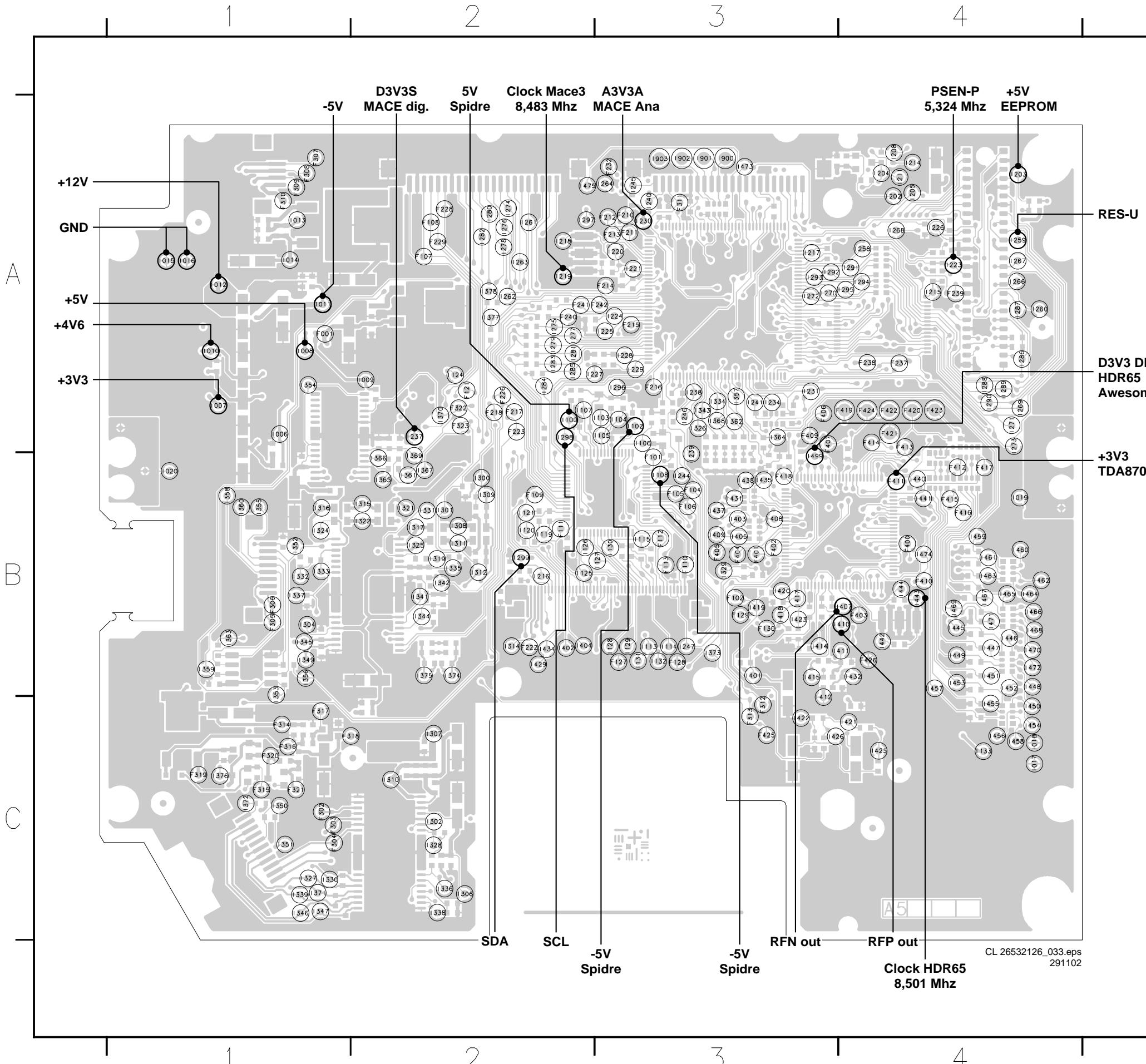
Figure 5-8 Mono board testin

## ***Personal Notes:***

## 6. Block Diagram.

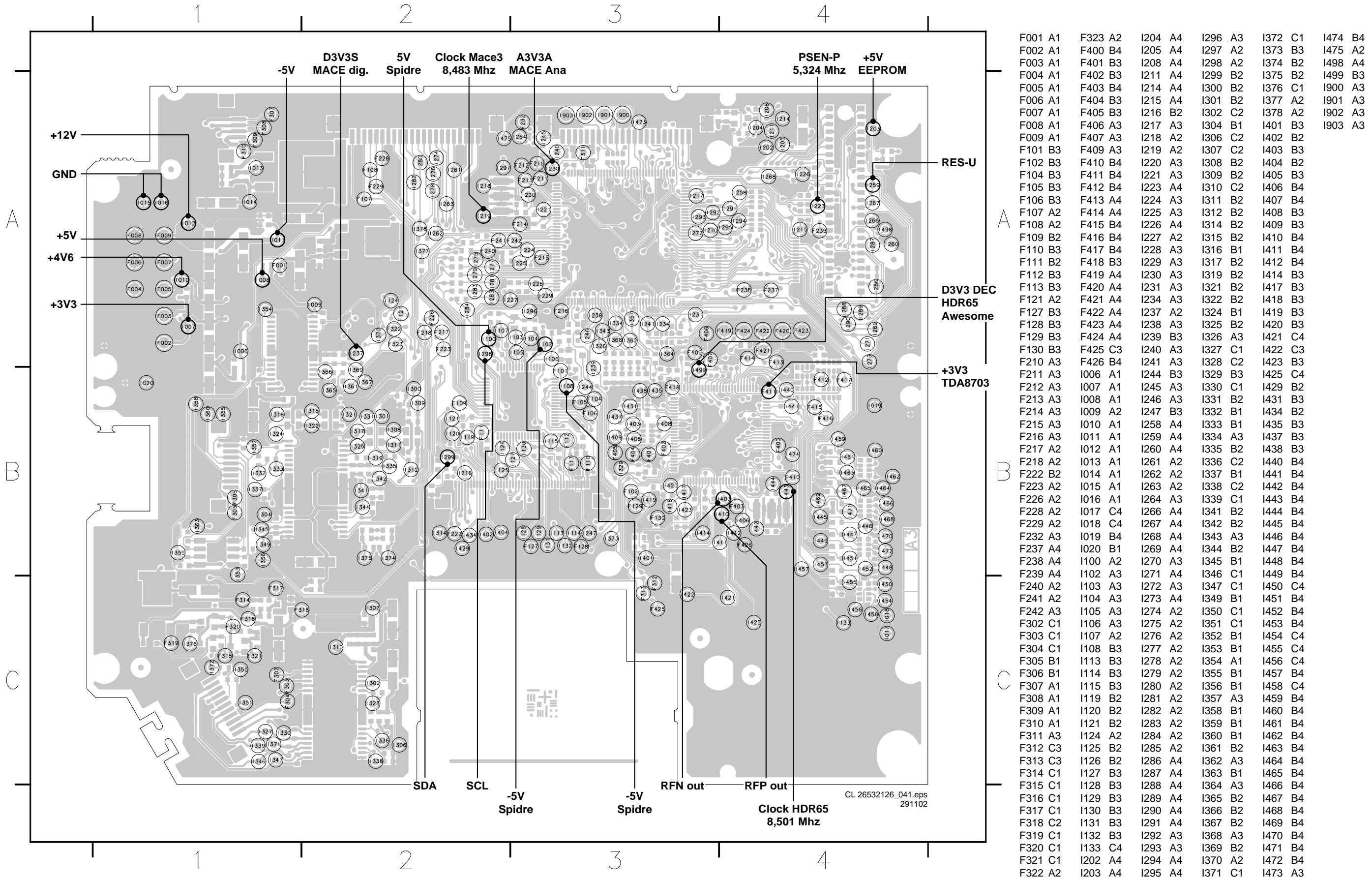
### Block Diagram



**Test Point Overview Servo Board 43015**

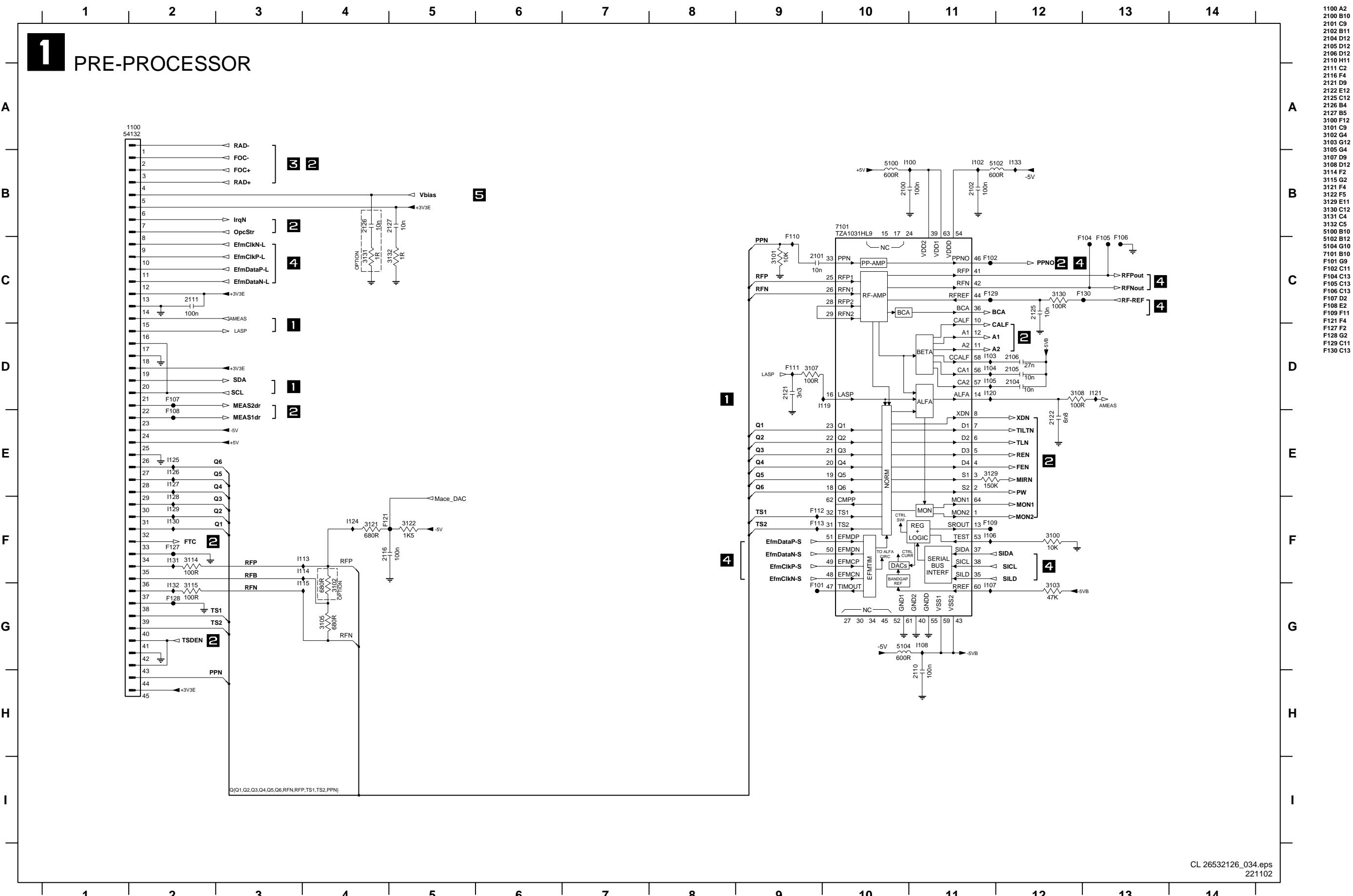
CL 26532126\_033.eps  
291102  
Clock HDR65  
8,501 Mhz

## Test Point Overview Servo Board 43353

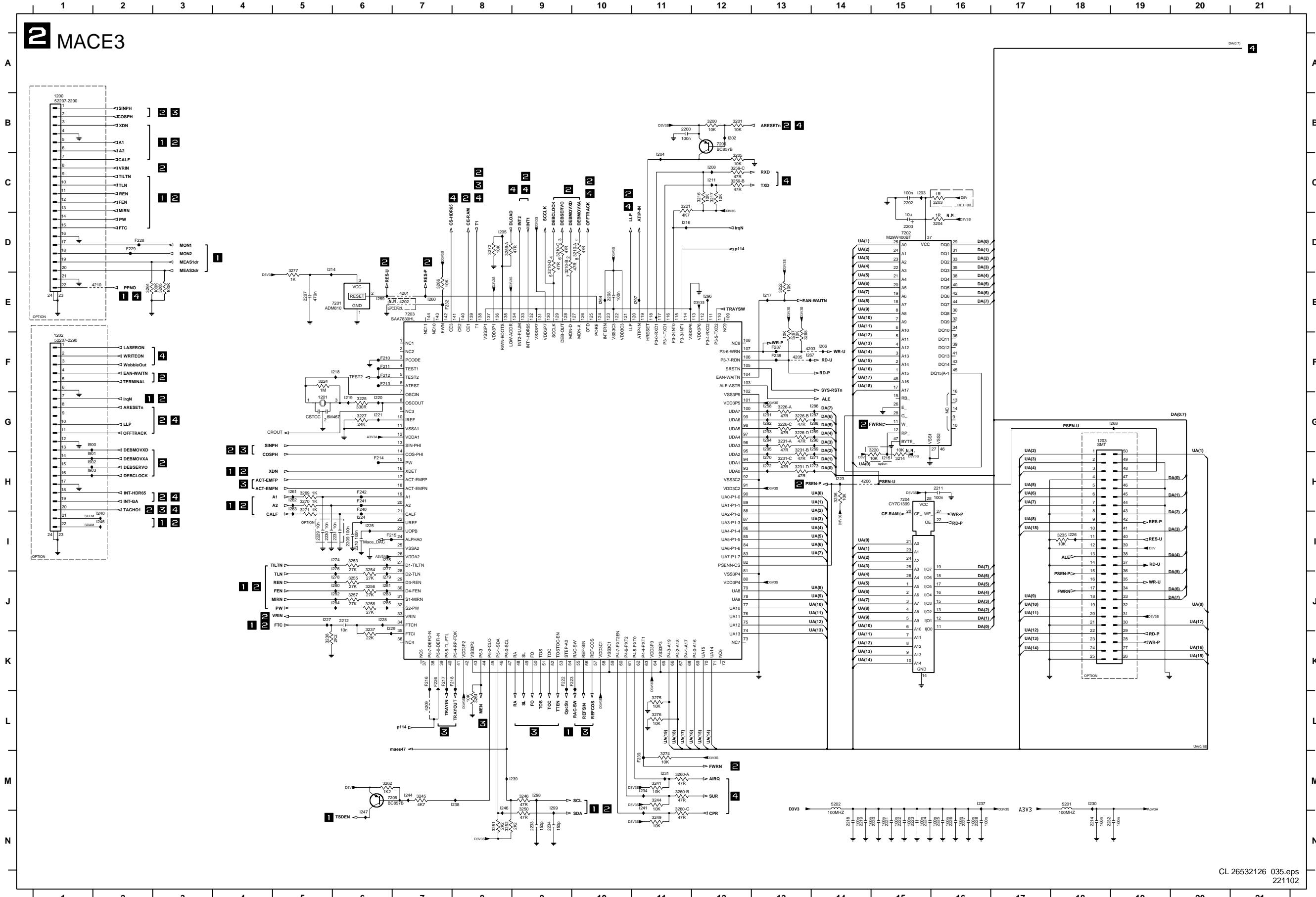


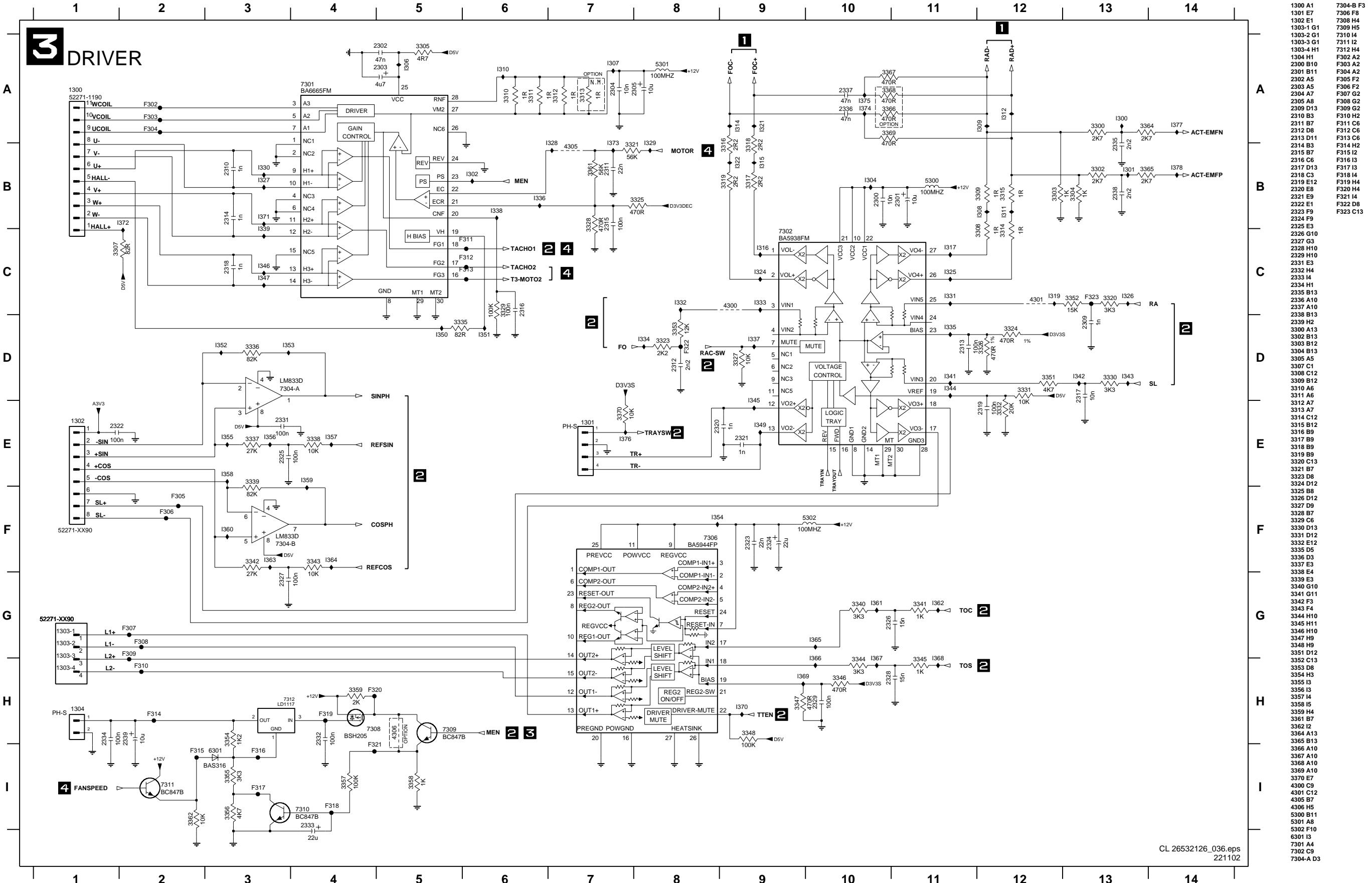
## 7. Electrical Diagrams and Print-Layouts

### Servo Board 43015: Pre- Processor

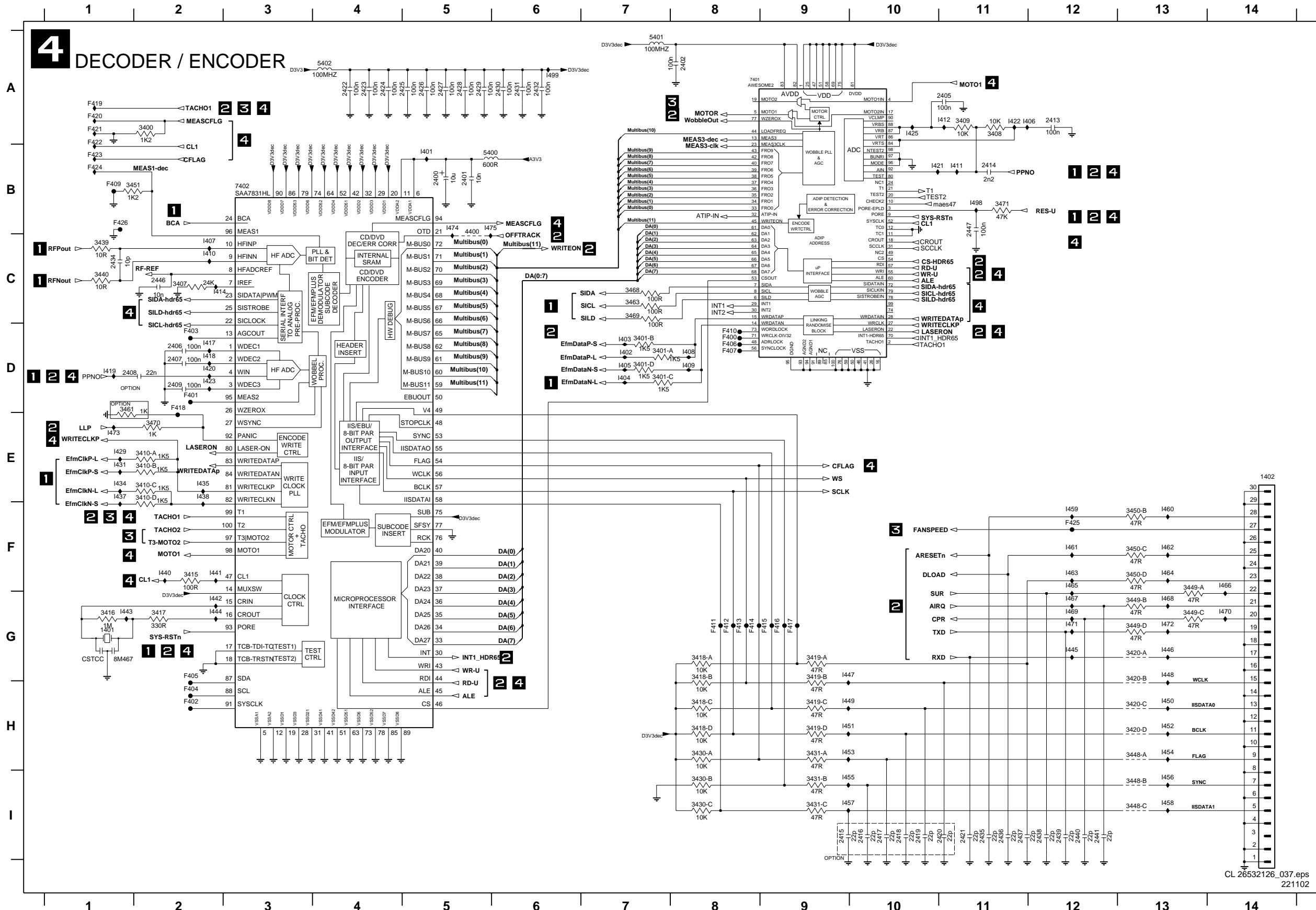


## Servo Board 43015: MACE3



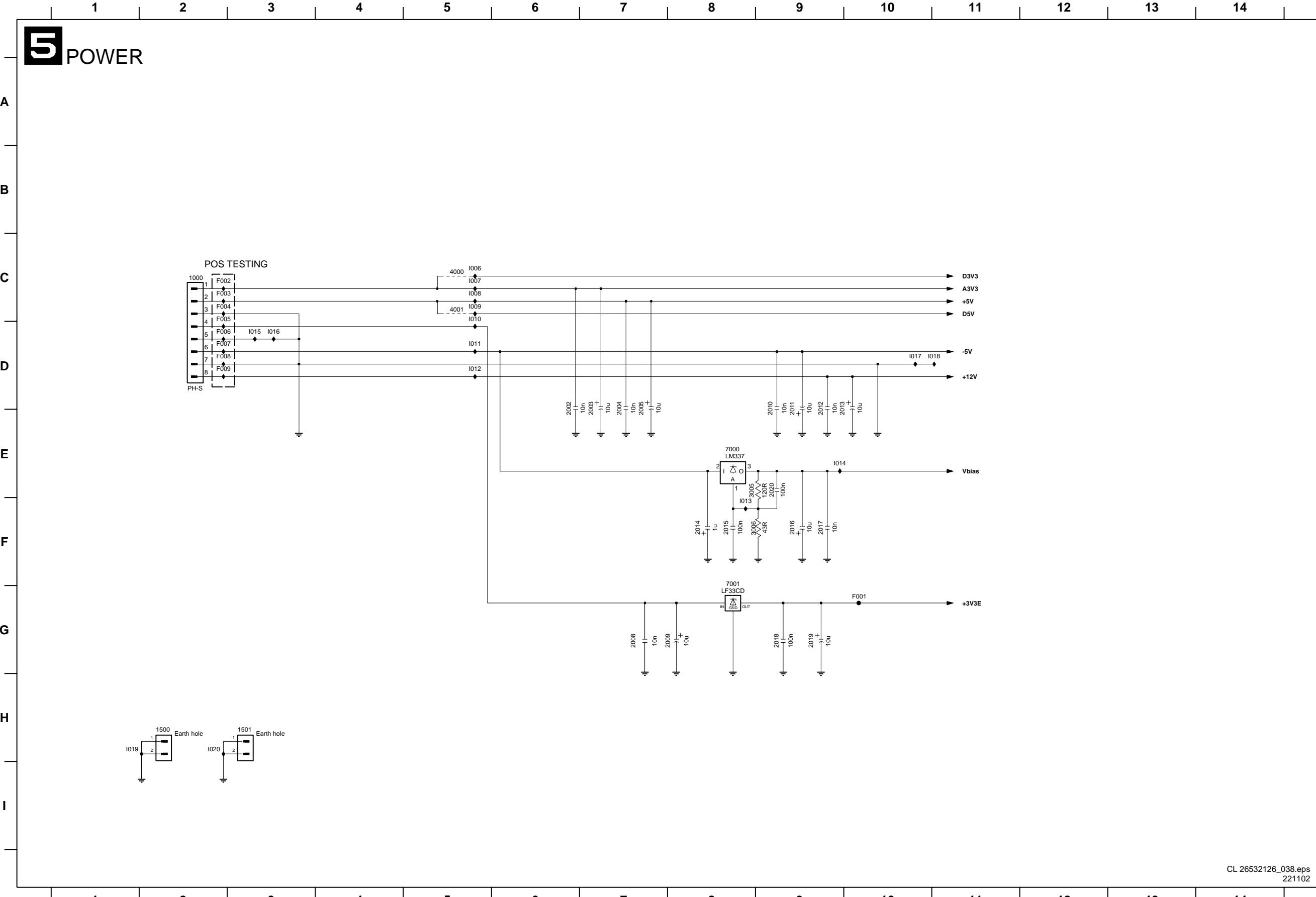
**Servo Board 43015: Driver**

## Servo Board 43015: Decoder / Encoder



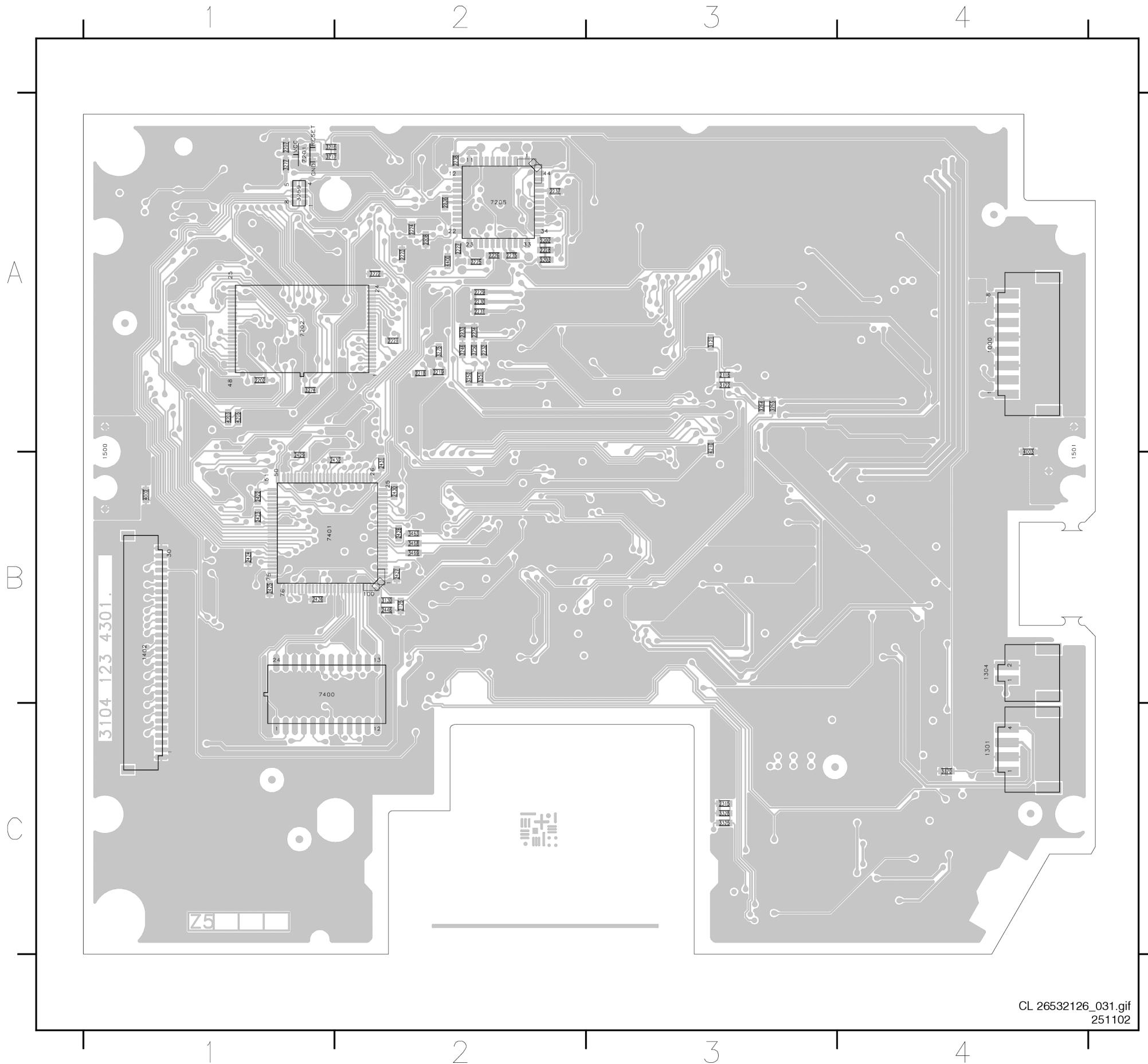
1401 G1	F425 F12
1402 E14	F426 B1
2401 B5	
2401 A8	
2405 A11	
2406 D2	
2407 D2	
2408 D1	
2409 D2	
2413 A12	
2414 B11	
2415 I9	
2416 I10	
2417 I10	
2418 I10	
2419 I10	
2420 I11	
2421 I11	
2422 A4	
2423 A4	
2424 A4	
2425 A5	
2426 A5	
2427 A5	
2428 A5	
2429 A5	
2430 A6	
2431 A6	
2432 A6	
2434 C1	
2435 I11	
2436 I11	
2437 I11	
2438 I12	
2439 I12	
2440 I12	
2441 I12	
2446 C2	
2447 B11	
3400 A2	
3401-A D7	
3401-B D7	
3401-C D7	
3401-D D7	
3407 C2	
3408 A11	
3409 A11	
3410-A E2	
3410-B E2	
3410-C E2	
3410-D E2	
3415 F2	
3416 G1	
3417 G2	
3418-A G8	
3418-B G8	
3418-C H8	
3418-D H8	
3419-A G9	
3419-B G9	
3419-C H9	
3419-D H9	
3420-A G13	
3420-B G13	
3420-C H13	
3420-D H13	
3430-A H8	
3430-B I8	
3430-C I8	
3431-A H9	
3431-B I9	
3431-C I9	
3439 C1	
3440 C1	
3448-A H13	
3448-B H13	
3448-C H13	
3448-D H13	
3449-A F13	
3449-B F13	
3449-C G13	
3449-D G13	
3450-B F13	
3450-C F13	
3450-D F13	
3451 B1	
3461 D1	
3463 C7	
3468 C7	
3469 C7	
3470 E2	
3471 B11	
4400 B5	
5400 B5	
5401 A7	
5402 A4	
7401 A8	
7402 B3	
F400 D8	
F401 D2	
F402 H2	
F403 D2	
F404 H2	
F405 G2	
F406 D8	
F407 D8	
F409 B1	
F410 D8	
F411 G8	
F412 G8	
F413 G8	
F414 G8	
F415 G9	
F416 G9	
F417 G9	
F418 D2	

CL 26532126\_037.eps  
221102

**Servo Board 43015: Power**

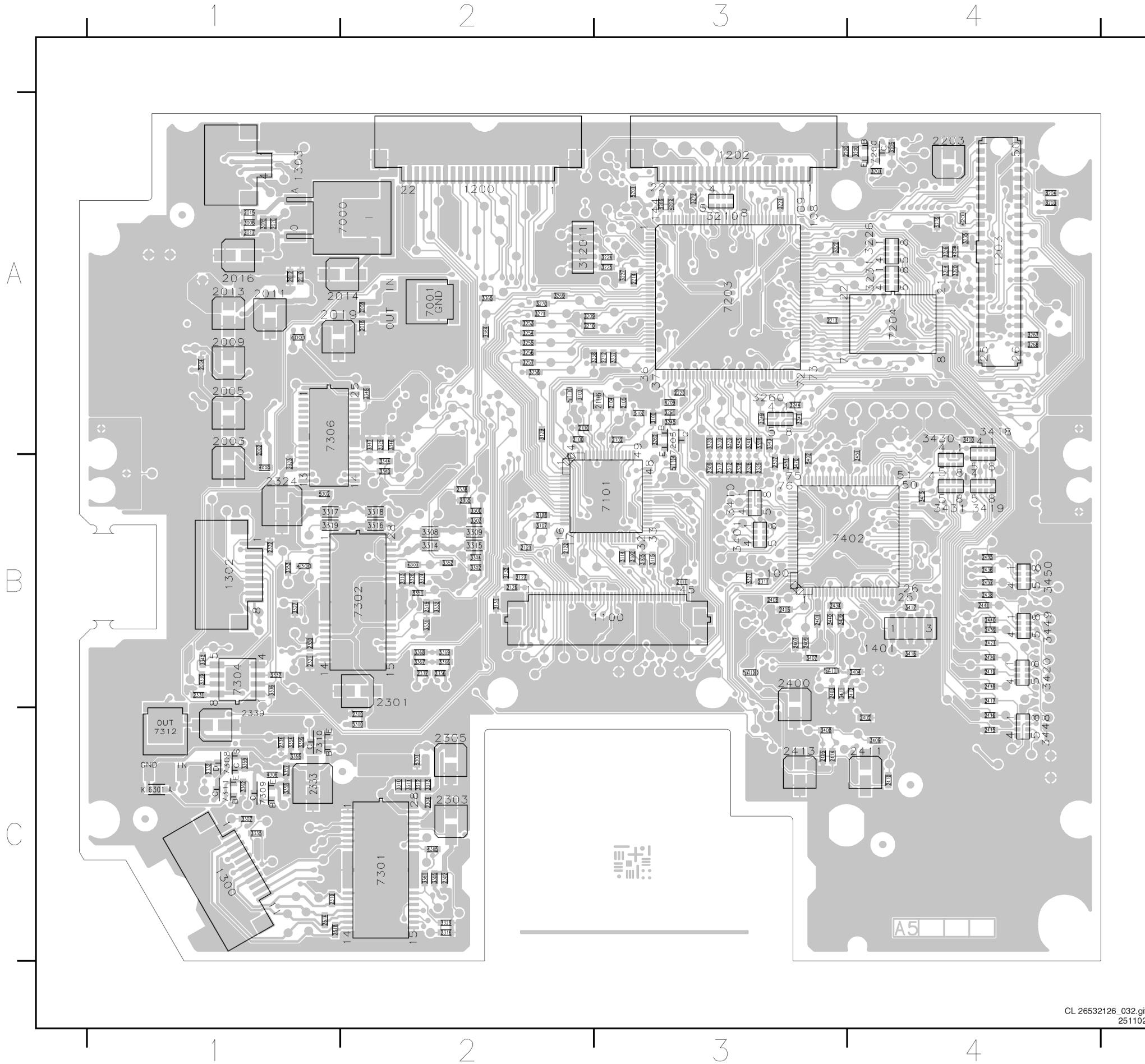
1000 C2  
1500 H2  
1501 H3  
2002 D6  
2003 D7  
2004 D7  
2005 D7  
2008 G7  
2009 G7  
2010 D9  
2011 D9  
2012 D9  
2013 D10  
2014 F8  
2015 F8  
2016 F9  
2017 F9  
2018 G9  
2019 G9  
2020 E9  
3005 E8  
3006 F9  
4000 C5  
4001 C5  
7000 E8  
7001 G8  
F001 G10

## **Layout Servo Board (Top Side)**



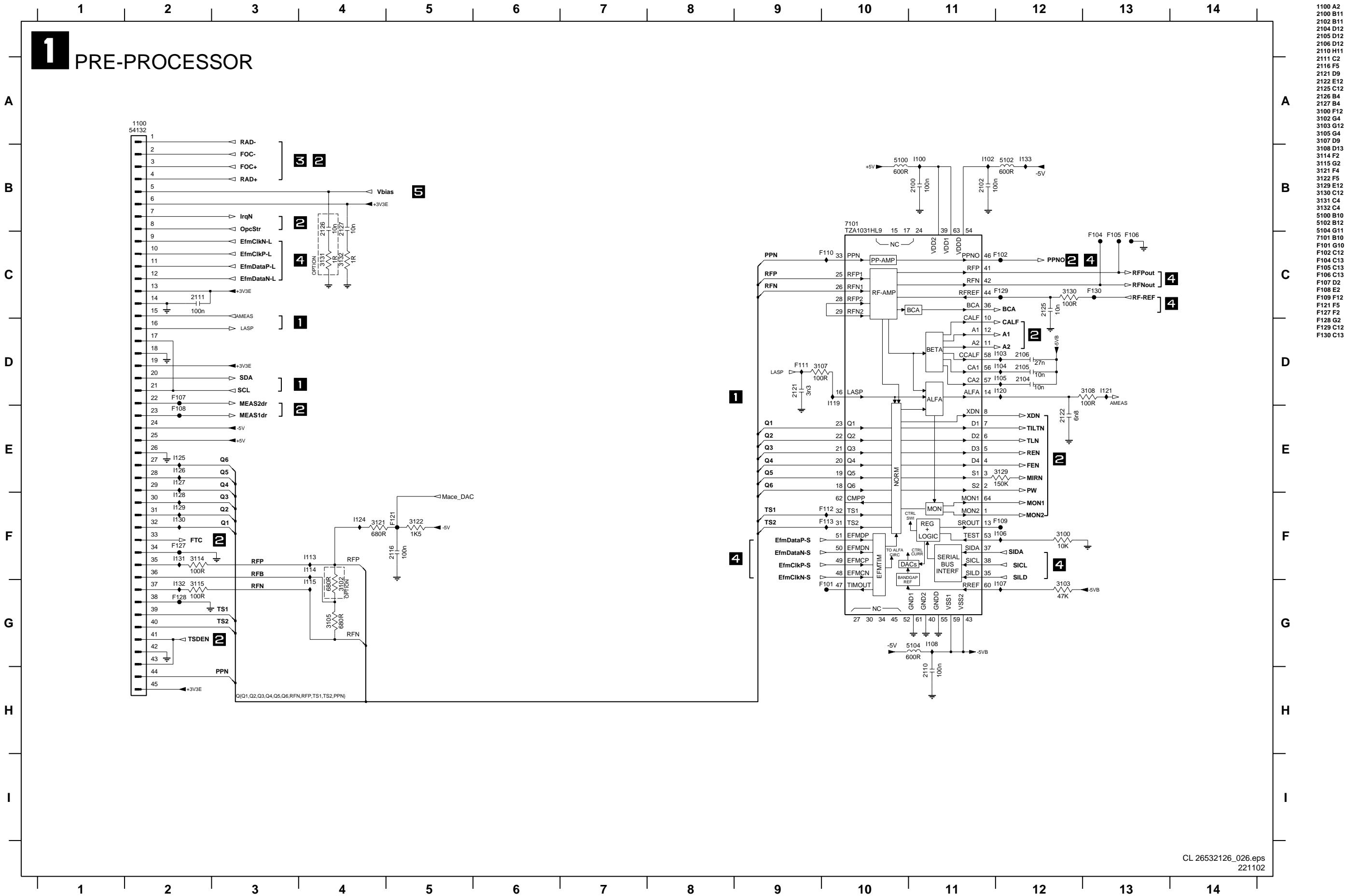
CL 26532126\_031.  
2511

## **Layout Servo Board (Bottom Side)**

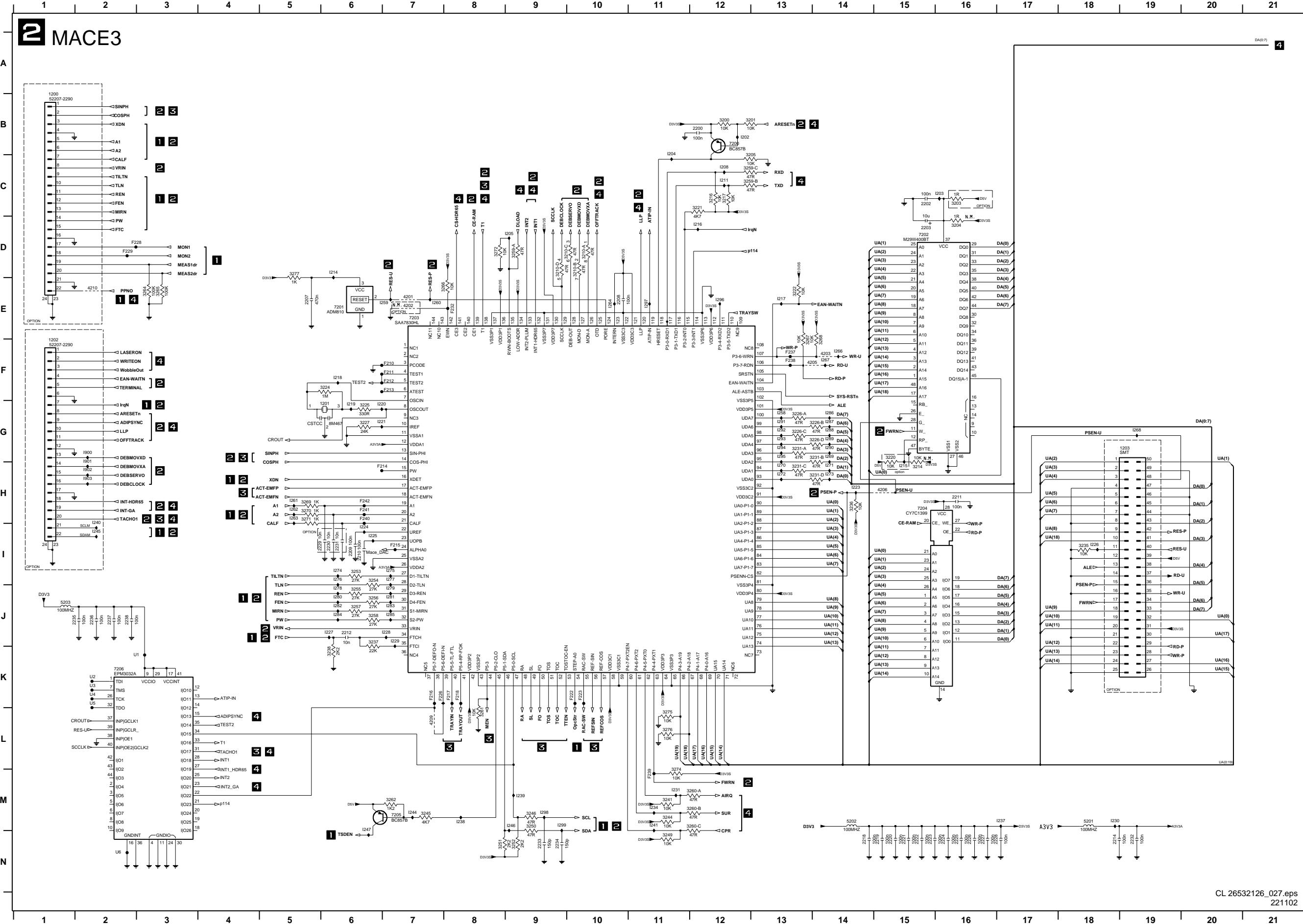


1100	B3	2327	B3	3227	A3	3348	A2	7205	A3
1200	A2	2328	B3	3231	A4	3351	B2	7301	C2
1201	A2	2329	A2	3235	A4	3352	B2	7302	B2
1202	A3	2331	B1	3236	A4	3353	B1	7304	B1
1203	A4	2332	C1	3237	A3	3354	C1	7306	A1
1300	C1	2333	C1	3238	A2	3355	C1	7308	C1
1302	B1	2334	C1	3241	A3	3356	C1	7309	C1
1303	A1	2335	B2	3244	A3	3357	C1	7310	C1
1401	B4	2336	B2	3245	A3	3358	C1	7311	C1
2002	A1	2337	B2	3249	A3	3359	C1	7312	C1
2003	B1	2338	B2	3253	A2	3361	C2	7402	B4
2004	A1	2339	C1	3254	A2	3362	C1		
2005	A1	2400	B3	3255	A2	3364	A2		
2008	A2	2401	B3	3256	A2	3365	A2		
2009	A1	2402	C4	3257	A2	3366	B2		
2010	A1	2403	B3	3258	A2	3367	B2		
2011	A1	2404	B4	3260	A3	3368	B2		
2012	A1	2405	C3	3261	A3	3369	B2		
2013	A1	2406	B3	3262	A3	3400	A4		
2014	A2	2407	B3	3266	A3	3401	B3		
2015	A1	2408	B3	3267	A4	3407	B3		
2016	A1	2409	B3	3268	A4	3408	C3		
2017	A1	2410	C4	3269	A2	3409	C4		
2018	A2	2411	C3	3270	A2	3410	B3		
2019	A1	2412	B4	3271	A2	3411	B3		
2020	A1	2413	C3	3272	A3	3415	B4		
2100	A2	2414	C3	3276	A4	3416	B4		
2102	A3	2415	C4	3300	B2	3417	B4		
2104	A3	2416	C4	3302	B2	3418	A4		
2105	A3	2417	B4	3303	B2	3419	B4		
2106	A3	2418	B4	3304	B2	3420	B4		
2110	A2	2419	B4	3305	C2	3430	B4		
2111	B3	2420	B4	3307	C1	3431	B4		
2121	B2	2421	B4	3308	B2	3439	B3		
2122	B2	2434	B3	3309	B2	3440	B3		
2126	B2	2435	B4	3310	C2	3448	C4		
2127	B2	2436	B4	3311	C2	3449	B4		
2200	A3	2437	B4	3312	C2	3450	B4		
2203	A4	2438	B4	3313	C2	3451	B4		
2209	A2	2439	B4	3314	B2	3461	B3		
2210	A2	2440	B4	3315	B2	3470	B3		
2211	A3	2441	B4	3316	B2	4000	B1		
2212	A3	3005	A1	3317	B1	4001	A1		
2214	A3	3006	A1	3318	B2	4201	A4		
2220	A3	3100	A3	3319	B1	4202	A3		
2300	C2	3102	B3	3320	A3	4206	A4		
2301	B1	3103	A2	3321	B3	4209	A3		
2302	C2	3105	B3	3323	A3	4300	B1		
2303	C2	3107	B2	3324	B2	4301	B2		
2304	C2	3108	B2	3326	B2	4305	C2		
2305	C2	3114	B3	3327	B1	4306	C1		
2309	B3	3115	B3	3329	C2	5100	A2		
2310	C1	3129	A2	3330	A3	5102	A3		
2311	B3	3131	B2	3331	B2	5104	B3		
2312	B3	3132	B2	3332	B2	5201	A3		
2313	B2	3200	A4	3335	C1	5300	C2		
2314	C1	3201	A4	3336	B1	5301	C2		
2316	C2	3203	A4	3337	B1	5302	B1		
2317	B3	3204	A4	3338	A3	5400	B3		
2318	C1	3205	A4	3339	B1	5401	B3		
2319	B2	3210	A3	3340	B2	5402	B3		
2320	B1	3214	A4	3341	A3	6301	C1		
2321	B1	3220	A4	3342	B1	7000	A2		
2322	B1	3221	A3	3343	A3	7001	A2		
2323	B1	3222	A3	3344	B2	7101	B3		
2324	B1	3224	A3	3345	A3	7200	A4		
2325	B3	3225	A3	3346	A2	7203	A3		
2326	B3	3226	A4	3347	A2	7204	A4		

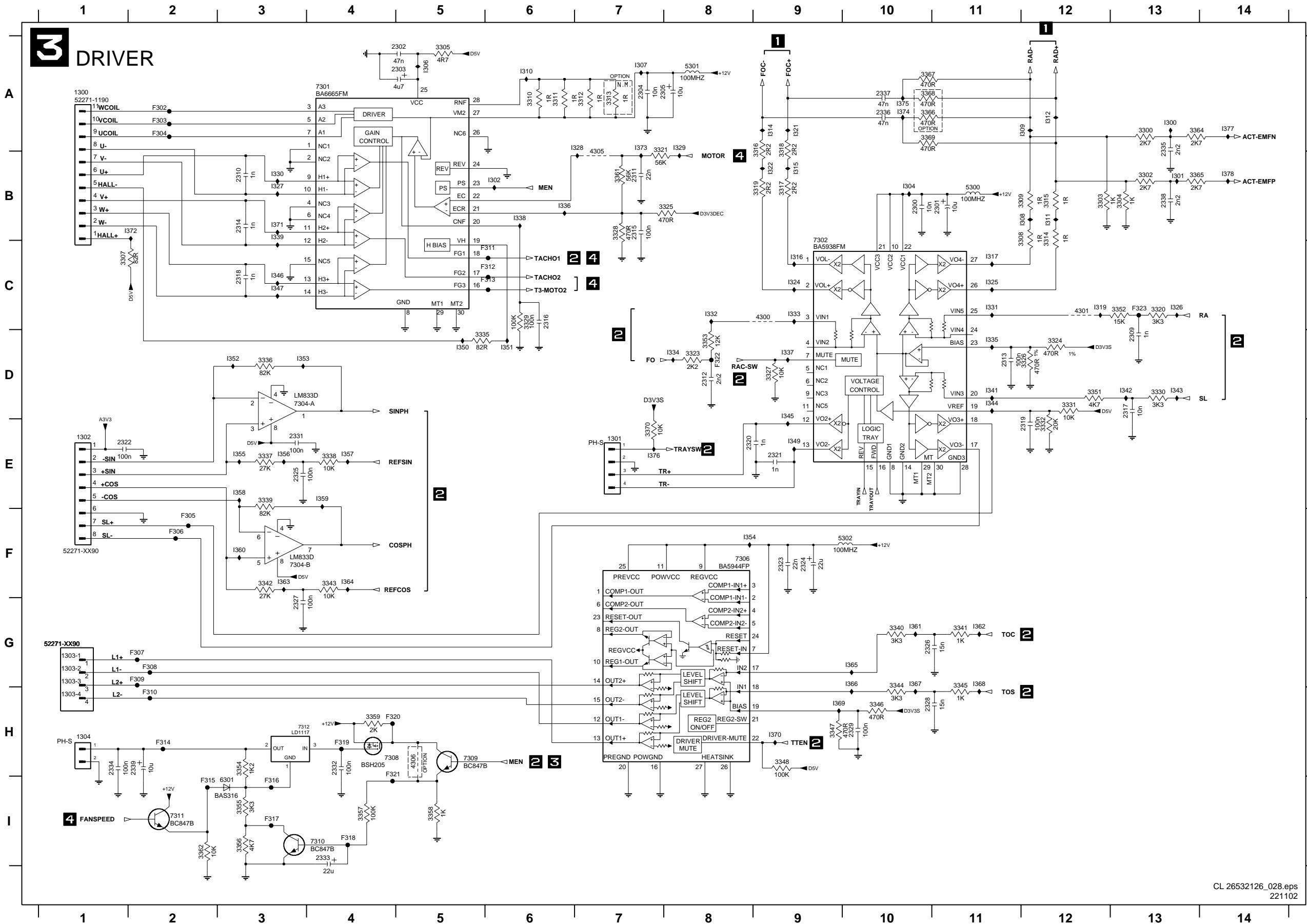
## Servo Board 43353: Pre- Processor



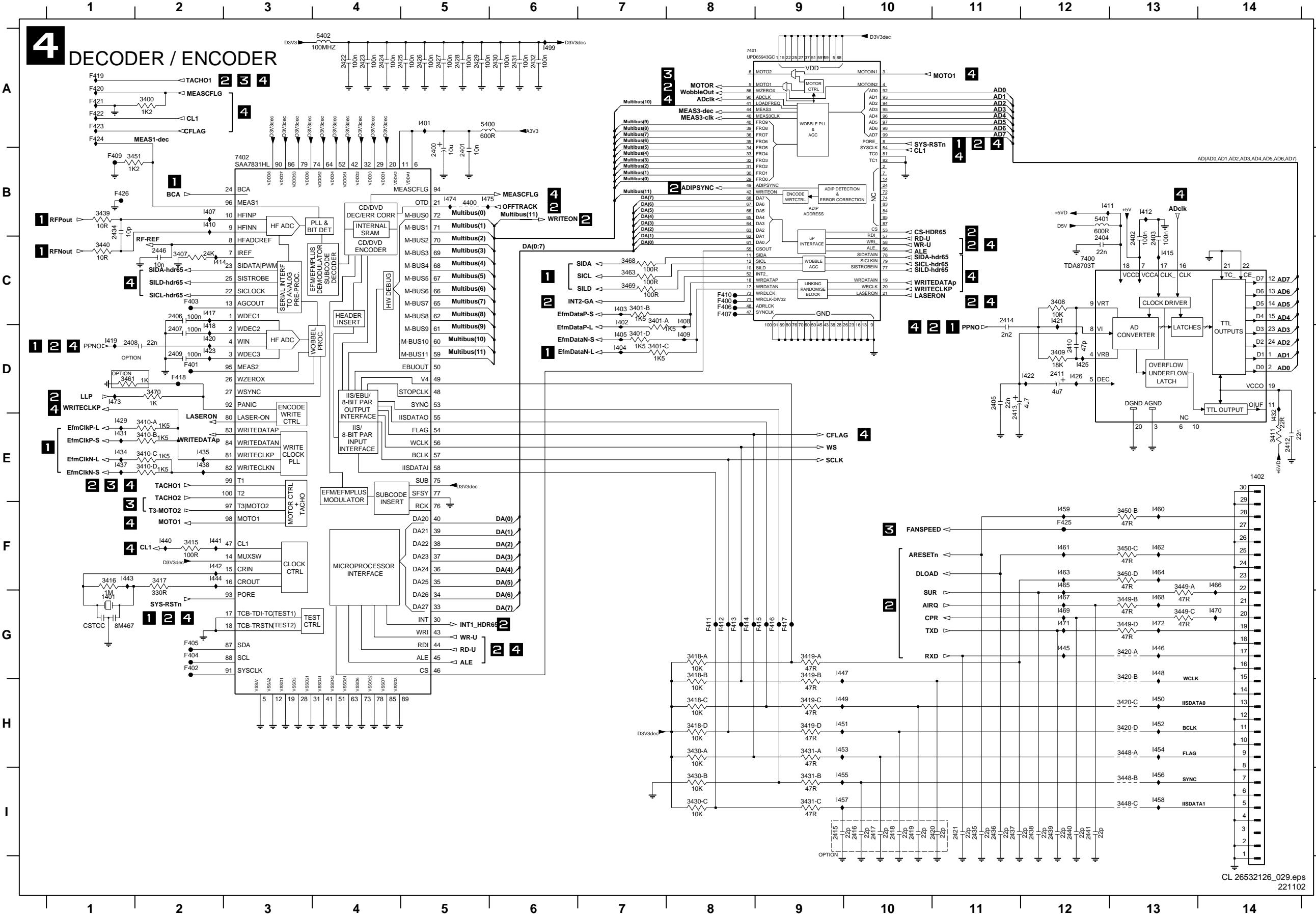
## Servo Board 43353: MACE3



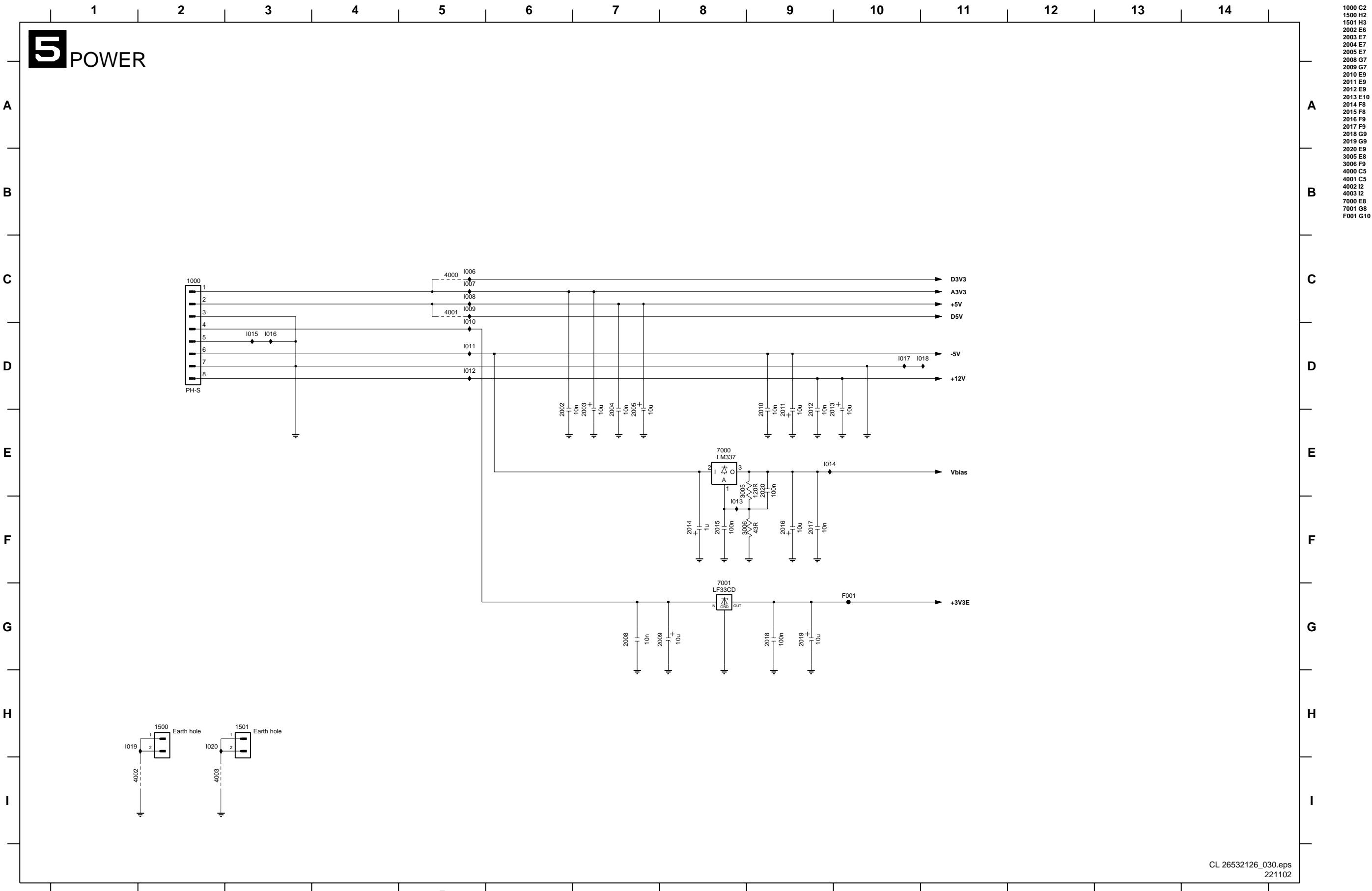
## Servo Board 43353: Driver

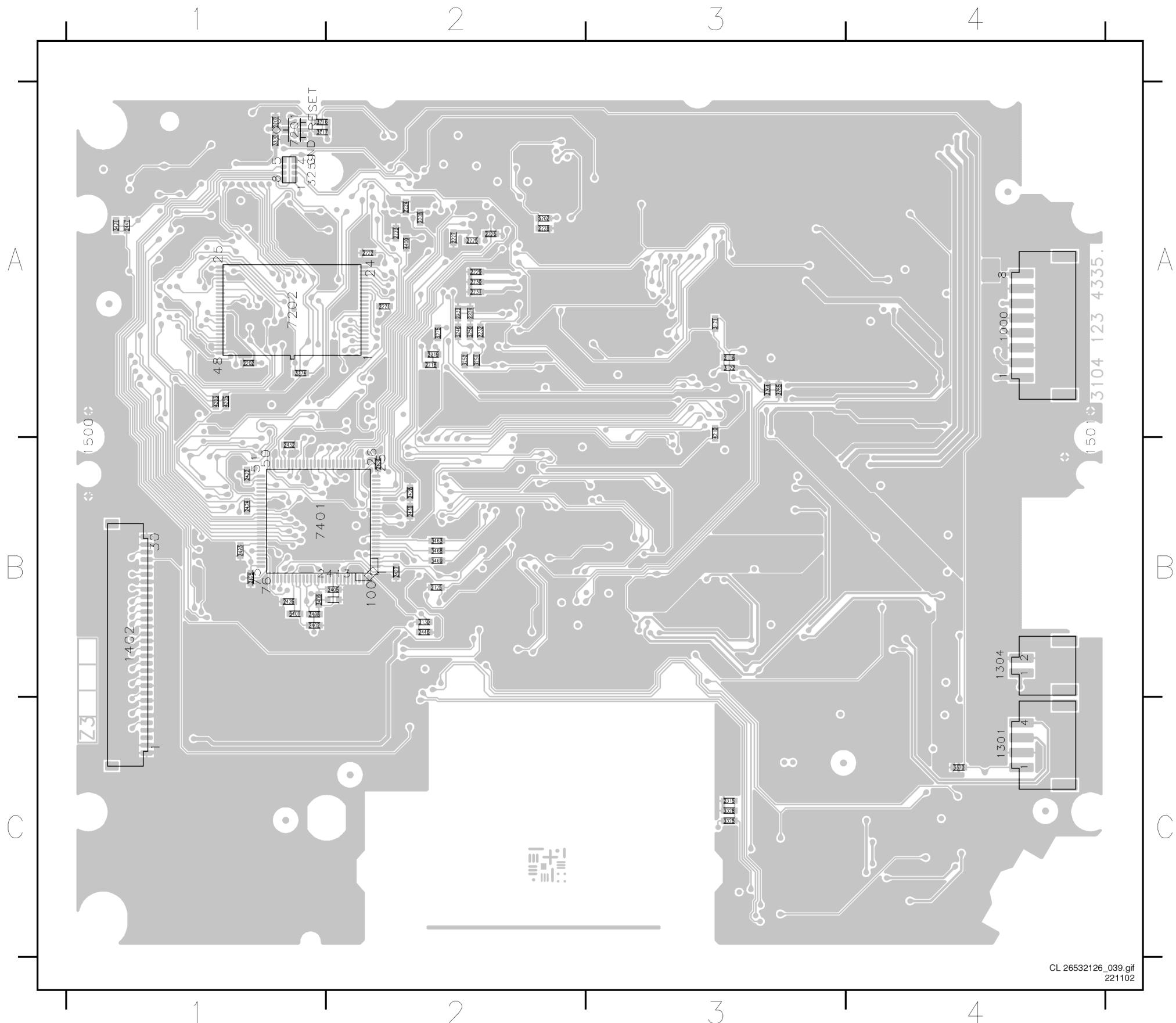


## Servo Board 43353: Decoder / Encoder



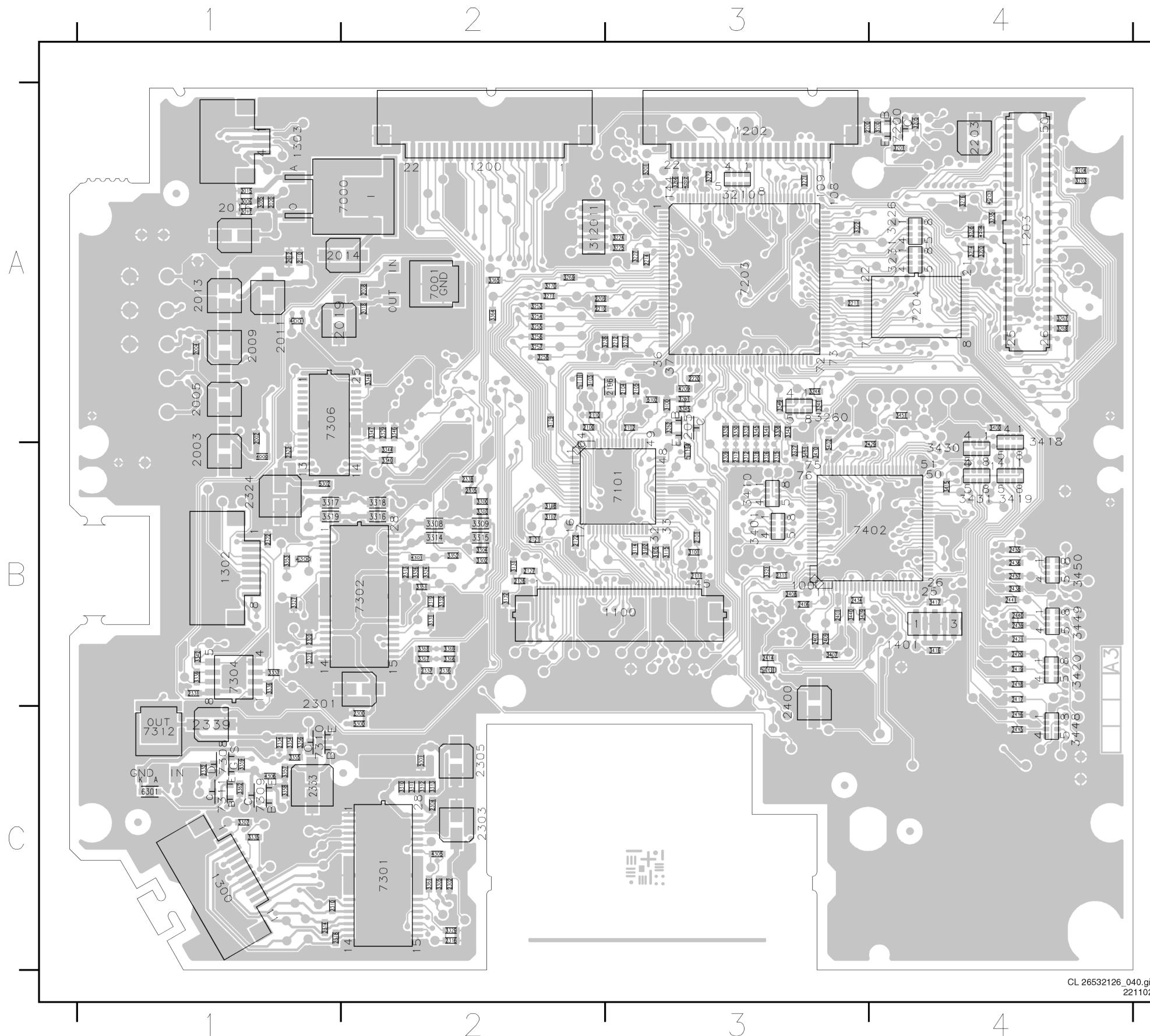
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2401 B5  
2402 C13  
2403 C13  
2404 C13  
2405 D11  
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2407 D2  
2408 D1  
2409 D2  
2410 D12  
2411 D12  
2412 E14  
2413 D11  
2414 C11  
2415 I9  
2416 I10  
2417 I10  
2418 I10  
2419 I10  
2420 I11  
2421 I11  
2422 A4  
2423 A4  
2424 A4  
2425 A5  
2426 A5  
2427 A5  
2428 A5  
2429 A5  
2430 A6  
2431 A6  
2432 A6  
2433 B1  
2434 I11  
2435 I11  
2436 I11  
2437 I12  
2438 I12  
2439 I12  
2440 I12  
2441 I2  
2442 C2  
3400 A2  
3401-A C7  
3401-B C7  
3401-C D7  
3401-D D7  
3407 C2  
3408 C12  
3409 D12  
3410-A E2  
3410-B E2  
3410-C E2  
3410-D E2  
3411 E14  
3415 F2  
3416 F1  
3417 F2  
3418-G8  
3418-B G8  
3418-C H8  
3418-D H8  
3419-A G9  
3419-B G9  
3419-C H9  
3419-D H9  
3420-B G13  
3420-D H13  
3420-E H13  
3420-F H8  
3430-B I8  
3430-C I8  
3431-A H9  
3431-B I9  
3431-C I9  
3433 B1  
3440 C1  
3446 A1 H13  
3446-B I13  
3446-C I13  
3446-D F13  
3446-G F13  
3447-B G13  
3447-C G13  
3447-D G13  
3448-B F13  
3448-C F13  
3448-D F13  
3449-B G13  
3449-C G13  
3449-D G13  
3450-B F13  
3450-C F13  
3450-D F13  
3451-B G13  
3461-C1  
3463-C7  
3468-C7  
3469-C7  
3470-D2  
4400-B5  
5400-A5  
5401-B12  
5402-A4  
7400-C12  
7401-A8  
7402-B3  
F400-C8  
F401-D2  
F402-G2  
F403-C2  
F404-C2  
F405-G2  
F406-C8  
F407-C8  
F409-B1  
F410-C8  
F411-G8  
F412-G8  
F414-G8  
F415-G9

**Servo Board 43353: Power**

**Layout Servo Board 43353 (Top Side)**

1000 A4	4400 A2
1301 C4	5202 A2
1304 B4	5401 B1
1402 B1	7201 A1
2116 A3	7202 A1
2125 B2	7401 B1
2202 A1	
2207 A1	
2208 A2	
2218 A2	
2219 A2	
2221 A2	
2222 A2	
2223 A2	
2224 A2	
2225 A2	
2226 A2	
2227 A2	
2228 A2	
2229 A2	
2230 A2	
2231 A2	
2232 A2	
2233 A2	
2234 A2	
2315 C3	
2402 B1	
2405 B2	
2413 B2	
2422 B1	
2423 B1	
2424 B1	
2425 B1	
2426 B1	
2427 B2	
2428 B2	
2430 B2	
2431 B2	
2432 B1	
2446 B2	
2447 A1	
3121 A3	
3122 A3	
3130 B2	
3216 A1	
3217 A1	
3246 A2	
3250 A2	
3251 A2	
3252 A2	
3259 A1	
3264 A3	
3265 A3	
3274 A1	
3275 A2	
3277 A1	
3325 C3	
3328 C3	
3370 C4	
3408 B1	
3409 B1	
3463 B2	
3468 B2	
3469 B2	
3471 A1	
4203 A1	
4205 A1	
4210 A3	

## **Layout Servo Board 43353 (Bottom Side)**



1100	B3	2326	B3	3238	A2	3355	C1	7312	C1
1200	A2	2327	B3	3241	A3	3356	C1	7402	B4
1201	A2	2328	B3	3244	A3	3357	C1		
1202	A3	2329	A2	3245	A3	3358	C1		
1203	A4	2331	B1	3249	A3	3359	C1		
1300	C1	2332	C1	3253	A2	3361	C2		
1302	B1	2333	C1	3254	A2	3362	C1		
1303	A1	2334	C1	3255	A2	3364	A2		
1401	B4	2335	B2	3256	A2	3365	A2		
2002	A1	2336	B2	3257	A2	3366	B2		
2003	B1	2337	B2	3258	A2	3367	B2		
2004	A1	2338	B2	3260	A3	3368	B2		
2005	A1	2339	C1	3261	A3	3369	B2		
2008	A2	2400	B3	3262	A3	3400	A4		
2009	A1	2401	B3	3266	A3	3401	B3		
2010	A1	2406	B3	3267	A4	3407	B3		
2011	A1	2407	B3	3268	A4	3410	B3		
2012	A1	2408	B3	3269	A2	3415	B4		
2013	A1	2409	B3	3270	A2	3416	B4		
2014	A2	2414	B3	3271	A2	3417	B4		
2015	A1	2415	C4	3272	A3	3418	A4		
2016	A1	2416	C4	3276	A4	3419	B4		
2017	A1	2417	B4	3300	B2	3420	B4		
2018	A2	2418	B4	3302	B2	3430	B4		
2019	A1	2419	B4	3303	B2	3431	B4		
2020	A1	2420	B4	3304	B2	3439	B3		
2100	A2	2421	B4	3305	C2	3440	B3		
2101	B3	2429	B4	3307	C1	3448	C4		
2102	A3	2434	B3	3308	B2	3449	B4		
2104	A3	2435	B4	3309	B2	3450	B4		
2105	A3	2436	B4	3310	C2	3451	A4		
2106	A3	2437	B4	3311	C2	3461	B3		
2110	A2	2438	B4	3312	C2	3470	B3		
2111	B3	2439	B4	3313	C2	4000	B1		
2121	B2	2440	B4	3314	B2	4001	A1		
2122	B2	2441	B4	3315	B2	4201	A4		
2126	B2	3005	A1	3316	B2	4202	A3		
2127	B2	3006	A1	3317	B1	4206	A4		
2200	A3	3100	A3	3318	B2	4209	A3		
2203	A4	3101	B3	3319	B1	4300	B1		
2209	A2	3102	B3	3320	A3	4301	B2		
2210	A2	3103	A2	3321	B3	4305	C2		
2211	A3	3105	B3	3323	A3	4306	C1		
2212	A3	3107	B2	3324	B2	5100	A2		
2214	A3	3108	B2	3326	B2	5102	A3		
2220	A3	3114	B3	3327	B1	5104	B3		
2300	C2	3115	B3	3329	C2	5201	A3		
2301	B1	3129	A2	3330	A3	5300	C2		
2302	C2	3131	B2	3331	B2	5301	C2		
2303	C2	3132	B2	3332	B2	5302	B1		
2304	C2	3200	A4	3335	C1	5400	B3		
2305	C2	3201	A4	3336	B1	5402	B3		
2309	B3	3203	A4	3337	B1	6301	C1		
2310	C1	3204	A4	3338	A3	7000	A2		
2311	B3	3205	A4	3339	B1	7001	A2		
2312	B3	3210	A3	3340	B2	7101	B3		
2313	B2	3214	A4	3341	A3	7200	A4		
2314	C1	3220	A4	3342	B1	7203	A3		
2316	C2	3221	A3	3343	A3	7204	A4		
2317	B3	3222	A3	3344	B2	7205	A3		
2318	C1	3224	A3	3345	A3	7301	C2		
2319	B2	3225	A3	3346	A2	7302	B2		
2320	B1	3226	A4	3347	A2	7304	B1		
2321	B1	3227	A3	3348	A2	7306	A1		
2322	B1	3231	A4	3351	B2	7308	C1		
2323	B1	3235	A4	3352	B2	7309	C1		
2324	B1	3236	A4	3353	B1	7310	C1		
2325	B3	3237	A3	3354	C1	7311	C1		

## ***Personal Notes:***

## ***Personal Notes:***

## 8. Alignments

No alignments necessary

## 9. Circuit Descriptions, Abbreviation List, and Data Sheets

Index of this chapter:

1. Introduction
2. Block diagram
3. DVD-Mechanism
4. Optical Pickup Unit
5. Printed Wiring Board
6. Abbreviation list
7. IC Data Sheets

### 9.1 Introduction

The Video Engine consist of a DVD-Mechanism with dual laser Optical Pickup Unit (OPU), a tray loader with fan unit and a PWB containing all electronics to control the module. The electronics of the module is responsible for all basic servo tasks. It reads from and writes data onto the disc.

### 9.2 Block Diagram

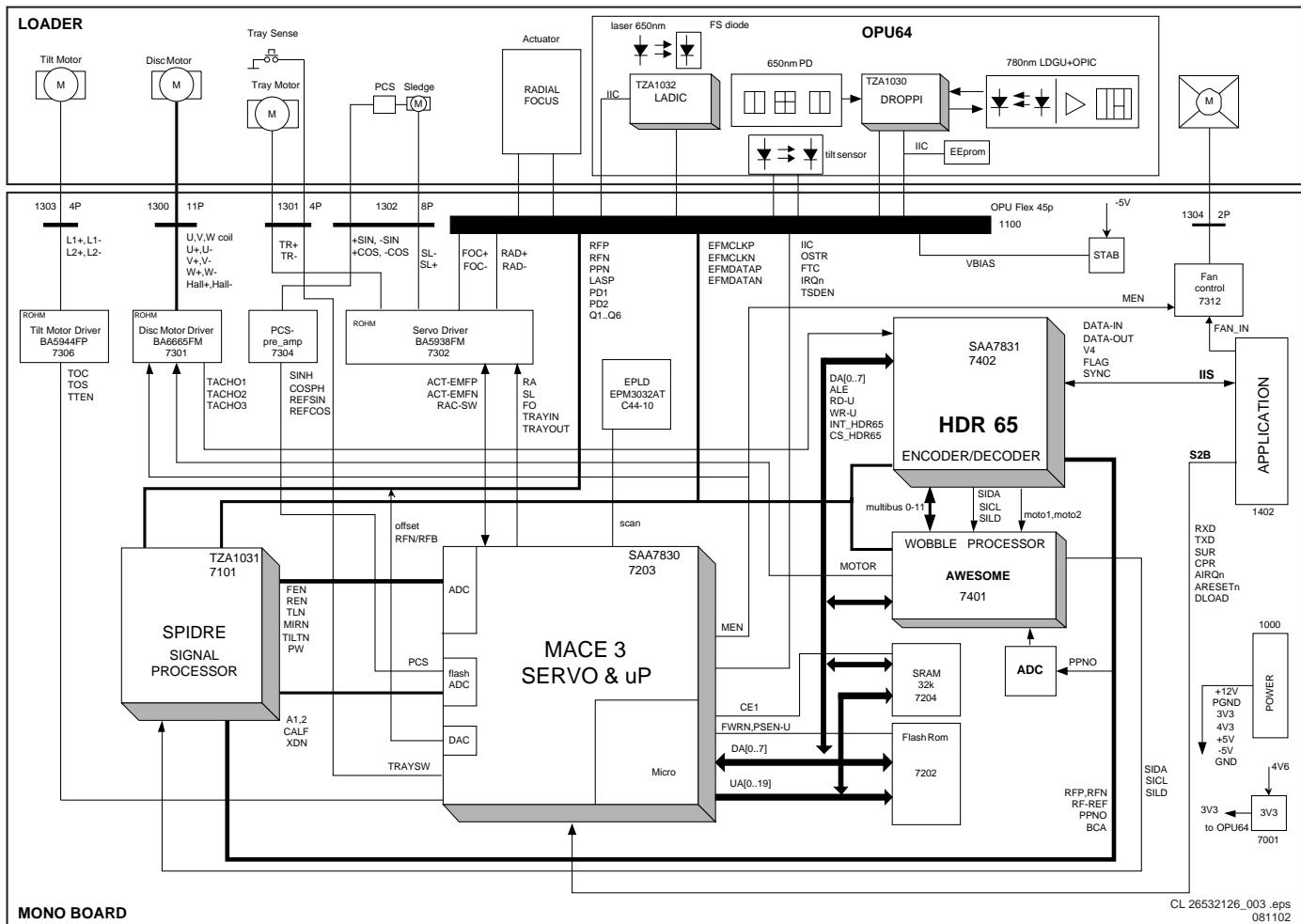


Figure 9-1 Block diagram Basic Engine

The PWB is a high tech module (multi layer, full SMC) with very high component density. Despite of this, it is designed in such a way, that repair on component level still is possible. Detailed diagnostics and fault finding is possible via ComPair.

Some specifications:

- Record DVD+RW
  - Loss less linking
  - Recording speed: 1.2 x
- Playback DVD
  - DVD+R(W), DVD (SL/DL), DVD-R, DVD-RW (V1.1)
  - Playback speed: 1.2 x
- Playback CD
  - CD-DA, CD-R, CD-RW, CD-ROM, VCD/SVCD
  - Playback speed: 3 x

This section describes briefly the functional behaviour of the engine. It performs all basic servo functions:

- It reads data from the disc,
- It writes data to the disc,
- It controls all other functions like tray control, start/stop the disc, tracking, jumping, and communication to the host.

#### 9.2.1 Initialisation process

After power-up or reset, a self-test will automatically start.

#### 9.2.2 Starting up the drive

After the internal initialisation process has been finished, the engine will wait for the first S2B user command. E.g. "Tray\_out".

#### 9.2.3 Disc recognition process

The process of disc recognition when a disc is loaded is entirely performed within the engine. Information about the disc type is send to the MPEG application (back-end).

#### 9.2.4 Write / Read process

Depending on the disc loaded and actions to be performed, Record, Play, Search, etc. a specific sequence of commands is executed depending on the implementation of the application.

#### 9.2.5 Shock behaviour during recording

Shock recovery during recording is performed by the application if the shock was too large to be absorbed by the engine. If the engine detects unacceptable servo behaviour, the laser will go into low power mode to protect overwriting of neighbour tracks. This action is signalised by the internal engine controller and reported to the application. The application can react on this with a shock recovery procedure.

#### 9.2.6 Function overview VAE801x and MPEG application

The VAE801x performs all basic servo functions. It reads data from the disc and writes data to the disc, and controls all other functions like tray control, start/stop the disc, tracking, jumping, and communication to the host.

### 9.3 DVD-Mechanism

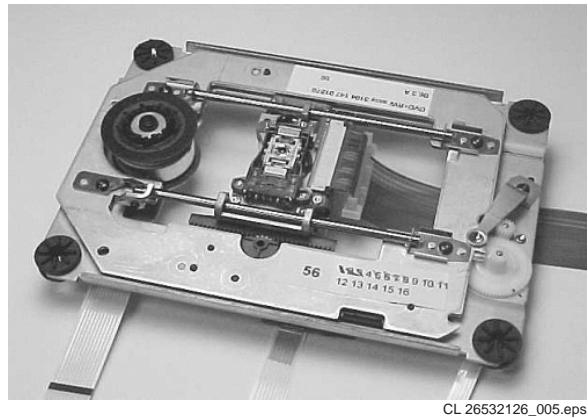


Figure 9-2 DVD-M assy

The DVD-M has an optical pickup unit (OPU) consisting of two lasers, one for CD with a wavelength of 780 nm, and one for DVD with a wavelength of 650 nm. The TZA1032 (LADIC) controls the data from these lasers, and the supply to them.

#### 9.4 Optical Pickup Unit

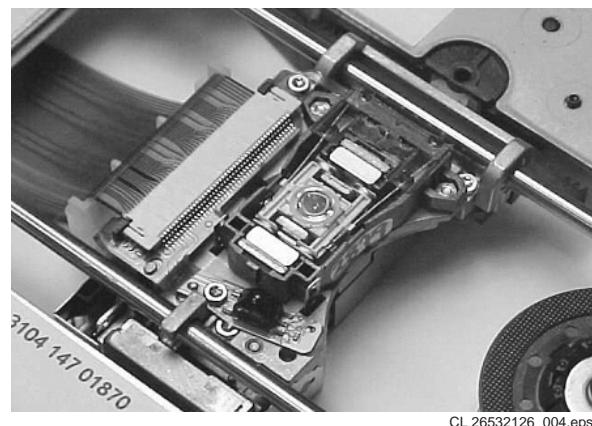


Figure 9-3 Optical Pickup Unit (OPU)

The OPU 64 (Optical Pickup Unit) consists of the following components:

- DVD+RW Optics.
- High-power red laser.
- Tilt sensor.
- Flexible connections.
- LADIC: Laser Driver IC.
- DROPPi: DVD Rewritable OPU Pre-Processor IC.
- EEPROM with OPU adjustment data.

OPU-64 Flex foil pinning specifications:

- 45 pins.
- Actuator signals: Rad +/-, Foc +/-
- Pre-processed signals.
- PPN: normalized, balanced PP output.
- RFP, RFN, RFB: differential RF output.
- Clocks: OPC-strobe, EFMClk-N/P, SCL.
- Data: EFMData-N/P, SDA.
- Power supplies:
  - +3V3E: 'clean' power supply for LADIC
  - +5V, -5V: for DROPPi
- TSDEn: tilt sensor power
- $V_{BIAS}$ : Laser bias voltage (~ -1.5 V)

#### 9.4.1 DROPPi

The DROPPi (DVD Rewritable OPU Pre-Processor IC) is a multi-purpose analogue pre-processor IC for use in the OPU of an optical bit engine. The device supports many photo detector configurations and output signal modes for RF and servo signals.

Some features of the DROPPi:

- Two inputs:
  - Current: DVD photo-diode.
  - Voltage: CD PDIC.
- Only one wideband signal across flex foil:
  - RF (differential signal).
- Other signals have relatively low bandwidth:
  - Wobble signal.
  - Servo signals.

#### 9.4.2 LADIC

The LADIC (Laser Driver IC, type number TZA1032) fulfills three main functions:

- It **drives** the laser with a sequence of programmable write strategy pulses with high timing accuracy and high peak current levels.
- It **encodes** the input modulated data to a sequence of write strategy pulses. This encoding is flexible with respect to input modulation code (EFM, EFM+, 17 pp, etc.). The write strategy is programmable with high flexibility for CD-R/RW and DVD-R/RW. For this purpose the TZA1032 includes two Random Access Memories (RAM) which can be loaded (non real-time) via the I<sub>2</sub>C-bus from microcontroller.
- It **controls** the exact light power levels coming from the laser and controls the exact power absorbed by the disc during recording.

The TZA1032 features three independent power supplies. These are the analogue and digital power supplies and a local power supply for the laser driver function. The supplies can be delivered separately to obtain maximum output performance of the TZA1032 in environments with large and highly dynamic current flows.

Some features of the LADIC:

- Single IC.
- Simple interface (EFM(+) Decoder, WSG, I<sub>2</sub>C Programming).
- Black-box laser.
- Temperature compensation.
- Dynamic power level control ("Alpha" control, OPC).
- Multi-application.
  - Highly programmable (control and write strategy),
  - Wide driver & frequency range.
- Under 500 mW dissipation in IC.

#### 9.5 Printed Wiring Board

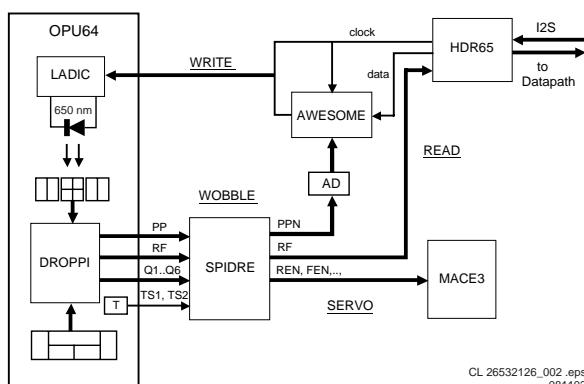


Figure 9-4 Signal path

##### 9.5.1 SPIDRE

The SPIDRE (Signal Processing IC for Dvd REwritable) is a multi purpose analogue pre-processor IC specifically intended for writing applications. Its main task is normalisation of the servo signals that go to the MACE3 servo processor (signals like 'focus servo', 'radial servo', 'track loss servo', and 'tilt sensor').

It is possible to optimise the dynamic range of this pre-amp/processor combination for the LF servo and RF data paths. The gain in both channels is separately programmable. This will guarantee an optimal playability for all kind of discs.

The SPIDRE is optimised to work with the optical pick-up unit pre-processor IC TZA1030 (DROPPi) and decoder IC HDR65.

Some features of the SPIDRE:

- A "Writer add-on".
- Double-Writer prepared (both CDR/RW and DVDRW).
- Direct connection to HDR65/MACE3.
- High Performance:
  - 16 x DVD-ROM read.
  - 64 x CD-ROM read.
  - 16 x CDR/RW write.
  - 4 x DVD+RW write.
- Normalisation of servo signals.
- Programmable RF gain and AGC functionality.
- Separate push-pull signal (with own AGC).
- Three wire serial interface for programming of the device by a decoder IC or microcontroller.

#### 9.5.2 MACE3 Servo and microprocessor

The MACE3 IC (Mini All Cd Engine, type number is SAA7830) is a combined servo processor and micro controller (80C51). See also the internal block diagram at the end of this chapter.

##### The servo front-end

The servo processor handles the signals for focusing, tracking, and access, but also generates the control signals for the loader block.

In a CD/DVD system, there are several control loops active. Some of them are needed to adjust the servo error signals (once per disc rotation). It also adjusts offsets, signal amplitudes, and loop gains (AGCs), to enlarge system robustness and to avoid expensive potentiometer adjustments in production.

The other loops determine the laser spot position on the disc in the radial, axial (focus), and tangential directions. It also has to take care that the spot accesses a required position as fast as possible. This access system consists of two parts, namely the actuator and the sled, which are (within a certain range) mechanically and electrically independent. Therefore, during an access, the servo has to control as well the actuator as the sled.

The analogue signals from the diode pre-processor are converted into a digital representation using A/D converters. The digital codes are then applied to logic circuitry to obtain the various control signals.

##### PCS (Position Control Sledge)

The PCS module is used to get fast sledge access. To achieve this, it is important that the sledge motor can rotate as fast as possible. But of course there is a limit to this maximum rotational speed. In order to let the sledge move even faster the ratio of the gearing can be reduced. However, if this ratio gets too low, the cogging/detent torque of the sledge motor will push the laser 'off track'.

There is a solution to this: Hold the sledge motor in its position by controlling the voltage on the motor continuously.

PCS is implemented in the following way:

A normal DC motor is used, combined with hall sensors. A DC sled motor has a magnetic ring mounted around the shaft. Above this magnetic ring are two hall sensors positioned 90 degrees apart. The two hall outputs, which go to the PCS control block, are 'sin phi' and 'cos phi'. The system tries to minimise the phase difference between the measured hall sine/cosine inputs and a reference sin/cosine waveform.

When the sledge is in tracking, the sin/cosine wave is constant, so the sledge is kept at its current position. When after a while, the radial actuator drifts away, the capture point crawls a bit further on the hall sine. So the PCS makes a micro-step. When doing an access the servo processor keeps on stepping the sine/cosine table, thus generating a sine/cosine wave, which the sledge will follow.

##### OPC (Optimum Power Calibration)

This device has an integrated Optimum Power Calculation block for use in CD-R, CD-RW, and DVD-RAM applications. It

reads three analogue signals (A1, A2 and CALF), representing Max, Min, and Average values of the EFM signal respectively. It also takes the Power (PW) signal from the laser controller and then feeds an analogue signal (ALPHA0) out to control the laser power. The conversion frequency is 88KHz per channel. Basically, the OPC procedure tries to find out the optimum laser power to be used on a specific disc. It consists of three phases:

- WRITE** - Random EFM data is written to the test area of the disc at increasing levels of laser power (controlled by ALPHA0).
- READ** - The data on A1, A2, and CALF is read back from the test area and stored in memory.
- CALCULATION** - the embedded 8051 then calculates the setting of ALPHA0 where the least jitter is encountered. Some pre-processing is carried out by the OPC logic to reduce the processor's load.

This sequence is done twice - first a coarse calibration, followed by a fine-tuning.

#### The microprocessor

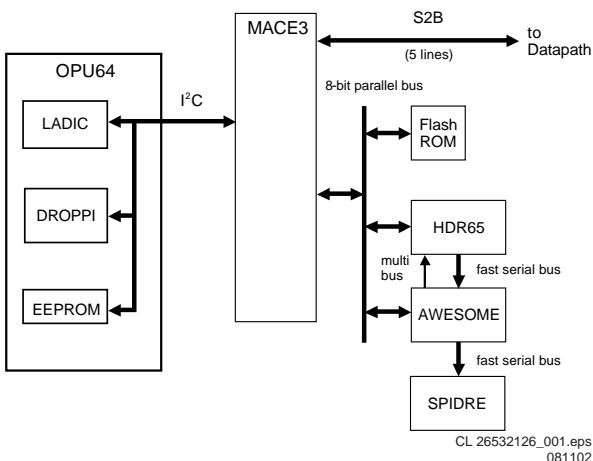


Figure 9-5 Control path

The micro controller processes the S2B commands from the DVD+RW interface (back-end), and controls the various processes in the OPU via I<sup>2</sup>C. Communication with the HDR65, SPIIDRE, and memories is done via an eight bit parallel bus.

Some features:

- Dedicated hardwired DSP.
- 8051-based microprocessor.
- External Flash ROM and SRAM memory.

#### 9.5.3 HDR65

The HDR65 has the following functions:

- Encoder** for DVD+RW. This part creates the EFM+ (16 bit) signals from the I<sup>2</sup>S data stream.
- Decoder** for DVD and CD. This part processes the HF-signal from the SPIIDRE. It converts the EFM(+) signals to data, and performs error detection and error correction.
- Output to SPIIDRE pre-processor for RF-AGC.

This IC decodes EFM or EFM+HF signals directly from the laser pre-amplifier, including analogue front-end, PLL data recovery, demodulation, and error correction.

The analogue front-end input converts the HF input to the digital domain via an 8-bit ADC, preceded by an AGC circuit to obtain the optimum performance from the converter. An external resonator clocks this block. This subsystem recovers the data from the channel stream. It corrects asymmetry, performs noise filtering and equalisation, and finally recovers the bit clock and data from the channel using a digital PLL.

The demodulator part detects the frame synchronisation signals and decodes the EFM (14 bit) and EFM+ (16 bit) data and sub-code words into 8-bit symbols. Via the serial output interface, the I<sup>2</sup>S data (audio and video) go to the DVD+RW interface (back-end).

The spindle-motor interface provides both motor control signals from the demodulator and, in addition, contains a tachometer loop that accepts tachometer pulses from the motor unit. They drive the motor IC (item 7301).

The SAA7831 has two independent microcontroller interfaces. The first is a serial I<sup>2</sup>C-bus and the second is a standard 8-bit multiplexed parallel interface. Both of these interfaces provide access to 32 8-bit registers for control and status.

Some HDR65 features:

- Playback speeds up to 48 x CD and 8 x DVD; recording up to 8 x CD and 4 x DVD
- Matched filter with digital equalizer, noise filter, and digital PLL.
- EFM and EFM+ modulator and demodulator.
- Decoding, de-interleaving, and error correction according to CD and DVD standards.
- Wobble processing for DVD-R(W) and CD.
- Motor control for CAV and CLV regulation on both recorded and unrecorded discs.
- Automated encode start/stop mechanism, supporting bit-accurate linking (only DVD).
- Write data/clock interface compatible with LADIC.
- Versatile serial input/output interface for different formats.
- 8 bit parallel data input/output interface.

#### 9.5.4 AWESOME

The AWESOME gate array chip (uPD65882, item 7401) is a fully digital DVD+RW add-on for the HDR65. A combination of both ICs can do CD and DVD decoding and CD, DVD-R(W), and DVD+RW encoding. It contains logic for:

- Wobble processing:
  - Address detection,
  - Write clock generation,
  - Start and stop.
- ADIP decoding.
- Spindle motor control to do CLV on wobble.
- Link bits insertion (according to DVD+RW standard).
- Output to SPIIDRE pre-processor for wobble-AGC

It also contains multiplexing logic for the motor signals and a merge of the internal serial bus to the analogue pre-processor (SPIIDRE) with the serial bus of the HDR65.

**Note:** AWESOME stands for: Adip decoding, Wobble processing, Error correction, Synchronous start/stop and Occasionally Mend Errors.

#### Wobble

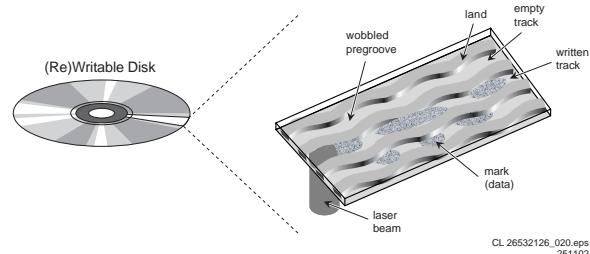


Figure 9-6 Pre-groove wobble on (re)writable discs

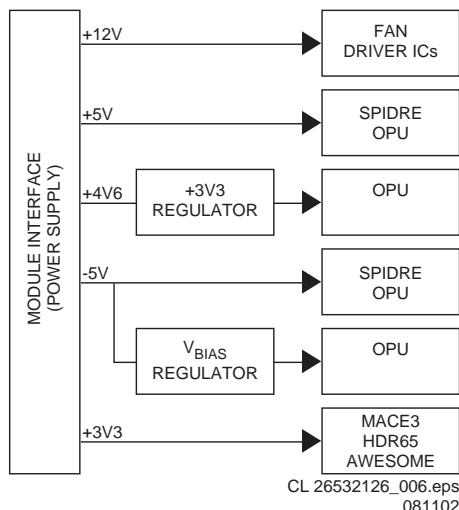
All **recordable** DVD media types feature a microscopic wobble groove embedded in the plastic substrate. This wobble provides the recorder with the timing information needed to place the data accurately on the disc. During recording, the drive's laser follows this groove, to ensure consistent spacing of data in a spiral track. The walls of the groove are modulated in a consistent sinusoidal pattern, so that a drive can read and compare it to an oscillator for precise rotation of the disc. This modulated pattern is called a *wobble groove*, because the walls of the groove appear to wobble from side to side. This signal is only used during recording, and therefore has no effect on the playback process. Among the DVD family of formats, only recordable media use wobble grooves.

For lossless linking it is necessary to write any data block in the correct position with high accuracy (within 1 micron). For this purpose the groove is mastered with a high wobble frequency (817 kHz at n=1), which ensures that the writing can be started and stopped at an accurately defined position. The writing clock as obtained from this groove is very accurate. At the same time, address information is stored in this wobbled groove by locally inverting the sign of the wobbled waveform.

Some characteristics:

- Only exists on (re)writable media.
- Pregroove wobble-detector present in recorders.
- Pregroove wobble-detector not present in players.
- Wobble-frequency is different for different standards:
  - DVD+R(W): 820 kHz
  - DVD-R(W), DVD-RAM: 141 kHz
- The wobble is phase modulated by inverting wobble cycles.
- The information contained in the wobble modulation is called Address-in-Pregroove or ADIP.

### 9.5.5 Power Supply



**Figure 9-7 Power supply overview**

The main power supplies to the module are 3.3 V, 5 V, and 12 V (input via connector 1000).

The MACE3, HDR65, and AWESOME ICs use the 3.3 V.

An on-board linear regulator is used to generate the -1.5 V required by the laser (V<sub>BIA</sub>S). The other on-board linear regulator is used to generate the 3V3E required by the OPU. The SPIDRE and OPU use the +5 V and -5 V. The motor, fan, and servo drivers use the 12 V.

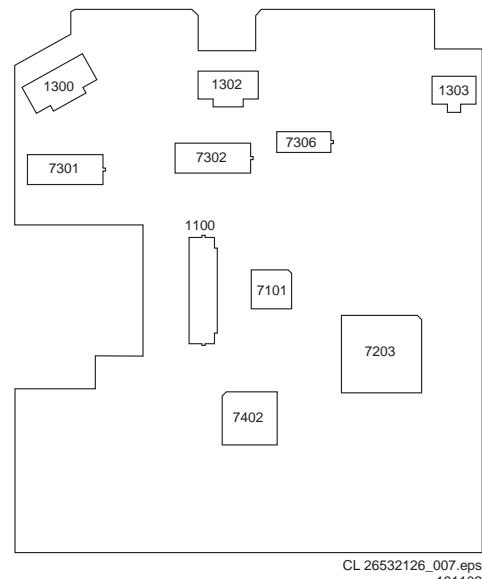
#### Power 'on' reset

At power 'on', a reset IC (AMD810, item 7201) generates a positive reset pulse of typical 240 ms. As a result, the micro

program will receive a reset, and the data of the Flash ROM is copied into the uP-RAM.

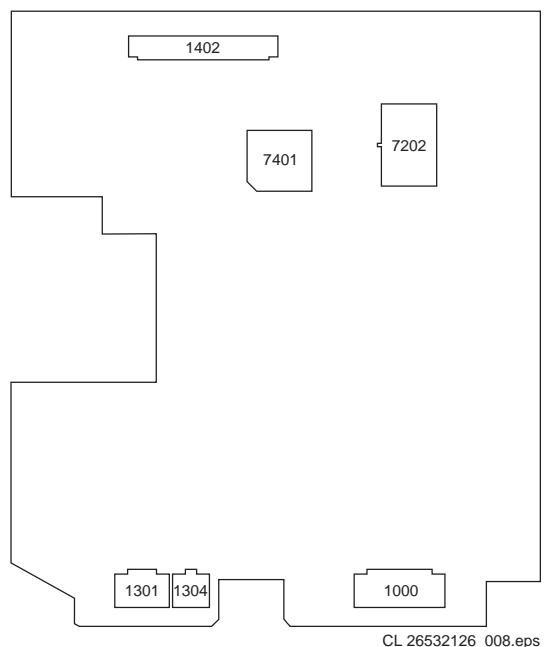
When the MACE IC is reset, also the HDR65 (SAA7831, item 7402) and AWESOME (uPD65882, item 7401) will get a reset via the 'SYS-RSTn', an output signal from the MACE.

### 9.5.6 PWB connections



**Figure 9-8 A-side connections**

- OPU-64: 45 pins flex foil (item 1100).
- Disk motor and hall elements: 11 pins flex foil (item 1300).
- Sledge motor and hall output: 8 pins flex foil (item 1302).
- Tilt motor: 4 pins flex foil (item 1303).



**Figure 9-9 Z-side connections**

- Tray motor: 4 pins wired (item 1301).
- Fan: 2 pins wired (item 1304).
- Digital PWB (back-end): 30 pins flex foil (item 1402).
- Power supply: 8 pins wired (item 1000).

## 9.6 Abbreviation list

ADC	Analogue to Digital Converter	uP	Microprocessor
ADIP	ADdress In Pre-groove	VCD	Video CD
AGC	Automatic Gain Control	Y/C	Luminance (Y) and Chrominance (C) signal
CD	Compact Disc	YUV	Component video
CLV	Constant Linear Velocity		
DROPPi	Dvd Rewritable Opu Pre-Processor IC		
AM	Amplitude Modulation		
BE	Basic Engine		
ComPair	Computer aided rePair		
CD-DA	CD Digital Audio		
CS	Chip Select		
DAC	Digital to Analogue Converter		
DAIO	Digital Audio Input Output		
DENC	Digital Encoder		
DFU	Direction For Use: description for the end user		
DNR	Dynamic Noise Reduction		
DRAM	Dynamic RAM		
DSD	Direct Stream Digital		
DSP	Digital Signal Processing		
DVD	Digital Versatile Disc		
EEPROM	Electrical Erasable Programmable ROM		
EFM	Eight to Fourteen bit Modulation		
FDS	Full Diagnostic Software		
HF	High Frequency		
I2C	Integrated Ic bus (signals at 5V level)		
I2S	Integrated Ic Sound bus (signals at 3.3V level)		
IC	Integrated Circuit		
IF	Intermediate Frequency		
IRQ	Interrupt ReQuest		
LADIC	LAser Driver IC		
LLD	Loss Less Decoder		
LPCM	Linear Pulse Code Modulation		
LRCLK	Left/Right CLocK		
MACE	Mini All Cd Engine		
MPEG	Motion Pictures Experts Group		
NC	Not Connected		
NVM	Non Volatile Memory: IC containing DVD related data e.g. alignments		
OPC	Optimum Power Calibration		
OPU	Optical Pickup Unit		
PCB	Printed Circuit Board (see PWB)		
PCS	Position Control Sledge		
PLL	Phase Locked Loop		
PCM	Pulse Code Modulation		
PCM_CLK	Audio system clock for DAC		
PCM_OUTx	Audio serial output data		
PSU	Power Supply Unit		
PWB	Printed Wiring Board (see PCB)		
RAM	Random Access Memory		
RGB	Red, Green and Blue colour space		
ROM	Read Only Memory		
RF	Radio Frequency		
S2B	Serial to Basic engine, communication bus between host- and servo processor		
SCL	Serial Clock I2C		
SCLK	Audio serial bit clock		
SDA	Serial Data I2C		
SDRAM	Synchronous DRAM		
SMC	Surface Mounted Components		
S/PDIF	Sony Philips Digital InterFace		
SPIDRE	Signal Processing Ic for Dvd REwritable		
SRAM	Static Random Access Memory		
STBY	STandBY		
SVCD	Super Video CD		
SW	SoftWare		
THD	Total Harmonic Distortion		
TTL	Transistor Transistor Logic (5V logic)		

## 9.7 IC Data Sheets

In this paragraph, the internal block diagrams and pinning are given of ICs that are drawn as a 'black box' in the electrical diagrams (with the exception of 'memory' and 'logic' ICs).

### 9.7.1 Diagram 2, SAA7830 (IC7203)

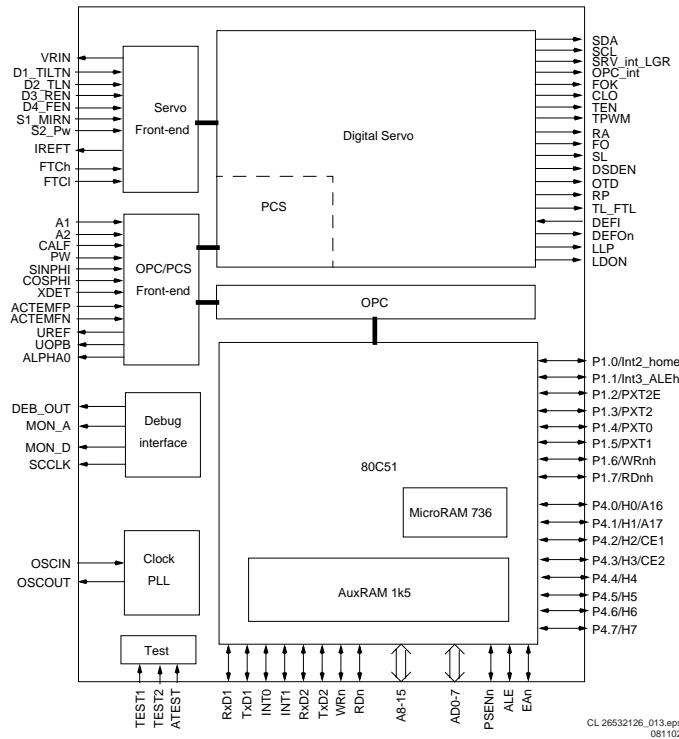


Figure 9-10 Internal Block Diagram MACE3

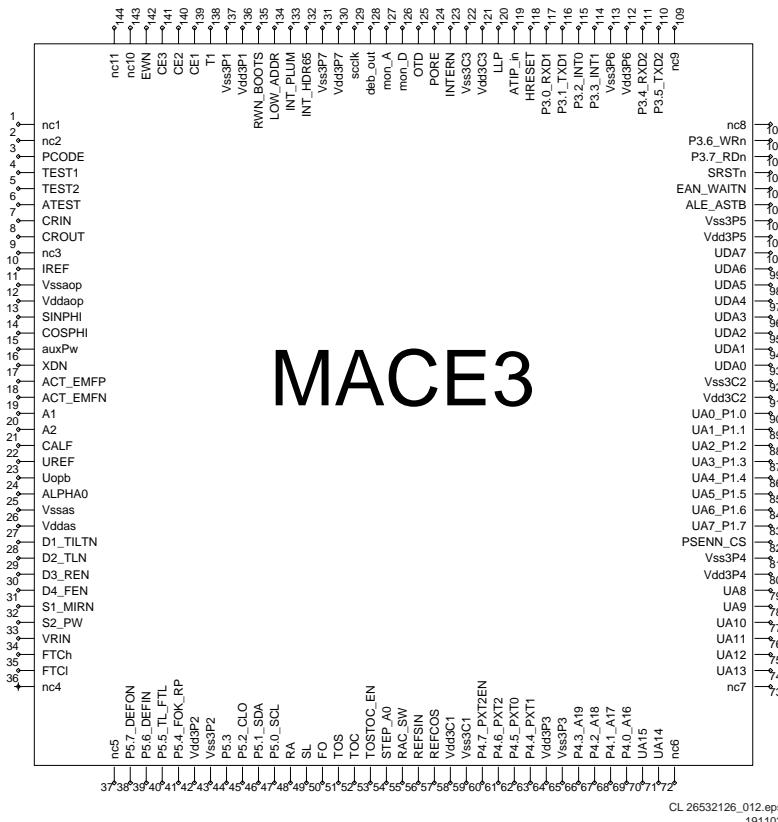


Figure 9-11 Pinning MACE3

## 10. Spare Parts List

<b>Miscellaneous</b>		2019	4822 124 23002	10µF 16V	2425	2238 586 59812	0603 50V 100NP80M	
<b>Various</b>		2020	2238 586 59812	0603 50V 100NP80M	2426	2238 586 59812	0603 50V 100NP80M	
0000	7104 099 28362	SUB CHASSIS 8 TEST CD	2100	2238 586 59812	0603 50V 100NP80M	2427	2238 586 59812	0603 50V 100NP80M
0000	7104 099 91691	MPTD PRINTED TEST DISC	2101	5322 126 11583	10nF 10% 50V 0603	2428	2238 586 59812	0603 50V 100NP80M
0000	7104 099 97941	CVP02.1	2102	2238 586 59812	0603 50V 100NP80M	2429	2238 586 59812	0603 50V 100NP80M
0000	7104 099 91041	MPTD PRINTED TEST DISC	2104	5322 126 11583	10nF 10% 50V 0603	2430	2238 586 59812	0603 50V 100NP80M
0000	7104 099 91041	CVP02.1	2105	5322 126 11583	10nF 10% 50V 0603	2431	2238 586 59812	0603 50V 100NP80M
0000	7104 099 91041	BURN-IN TEST DISC DL	2106	4822 122 33735	27nF 10% 63V	2432	2238 586 59812	0603 50V 100NP80M
0000	7104 099 97931	LVP04.15	2110	2238 586 59812	0603 50V 100NP80M	2434	4822 122 33741	10pF 10% 50V
0000	7104 099 97931	MPTD THIN TEST DISC	2111	2238 586 59812	0603 50V 100NP80M	2435	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	CVP02.60	2116	2238 586 59812	0603 50V 100NP80M	2436	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	DVDRO	2121	5322 126 11579	3.3nF 10% 63V	2437	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	NLT 01.00 LINK TEST DISC	2122	5322 126 11582	6.8nF 10% 63V	2438	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	DVDRO	2125	5322 126 11583	10nF 10% 50V 0603	2439	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	BLANK DVD+RW DISC	2127	5322 126 11583	10nF 10% 50V 0603	2440	4822 122 33761	22pF 5% 50V
0000	7104 099 98502	RICOH	2200	2238 586 59812	0603 50V 100NP80M	2441	4822 122 33761	22pF 5% 50V
0000	7104 099 91041	BLANK DVD+R DISC	2202	2238 586 59812	0603 50V 100NP80M	2446	5322 126 11583	10nF 10% 50V 0603
0000	7104 099 91041	RICOH	2203	4822 124 23002	10µF 16V	2447	2238 586 59812	0603 50V 100NP80M
<b>Mechanical Parts</b>		2207	3198 017 44740	0603 10V 470nF COL	<b>-WW-</b>			
<b>Various</b>		2208	2238 586 59812	0603 50V 100NP80M	3000	4822 051 30479	47Ω 5% 0.062W	
0001	3104 147 17471	LOADER ASSY	2209	2238 586 59812	0603 50V 100NP80M	3005	4822 051 30121	120Ω 5% 0.062W
0100	9305 022 82001	DVDM/01, LADIC	2210	2238 586 59812	0603 50V 100NP80M	3006	2322 704 64309	RST SM 0603 RC22H 43Ω PM1
0100	9305 022 82002	M3,OPU6464.3 EU	2211	5322 126 11583	10nF 10% 50V 0603	3100	4822 051 30103	10k 5% 0.062W
0100	9305 022 82003	DVDM/02, LADIC	2212	2238 586 59812	0603 50V 100NP80M	3101	4822 051 30103	10k 5% 0.062W
0001	3104 147 17471	M3,OPU6464.4 US	2214	2238 586 59812	0603 50V 100NP80M	3103	5322 117 13029	47k 1% 0.063W 0603 RC22H
0001	3104 147 17471	M4,OPU6410.1	2218	2238 586 59812	0603 50V 100NP80M	3105	4822 051 30681	680Ω 5% 0.062W
0011	3104 148 01780	FAN	2219	2238 586 59812	0603 50V 100NP80M	3107	4822 051 30101	100Ω 5% 0.062W
0104	3104 144 06540	SUSPENSION	2220	2238 586 59812	0603 50V 100NP80M	3108	4822 051 30101	100Ω 5% 0.062W
0105	3104 144 06540	SUSPENSION	2221	2238 586 59812	0603 50V 100NP80M	3114	4822 051 30101	100Ω 5% 0.062W
0106	3104 144 06540	SUSPENSION	2222	2238 586 59812	0603 50V 100NP80M	3115	4822 051 30101	100Ω 5% 0.062W
0107	3104 144 06540	SUSPENSION	2223	2238 586 59812	0603 50V 100NP80M	3121	4822 051 30681	680Ω 5% 0.062W
0108	3104 140 40650	DVDM MOUNTING SCREW	2224	2238 586 59812	0603 50V 100NP80M	3122	4822 051 30152	1k5 5% 0.062W
0109	3104 140 40650	DVDM MOUNTING SCREW	2225	2238 586 59812	0603 50V 100NP80M	3129	4822 051 30154	150k 5% 0.062W
0110	3104 140 40650	DVDM MOUNTING SCREW	2226	2238 586 59812	0603 50V 100NP80M	3130	4822 051 30101	100Ω 5% 0.062W
0111	3104 140 40650	DVDM MOUNTING SCREW	2227	2238 586 59812	0603 50V 100NP80M	3132	4822 117 12917	1Ω 5% 0.062W CASE0603
0000	3104 144 07200	TILT GEAR	2228	2238 586 59812	0603 50V 100NP80M	3200	4822 051 30103	10k 5% 0.062W
0000	3104 141 23380	TILT BLADE SPRING	2230	2238 586 59812	0603 16V 47nF COL	3201	4822 051 30103	10k 5% 0.062W
0000	3104 148 01520	TILT MOTOR ASSY	2303	3198 030 74780	EL SM 35V 4U7 PM20 COL R	3204	4822 117 12917	1Ω 5% 0.062W CASE0603
0000	3104 147 17481	SLEDGE MOTOR ASSY	2304	5322 126 11583	10nF 10% 50V 0603	3205	4822 051 30103	10k 5% 0.062W
0034	3104 148 01960	LOADING TRAY MOTOR	2305	4822 124 23002	10µF 16V	3210	4822 117 13573	NETW 4 X 47Ω 5% MNR14
0000	3104 144 07244	OPU RACK SLEDGE	2309	3198 016 31020	0603 25V 1nF	3214	4822 051 30103	10k 5% 0.062W
0000	3104 141 22833	OPU RACK SPRING	2310	3198 016 31020	0603 25V 1nF	3216	4822 051 30103	10k 5% 0.062W
0000	3104 157 12392	OPU FLEX	2311	4822 126 14494	22nF 10% 25V 0603	3217	4822 051 30103	10k 5% 0.062W
0002	3104 128 08262	PCB ASSY VAE8010, 8015	2312	2238 786 11554	0603 16V 2N2 PM5 R	3221	4822 051 30472	4k7 5% 0.062W
0002	3104 128 08322	PCB ASSY VAE8020/02/03	2313	2238 586 59812	0603 50V 100NP80M	3222	4822 051 30103	10k 5% 0.062W
<b>Electrical Parts</b>		2314	3198 016 31020	0603 25V 1nF	3224	4822 051 30105	1M 5% 0.062W	
<b>Various</b>		2315	2238 586 59812	0603 50V 100NP80M	3225	4822 051 30331	330Ω 5% 0.062W	
0000	2422 025 17276	S2B CONNECTOR	2316	2238 586 59812	0603 50V 100NP80M	3226	4822 117 13573	NETW 4 X 47Ω 5% MNR14
1100	2422 025 17751	CON BM H 45P F 0.5 FFC	2317	5322 126 11583	10nF 10% 50V 0603	3231	4822 117 13573	NETW 4 X 47Ω 5% MNR14
0000	3104 147 17481	0.3 R	2318	3198 016 31020	0603 25V 1nF	3235	4822 051 30103	10k 5% 0.062W
0034	3104 148 01960	LOADING TRAY MOTOR	2319	2238 586 59812	0603 50V 100NP80M	3236	4822 051 30103	10k 5% 0.062W
0000	3104 144 07244	OPU RACK SLEDGE	2320	3198 016 31020	0603 25V 1nF	3237	4822 051 30223	22k 5% 0.062W
0000	3104 141 22833	OPU RACK SPRING	2321	2238 586 59812	0603 25V 1nF	3238	4822 051 30222	2k2 5% 0.062W
0000	3104 157 12392	OPU FLEX	2322	2238 586 59812	0603 50V 100NP80M	3241	4822 051 30103	10k 5% 0.062W
0002	3104 128 08262	RESONATOR 8.467MHz	2323	2238 586 59812	0603 50V 100NP80M	3244	4822 051 30103	10k 5% 0.062W
1300	4822 267 51454	CONN. 11P FEMALE	2326	3198 017 31530	0603 50V 15nF COL R	3245	4822 051 30472	4k7 5% 0.062W
1302	2422 025 16158	CON BM H 8P F 1.00 FFC	2327	2238 586 59812	0603 50V 100NP80M	3246	4822 051 30479	47Ω 5% 0.062W
0000	3104 147 17481	0.3 R	2328	3198 017 31530	0603 50V 15nF COL R	3249	4822 051 30103	10k 5% 0.062W
1303	2422 025 17427	CON BM H 4P F 1.00 FFC	2329	2238 586 59812	0603 50V 100NP80M	3250	4822 051 30479	47Ω 5% 0.062W
0000	3104 147 17481	0.3 R	2331	2238 586 59812	0603 50V 100NP80M	3251	4822 051 30222	2k2 5% 0.062W
1401	2422 540 98428	RESONATOR 8.467MHz	2332	2238 586 59812	0603 50V 100NP80M	3252	4822 051 30222	2k2 5% 0.062W
1402	2422 025 17276	CON BM H 30P F 1.00 FFC	2333	5322 124 41945	22µF 20% 35V	3253	4822 051 30273	27k 5% 0.062W
0000	3104 147 17481	SMT R	2334	2238 586 59812	0603 50V 100NP80M	3254	4822 051 30273	27k 5% 0.062W
0002	5322 126 11583	10nF 10% 50V 0603	2335	2238 786 11554	0603 16V 2N2 PM5 R	3255	4822 051 30273	27k 5% 0.062W
2003	4822 124 23002	10µF 16V	2336	3198 017 34730	0603 16V 47nF COL	3256	4822 051 30103	10k 5% 0.062W
2004	5322 126 11583	10nF 10% 50V 0603	2337	3198 017 34730	0603 16V 47nF COL	3261	4822 051 30103	10k 5% 0.062W
2005	4822 124 23002	10µF 16V	2338	2238 786 11554	0603 16V 2N2 PM5 R	3262	4822 117 11817	1k2 1% 1/16W
2008	5322 126 11583	10nF 10% 50V 0603	2339	4822 124 23002	10µF 16V	3264	4822 117 13632	100k 1% 0603 0.62W
2009	4822 124 23002	10µF 16V	2400	4822 124 23002	10µF 16V	3265	4822 117 13632	100k 1% 0603 0.62W
2010	5322 126 11583	10nF 10% 50V 0603	2401	5322 126 11583	10nF 10% 50V 0603	3266	4822 051 30103	10k 5% 0.062W
2011	4822 124 23002	10µF 16V	2402	2238 586 59812	0603 50V 100NP80M	3267	4822 051 30103	10k 5% 0.062W
2012	5322 126 11583	10nF 10% 50V 0603	2403	2238 586 59812	0603 50V 100NP80M	3268	4822 051 30103	10k 5% 0.062W
2013	4822 124 23002	10µF 16V	2404	2238 586 59812	0603 50V 100NP80M	3269	4822 051 30102	1k 5% 0.062W
2014	4822 124 12084	1µF 20% SM 50V	2405	2238 586 59812	0603 50V 100NP80M	3270	4822 051 30102	1k 5% 0.062W
2015	2238 586 59812	0603 50V 100NP80M	2406	2238 586 59812	0603 50V 100NP80M	3271	4822 051 30102	1k 5% 0.062W
2016	4822 124 23002	10µF 16V	2407	2238 586 59812	0603 50V 100NP80M	3272	4822 051 30103	10k 5% 0.062W
2017	5322 126 11583	10nF 10% 50V 0603	2409	2238 586 59812	0603 50V 100NP80M	3274	4822 051 30103	10k 5% 0.062W
2018	22							

3302	4822 051 30272	2k7 5% 0.062W	3469	4822 051 30101	100Ω 5% 0.062W
3303	4822 051 30102	1k 5% 0.062W	3470	4822 051 30102	1k 5% 0.062W
3304	4822 051 30102	1k 5% 0.062W	3471	4822 117 12925	47k 1% 0.063W 0603
3305	4822 117 13608	4.7Ω 5% 0603 0.0016W			
3307	5322 117 13068	82Ω 1% 0.063W 0603 RC22H			
3308	4822 051 20108	1Ω 5% 0.1W	5100	2422 549 43303	ADJ.COIL (100μH +/-6%)
3309	4822 051 20108	1Ω 5% 0.1W	5102	2422 549 43303	ADJ.COIL (100μH +/-6%)
3310	4822 117 12917	1Ω 5% 0.062W CASE0603	5104	2422 549 43303	ADJ.COIL (100μH +/-6%)
3311	4822 117 12917	1Ω 5% 0.062W CASE0603	5201	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3312	4822 117 12917	1Ω 5% 0.062W CASE0603	5202	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3314	4822 051 20108	1Ω 5% 0.1W	5300	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3315	4822 051 20108	1Ω 5% 0.1W	5301	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3316	4822 051 20228	2Ω2 5% 0.1W	5302	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3317	4822 051 20228	2Ω2 5% 0.1W	5400	2422 549 43303	ADJ.COIL (100μH +/-6%)
3318	4822 051 20228	2Ω2 5% 0.1W	5401	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3319	4822 051 20228	2Ω2 5% 0.1W	5402	2422 549 43769	IND FXD SM EMI 100mH z 30R R
3320	4822 051 30332	3k3 5% 0.062W			
3321	4822 051 30563	56k 5% 0.062W			
3323	4822 051 30222	2k2 5% 0.062W			
3324	5322 117 13049	470Ω 1% 0.063W 0603 RC22H			
3325	5322 117 13049	470Ω 1% 0.063W 0603 RC22H			
3326	5322 117 13049	470Ω 1% 0.063W 0603 RC22H			
3327	4822 051 30103	10k 5% 0.062W	6301	4822 130 11397	BAS316
3328	5322 117 13049	470Ω 1% 0.063W 0603 RC22H			
3329	4822 117 13632	100k 1% 0603 0.62W			
3330	4822 051 30332	3k3 5% 0.062W			
3331	4822 051 30103	10k 5% 0.062W			
3332	2322 704 62003	RST SM 0603 RC22H 20k PM1 R			
3335	5322 117 13068	82Ω 1% 0.063W 0603 RC22H			
3336	4822 117 12864	82k 5% 0.6W	7000	9322 150 89668	IC SM LM337D2T (ONSE) R
3337	4822 051 30273	27k 5% 0.062W	7001	9322 121 67668	IC SM LF33CD (ST00) R
3338	4822 051 30103	10k 5% 0.062W	7101	9352 688 06157	IC SM TZA1031HL (PHSE) Y
3339	4822 117 12864	82k 5% 0.6W	7200	4822 130 60373	BC856B
3340	4822 051 30332	3k3 5% 0.062W	7201	9322 155 26685	IC SM ADM810SART (ANA0) Y
3341	4822 051 30102	1k 5% 0.062W	7202	9965 000 16590	FLASH FW15.07.xx VAE8010,8015
3342	4822 051 30273	27k 5% 0.062W	7202	9965 000 16595	FLASH FW20.09.xx VAE8020/01/02
3343	4822 051 30103	10k 5% 0.062W	7202	9965 000 16596	FLASH FW20.01.xx VAE8020/03
3344	4822 051 30332	3k3 5% 0.062W	7202	9965 000 16597	FLASH FW20.02.xx VAE8020/05
3345	4822 051 30102	1k 5% 0.062W	7203	9352 687 34557	IC SM SAA7830HL (PHSE) Y
3346	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	7204	9322 036 99685	IC SM CY7C1399B-15ZC (CYPR) R
3347	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	7205	4822 130 60373	BC857B
3348	4822 117 13632	100k 1% 0603 0.62W	7301	9322 139 85668	BA6665FM
3351	4822 051 30472	4k7 5% 0.062W	7302	4822 209 17229	BA5938FM
3352	4822 051 30153	15k 5% 0.062W	7304	4822 209 30095	LM833D
3353	4822 051 30123	12k 5% 0.062W	7306	9322 166 66668	IC SM BA5944FP (RHM0) R
3354	4822 117 11817	1k2 1% 1/16W	7308	9340 547 21215	FET POW SM BSH205 (PHSE) R
3355	4822 051 30332	3k3 5% 0.062W	7309	5322 130 60159	BC846B
3356	4822 051 30472	4k7 5% 0.062W	7310	5322 130 60159	BC846B
3357	4822 117 13632	100k 1% 0603 0.62W	7311	5322 130 60159	BC846B
3358	4822 051 30102	1k 5% 0.062W	7312	9322 144 97668	IC SM LD1117DT (ST00) R
3359	2322 704 62002	RST SM 0603 RC22H 2k PM1 R	7401	9322 180 17671	IC SM AWESOME2 (NECO) Y
3361	4822 051 30563	56k 5% 0.062W	7401	9322 189 16671	IC SM AWESOME3 (NECO) Y
3362	4822 051 30103	10k 5% 0.062W	7402	9352 687 36557	IC SM SAA7831HL (PHSE) Y
3364	4822 051 30272	2k7 5% 0.062W			
3365	4822 051 30272	2k7 5% 0.062W			
3367	4822 051 30471	470Ω 5% 0.062W			
3369	4822 051 30471	470Ω 5% 0.062W			
3370	4822 051 30103	10k 5% 0.062W			
3400	4822 117 11817	1k2 1% 1/16W			
3401	2350 035 10152	RST NETW SM ARV24 4X1k5 PM5 R			
3407	4822 117 13525	24k 1% 0.62W RC22H 0603			
3408	4822 051 30103	10k 5% 0.062W			
3409	4822 051 30103	10k 5% 0.062W			
3410	2350 035 10152	RST NETW SM ARV24 4X1k5 PM5 R			
3415	4822 051 30101	100Ω 5% 0.062W			
3416	4822 051 30105	1M 5% 0.062W			
3417	4822 051 30331	330Ω 5% 0.062W			
3418	4822 117 13578	4X10k 5% MNR14			
3419	4822 117 13573	NETW 4 X 47Ω 5% MNR14			
3420	2350 035 91001	RST NETW SM ARV24 4X jumper R			
3430	4822 117 13578	4X10k 5% MNR14			
3431	4822 117 13573	NETW 4 X 47Ω 5% MNR14			
3439	4822 051 30109	10Ω 5% 0.062W			
3440	4822 051 30109	10Ω 5% 0.062W			
3448	2350 035 91001	RST NETW SM ARV24 4X jumper R			
3449	4822 117 13573	NETW 4 X 47Ω 5% MNR14			
3450	4822 117 13573	NETW 4 X 47Ω 5% MNR14			
3451	4822 117 11817	1k2 1% 1/16W			
3463	4822 051 30101	100Ω 5% 0.062W			
3468	4822 051 30101	100Ω 5% 0.062W			