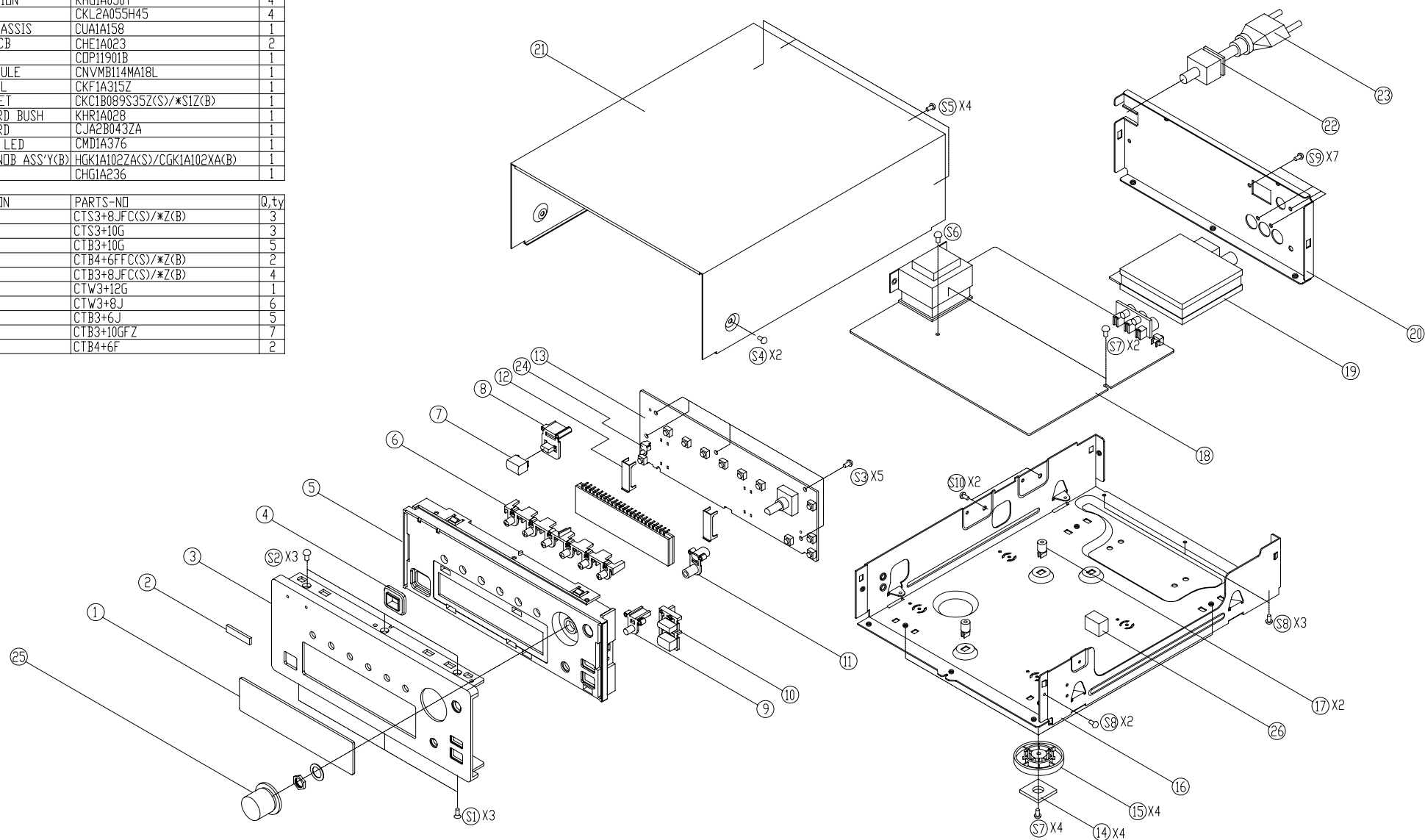


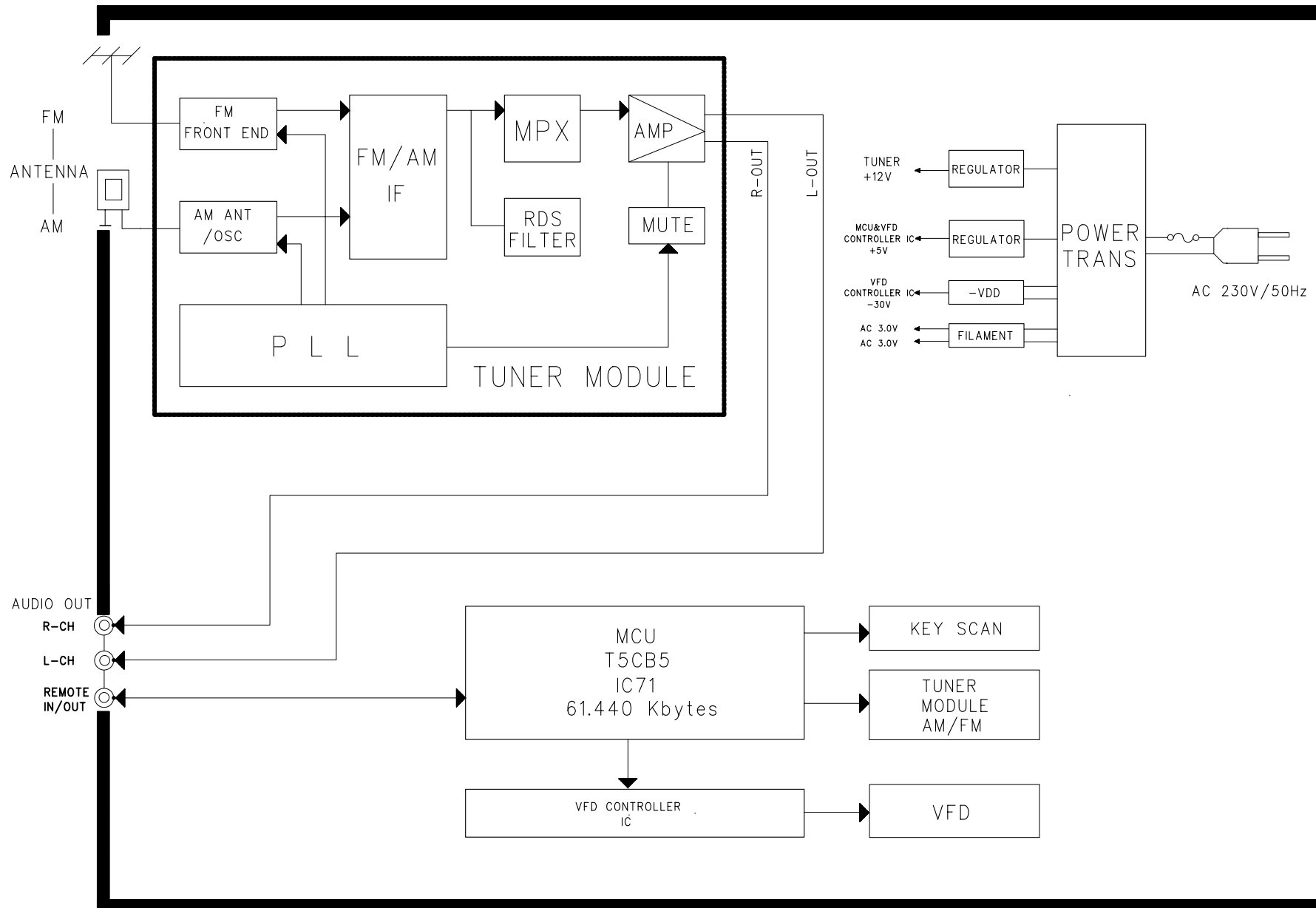
NO	DESCRIPTION	PARTS-NO	Q.ty
1	FIP WINDOW	CGUIA154Y	1
2	BADGE	KGBIA047Z	1
3	AL PANEL	CKMIA151XC35(S)/*XC61(B)	1
4	STANBY INDICATOR	CGLIA133	1
5	SUB PANEL	CGWIA387M7G5(S)/*K128	1
6	TACT KNOB	CBTIA944M7G5(S)/*K128	1
7	STANBY CAP	CBTIA495M7G5(S)/*K128	1
8	STANBY KNOB	CBTIA483	1
9	DIRECT KNOB	CBTIA484M7G5(S)/*K128	1
10	FM MODE KNOB	CBTIA531M7XG5(S)/*K128	1
11	RDS KNOB	CBTIA491M7G5(S)/*K128	1
12	FLT BRACKET	CMDIA468	2
13	FRONT PCB	CDP11901B	1
14	FOOT CUSHION	KHGA050Y	4
15	FOOT	CKL2A055H45	4
16	BOTTOM CHASSIS	CUAIA158	1
17	MOUNT , PCB	CHEIA023	2
18	MAIN PCB	CDP11901B	1
19	TUNER MODULE	CNVMB114MA18L	1
20	REAR PANEL	CKFIA315Z	1
21	TOP CABINET	CKCIB089S35Z(S)/*S1Z(B)	1
22	POWER CORD BUSH	KHRIA028	1
23	POWER CORD	CJA2B043ZA	1
24	SUPPORT , LED	CMDIA376	1
25	VOLUME_KNOB ASS'Y(B)	HGKIA102ZA(S)/CGKIA102XA(B)	1
26	CUSHION	CHGIA236	1

NO	DESCRIPTION	PARTS-NO	Q.ty
S1	SCREW	CTS3+8JFC(S)/*Z(B)	3
S2	SCREW	CTS3+10G	3
S3	SCREW	CTB3+10G	5
S4	SCREW	CTB4+6FFC(S)/*Z(B)	2
S5	SCREW	CTB3+8JFC(S)/*Z(B)	4
S6	SCREW	CTW3+12G	1
S7	SCREW	CTW3+8J	6
S8	SCREW	CTB3+6J	5
S9	SCREW	CTB3+10GFZ	7
S10	SCREW	CTB4+6F	2

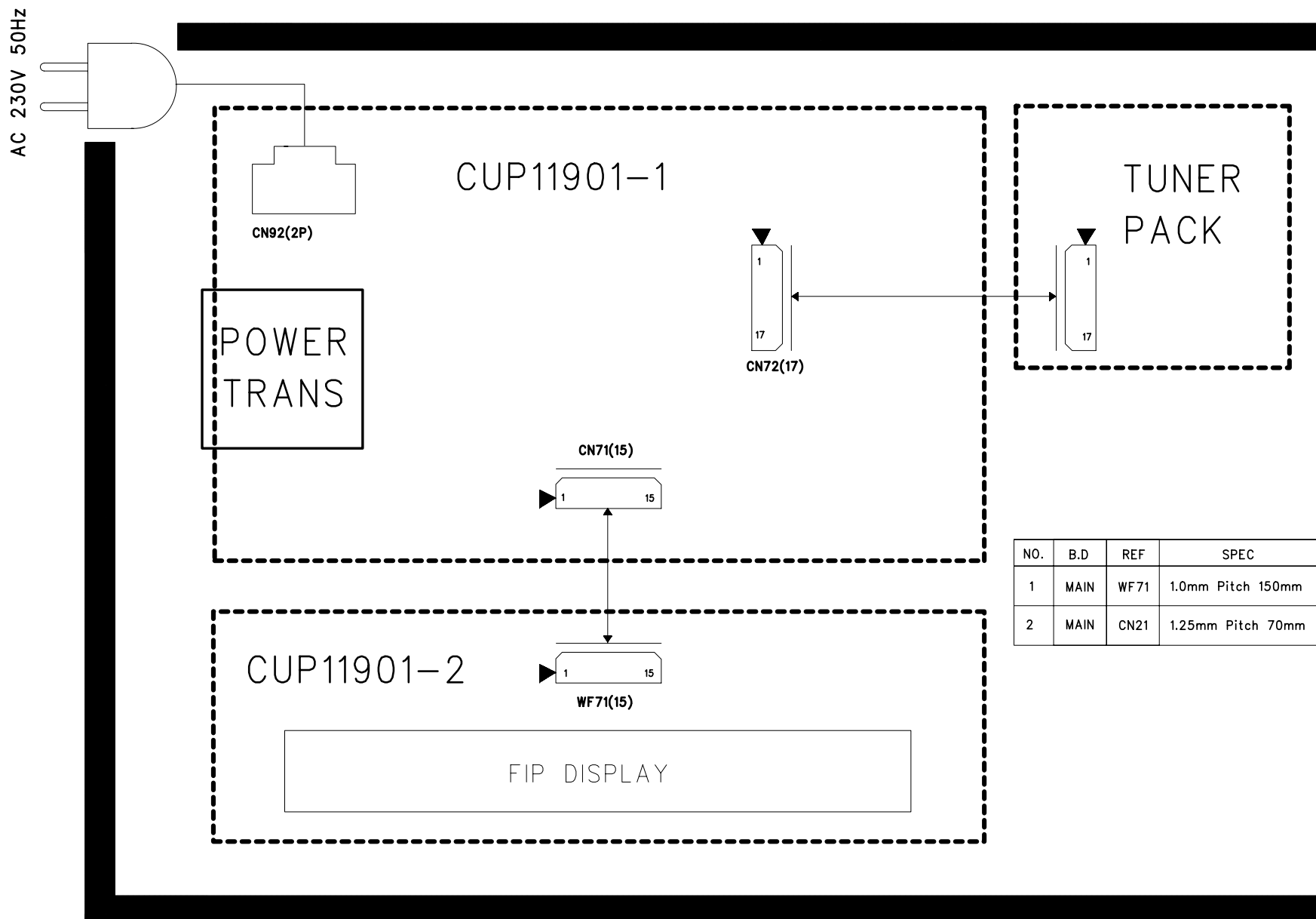
T-H300mk3-S,B EXPLODED VIEW



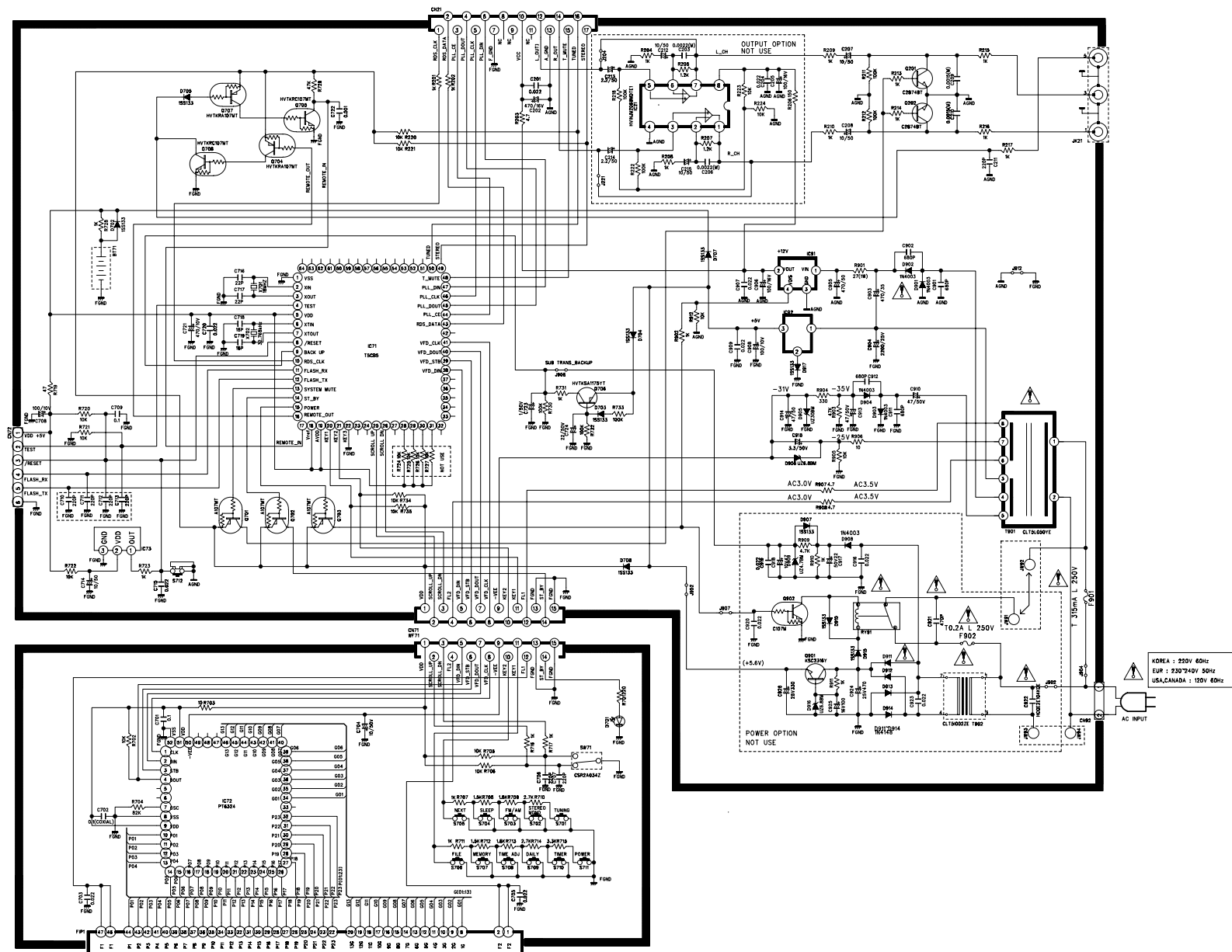
■ BLOCK DIAGRAM



WIRING DIAGRAM



SCHEMATIC DIAGRAM



1.2 Pin Assignment

P-QFP64-1414-0.80A

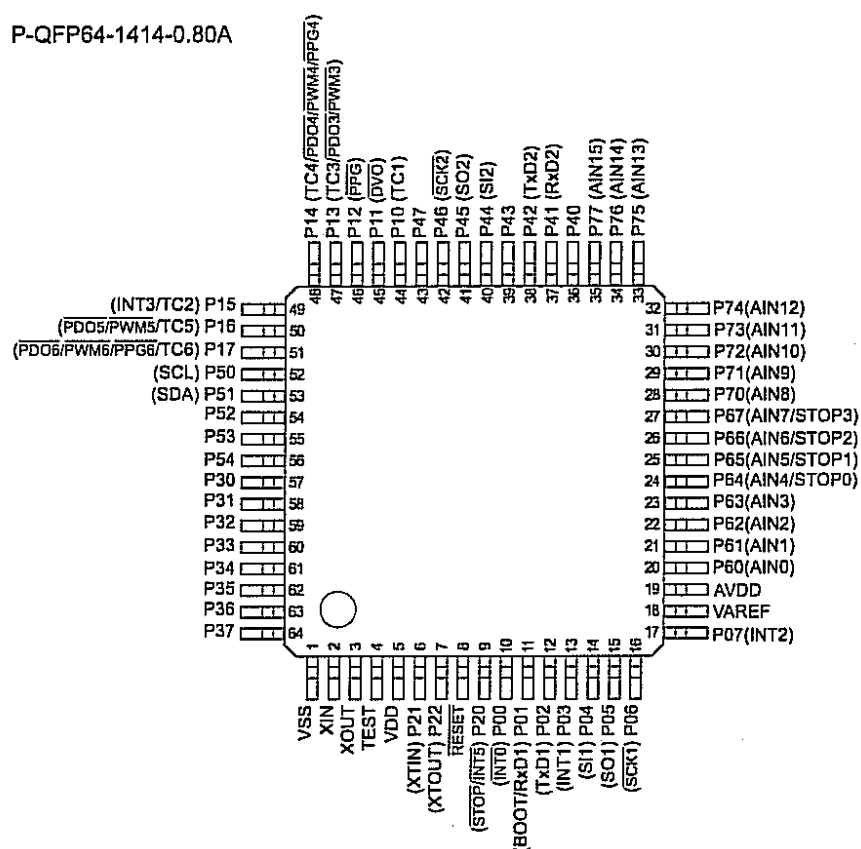
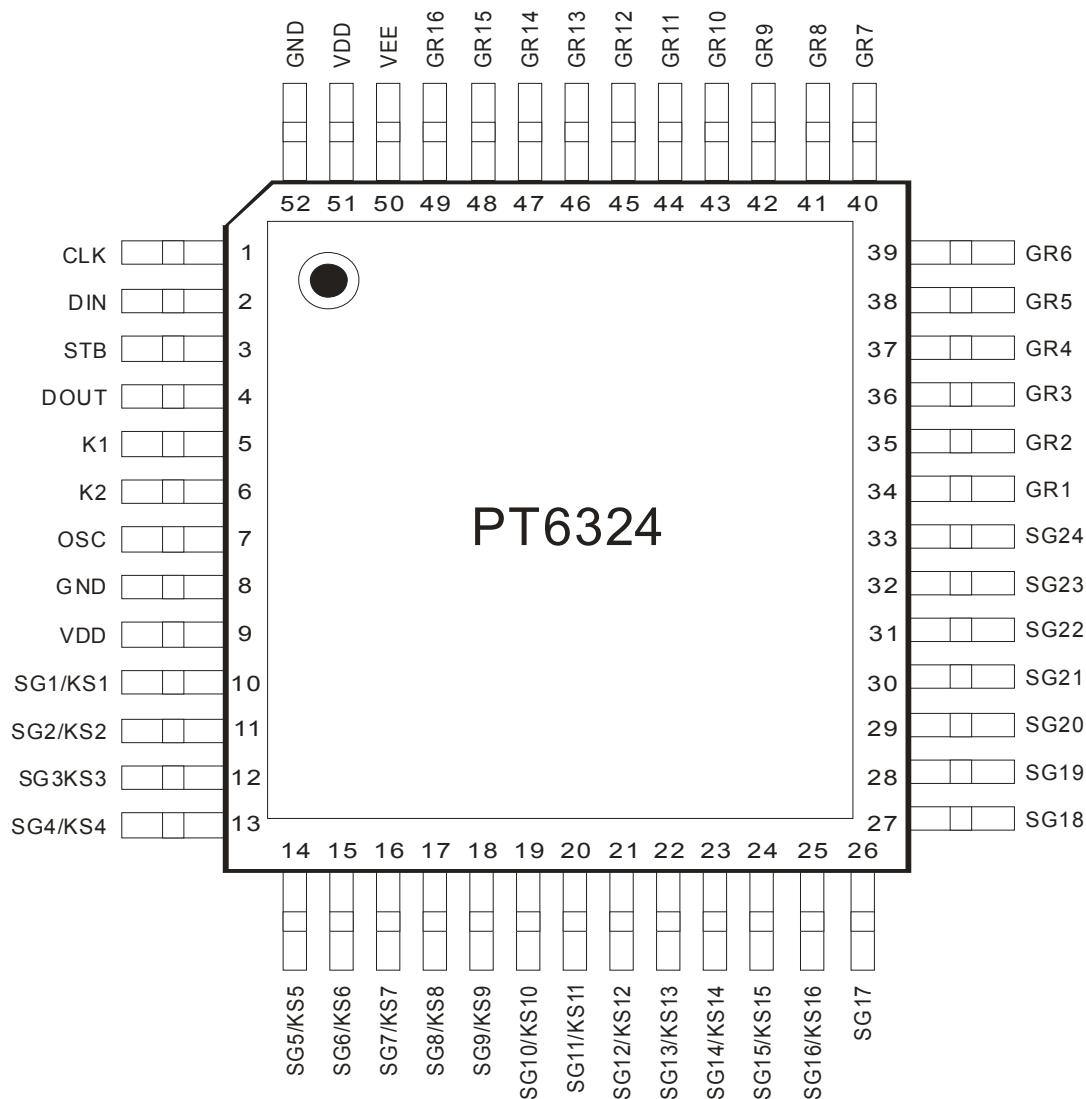


Figure 1-1 Pin Assignment



PIN CONFIGURATION



IC FUNCTION (MI-COM CVIT5CB5PQ)

Pin No.	PIN NAME	I/O	DESCRIPTION
1	VSS	I	GROUND
2	XIN	I	Crystal Input(16MHz)
3	XOUT	O	Crystal Output(16MHz)
4	TEST	I	Program Write check Input
5	VDD	I	+5V
6	XTIN	I	Timer Crystal Input(32.768kHz)
7	XTOUT	O	Timer Crystal Output(32.768kHz)
8	RESET	I	Device reset pluse input
9	BACK UP	I	Back-up Input
10	RDS_CLK	I	RDS_Clock Input
11	FLASH_RX	I	Program Write Receive Input
12	FLASH_TX	O	Program Write Transmit Output
13	SYSTEM MUTE	O	System mute Output
14	STAND BY	O	St-by LED On/Off Output
15	POWER	O	Power On/Off Output
16	REMOTE_OUT	O	Remote control Output
17	REMOTE_IN	I	Remote control Input
18	VAref	I	+5V
19	Avdd	I	+5V
20	KEY1	I	Key matrix signal Input
21	KEY2	I	Key matrix signal Input
22	KEY3	I	GROUND
23~24	N.C	–	Non connection
25	SCROLL UP	I	Scroll up Input
26	SCROLL DOWN	I	Scroll Down Input
27~37	N.C	–	Non connection
38	VFD_DIN	I	Not use
39	VFD_STB	O	VFD Control Strobe Output
40	VFD_DOUT	O	VFD Control Data Output
41	VFD_CLK	O	VFD Control Clock Output
42	N.C	–	Non connection
43	RDS_DATA	I	RDS Data Output
44	PLL_CE	O	PLL Chip Enable Output
45	PLL_DOUT	O	PLL Data Output
46	PLL_CLK	O	PLL Clock Output
47	PLL_DIN	I	PLL Data Input
48	T_MUTE	O	Tuner Mute Output
49	STEREO	I	Tuner Stereo Input
50	TUNED	I	Tuner Tuned Input
51~64	N.C	–	Non connection

PRINTED CIRCUIT BOARD

